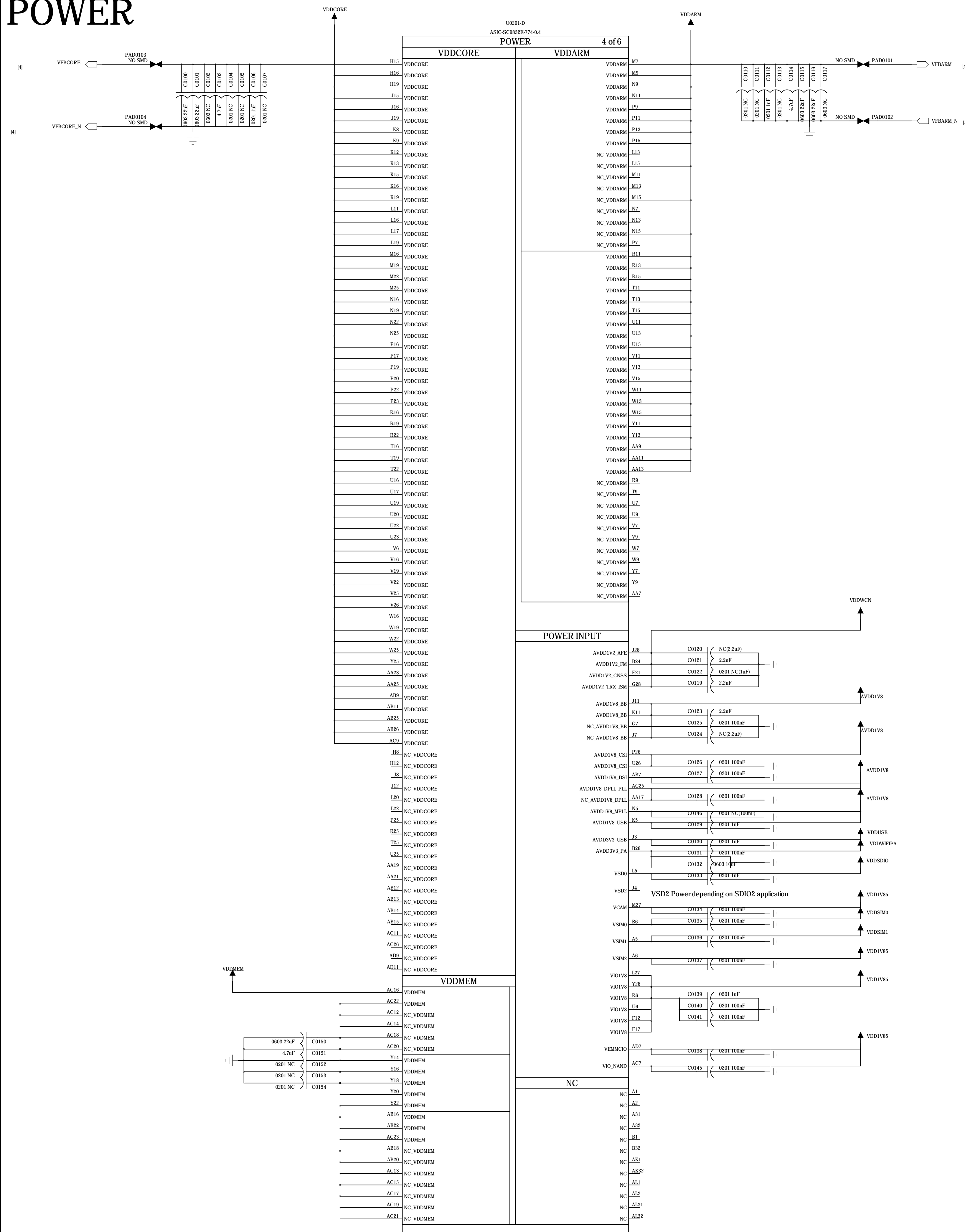
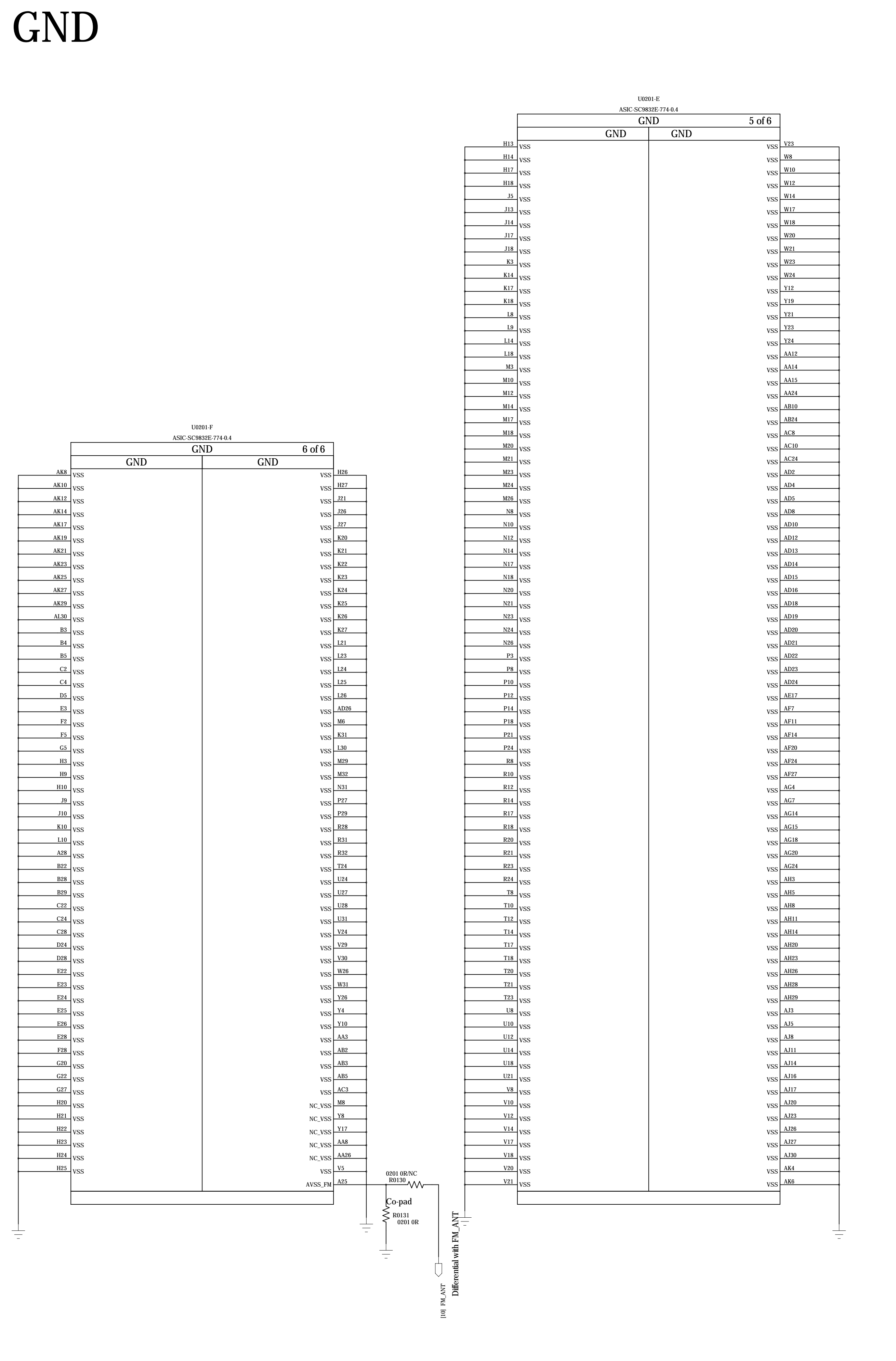


# POWER

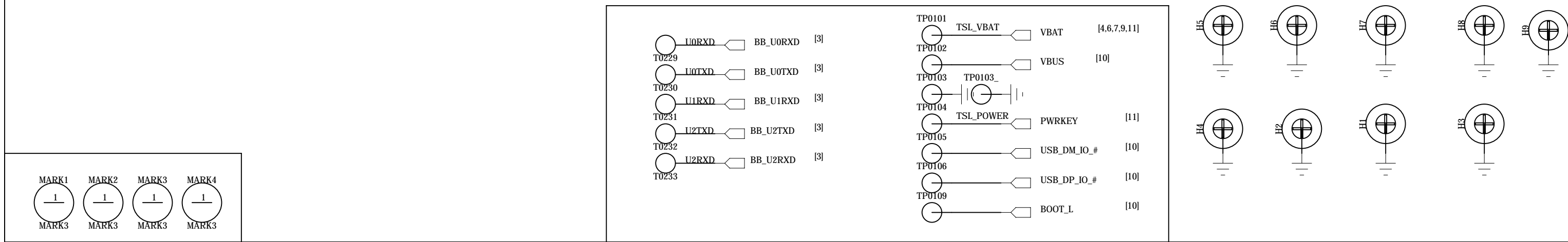
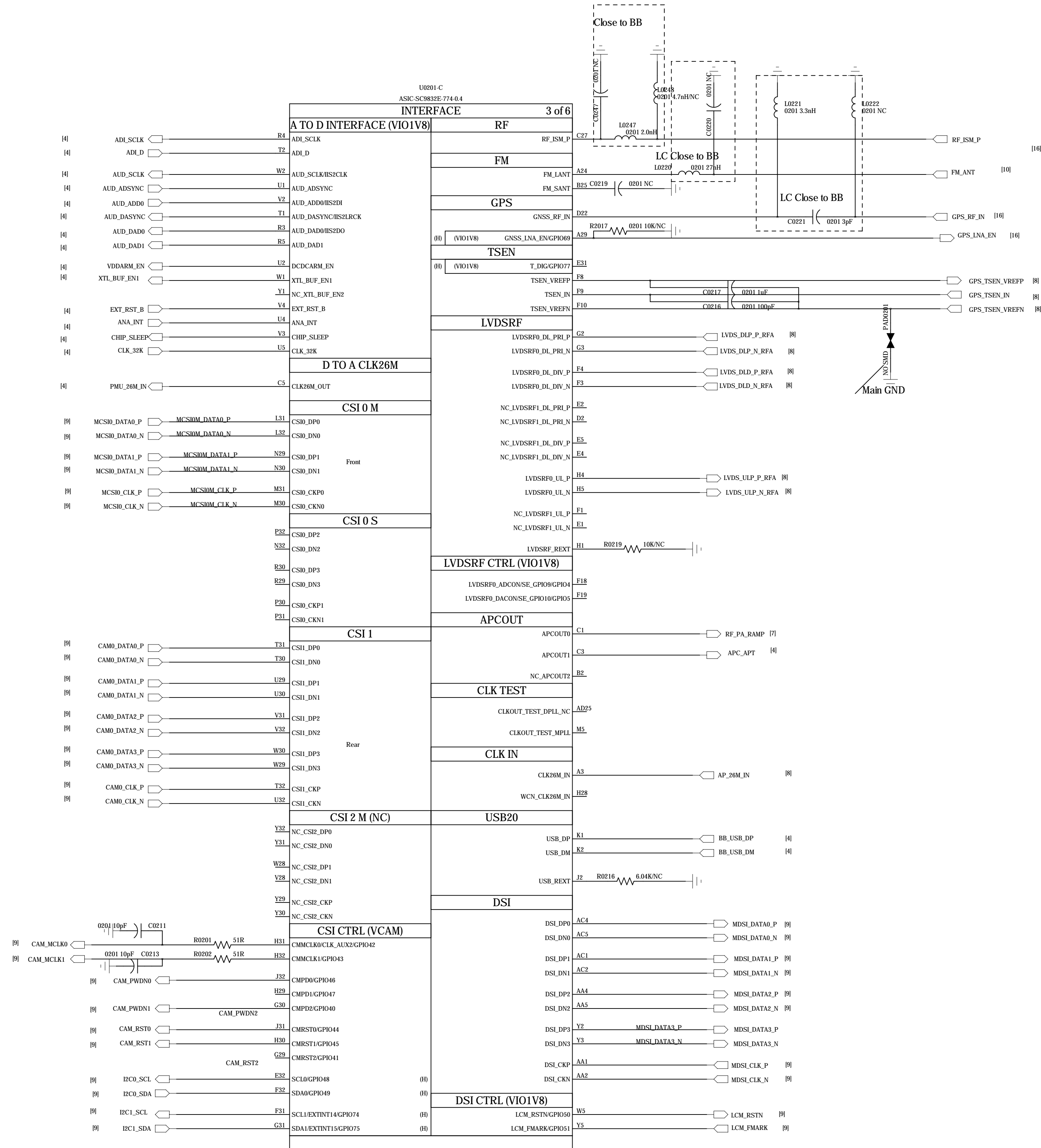
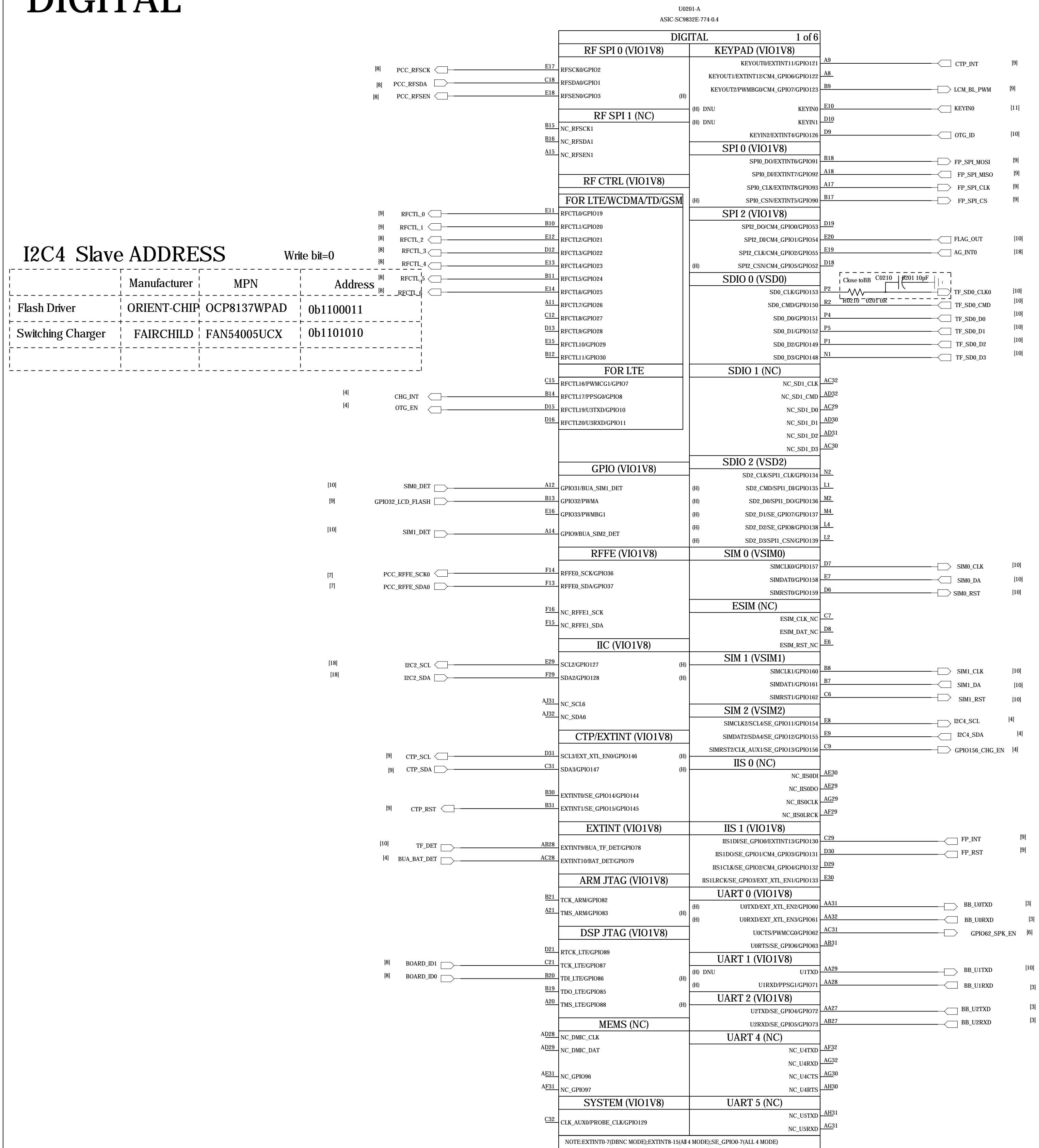


# GND



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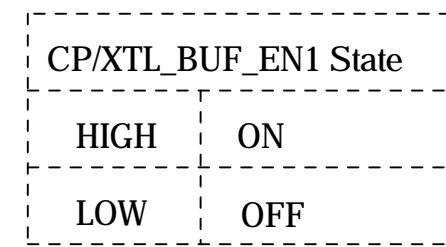
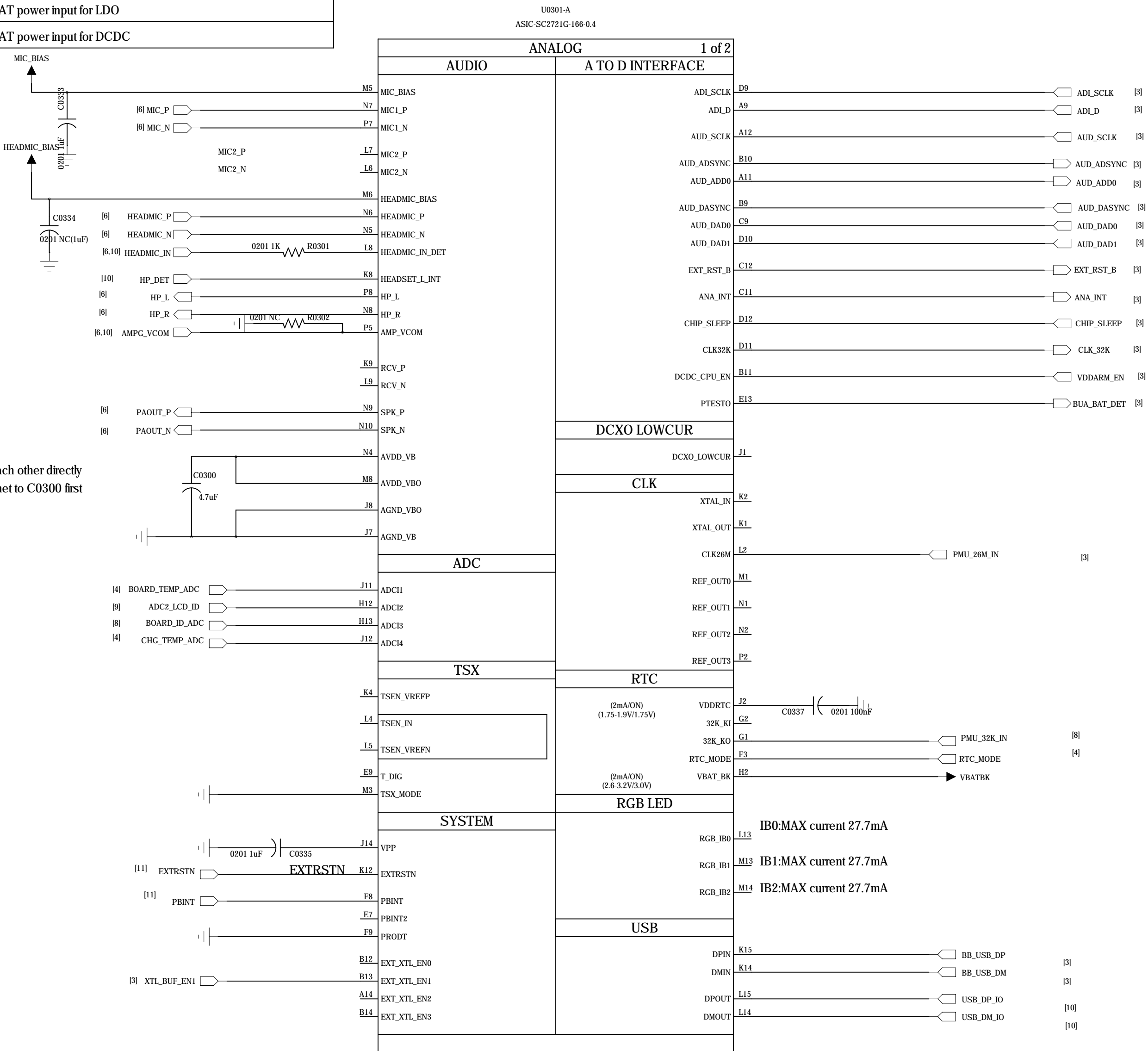
# DIGITAL



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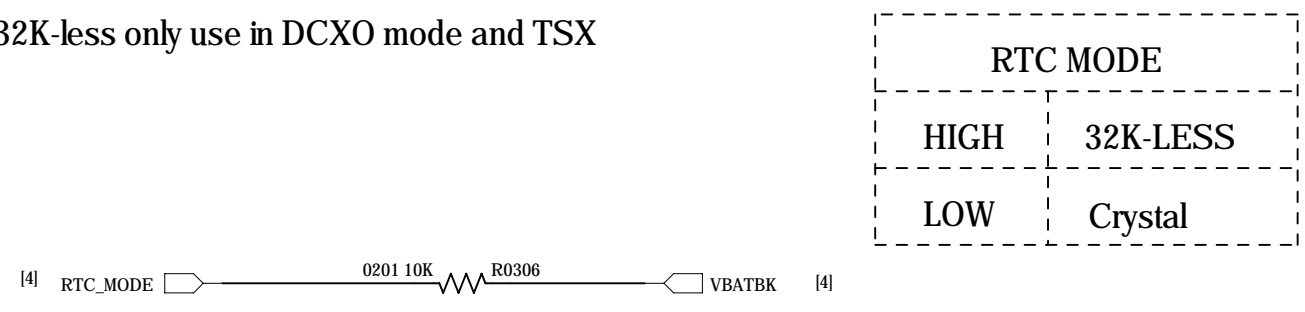


N4 and M8 can not connect each other directly  
N4 and M8 must connet to C0300 first

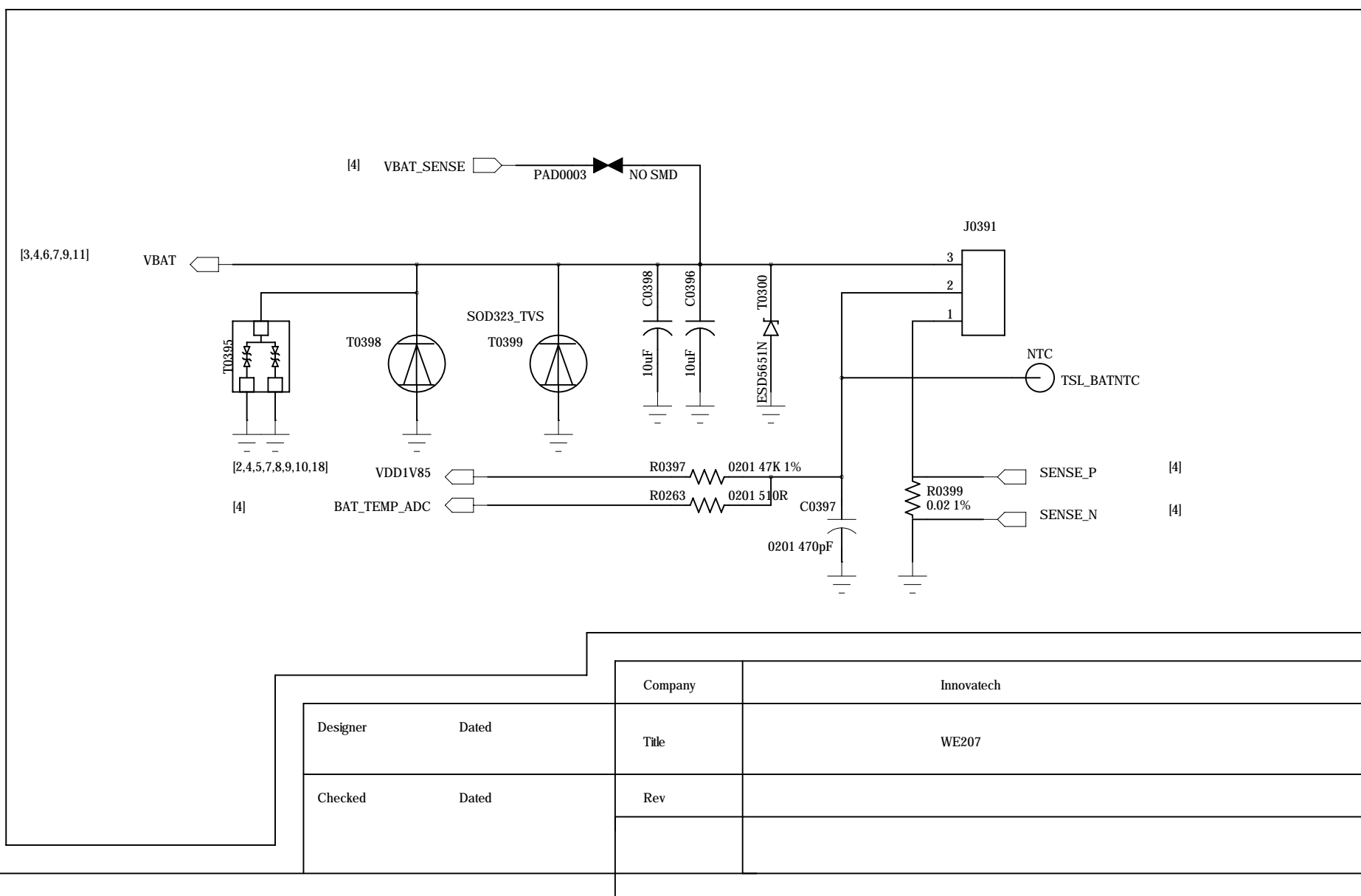
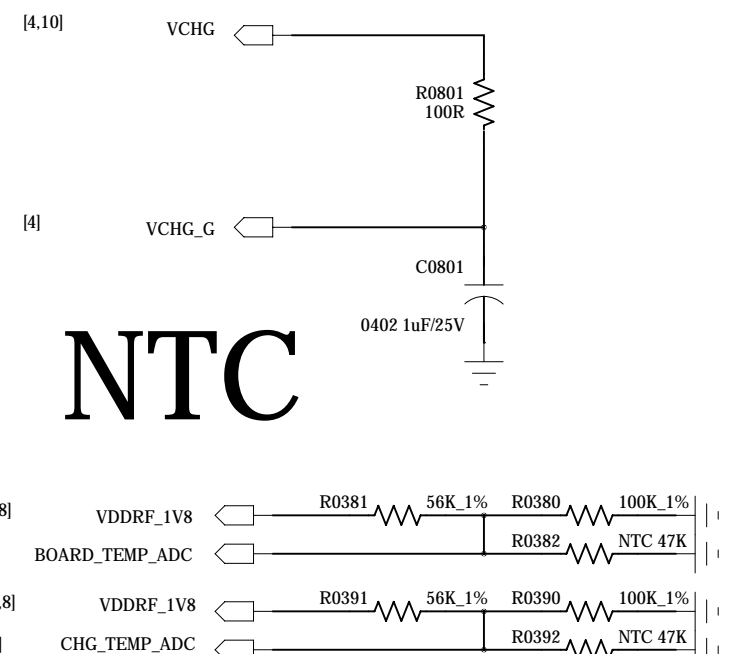
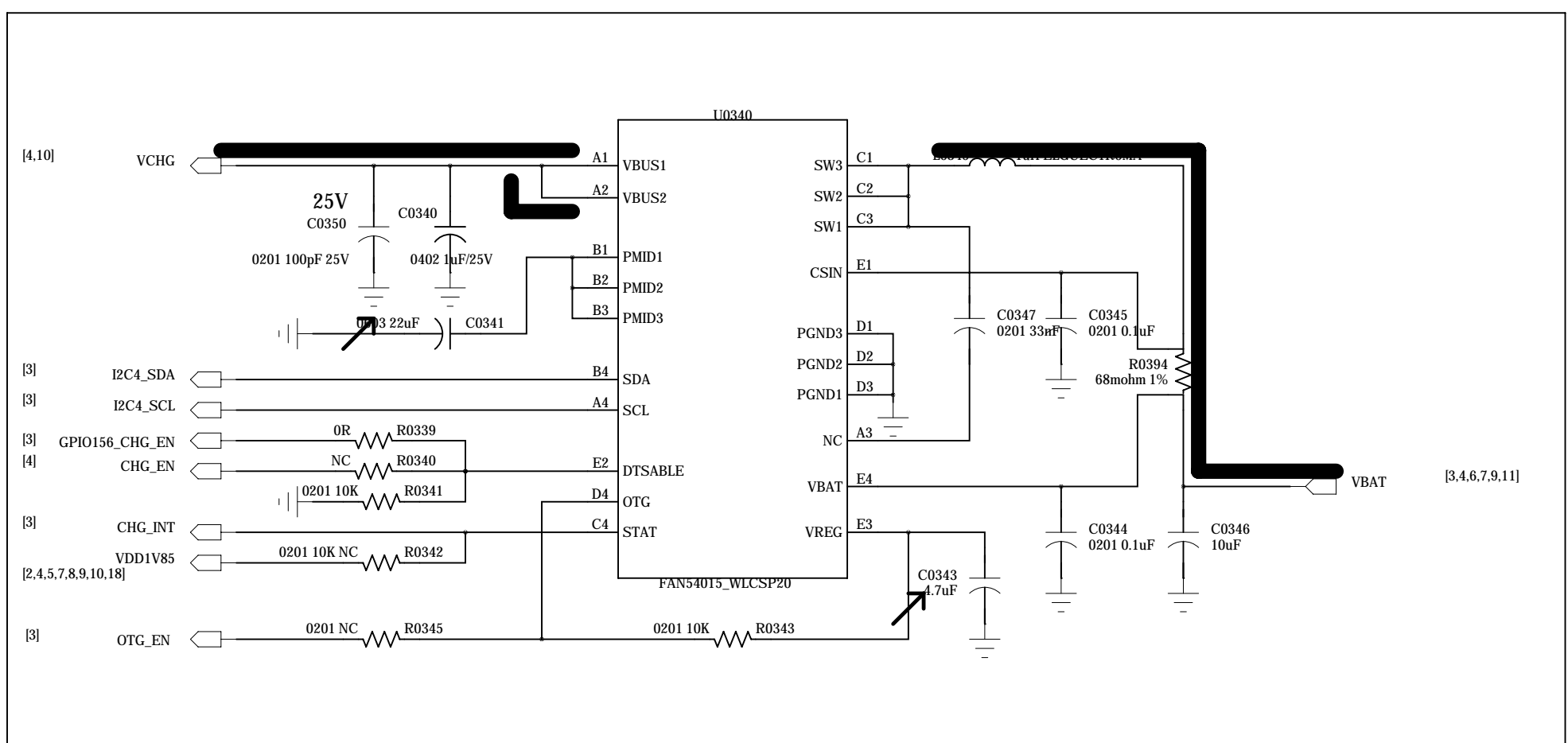


RTC Mode	R0306	R0307	X0201	C0390	C0392	R0308	R0309	R1701
32K Crystal	NF	0R	Crystal	8.2pF?	8.2pF?	0R	NF	NF
32K-less	10K	NF	NF	NF	NF	NF	0R	0R

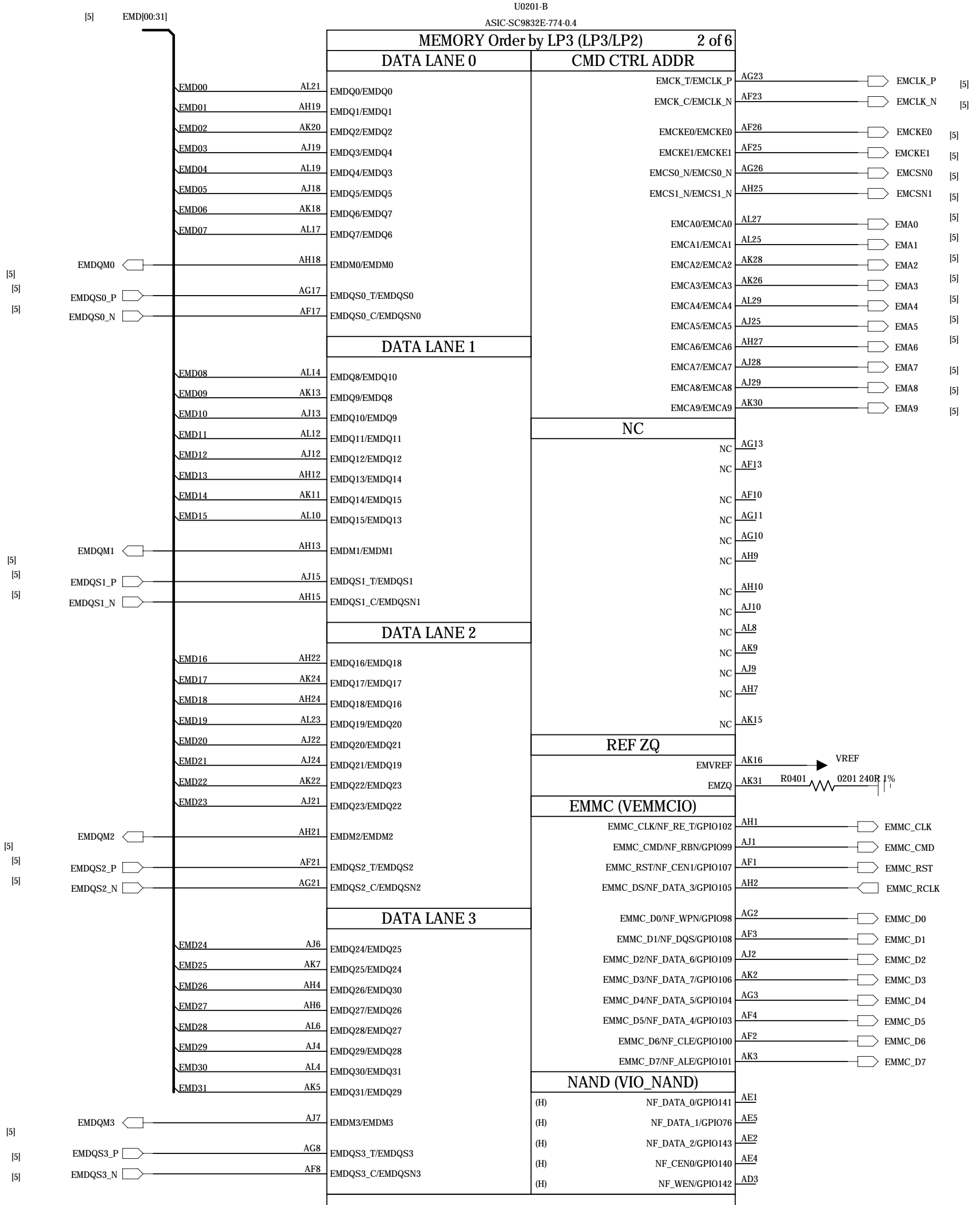
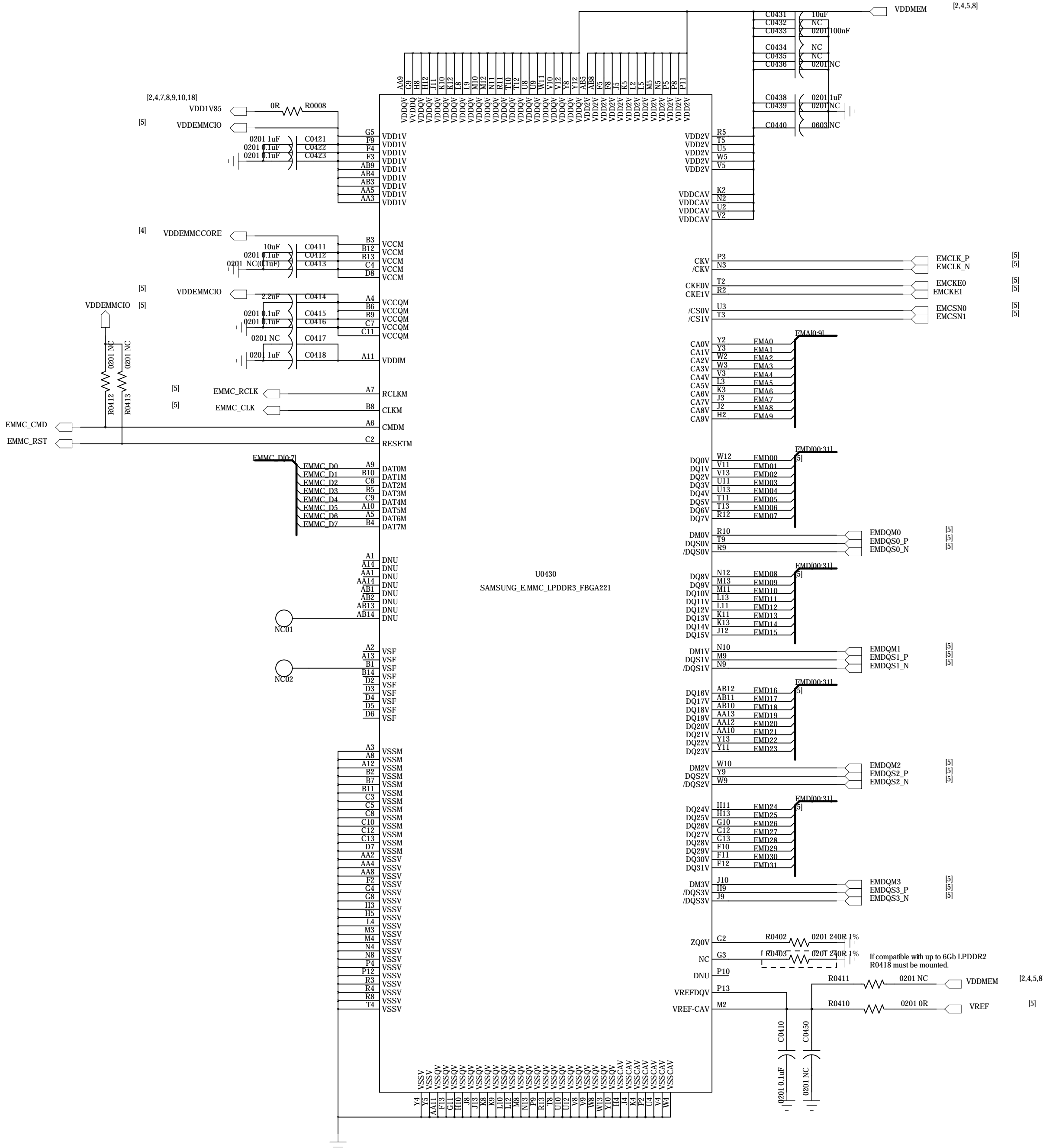
Note:32K-less only use in DCX0 mode and TSX



RTC  
BKBT

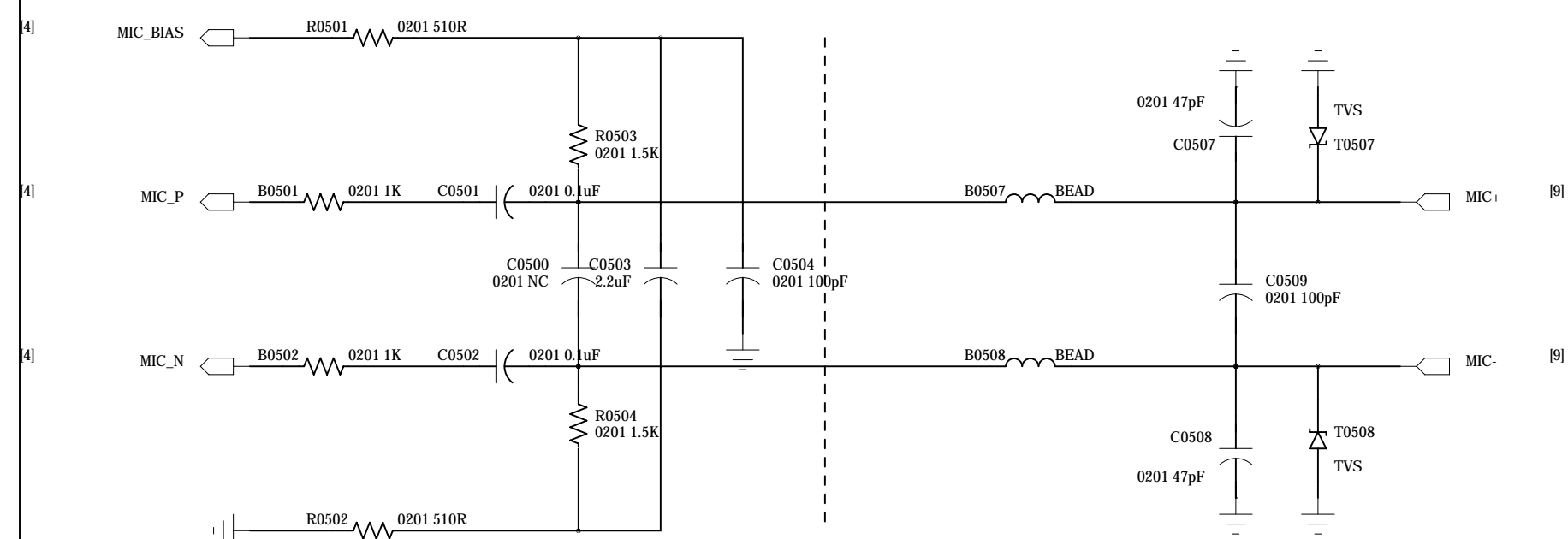


# 9832E\_Memory

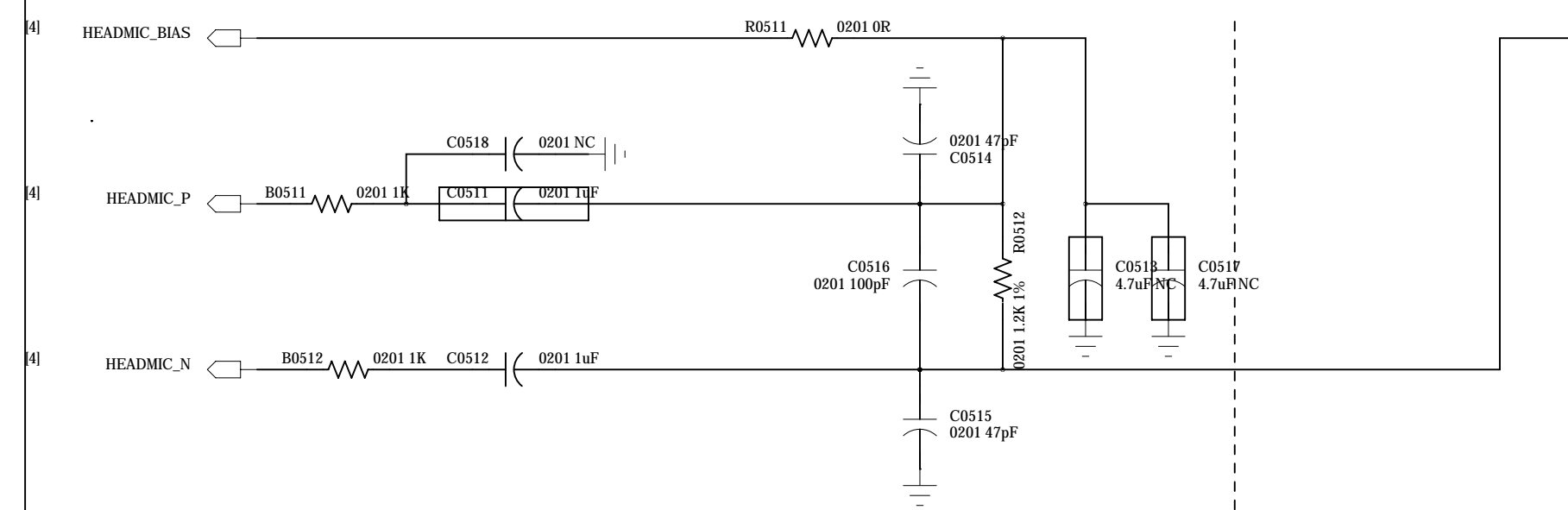


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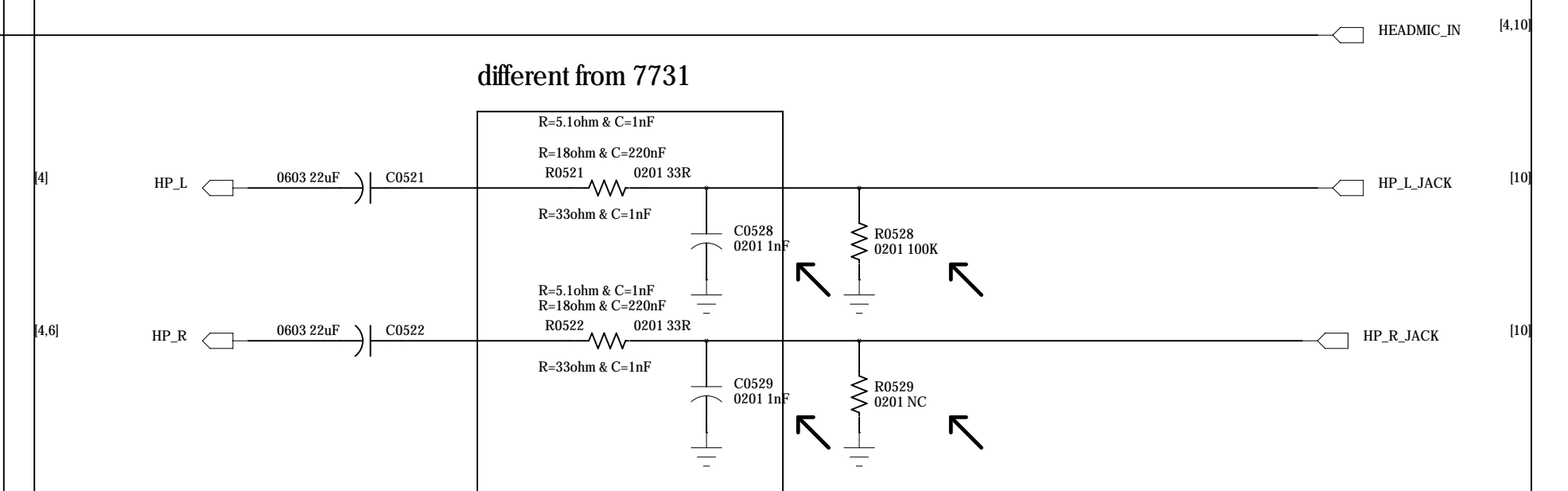
MICPHONE



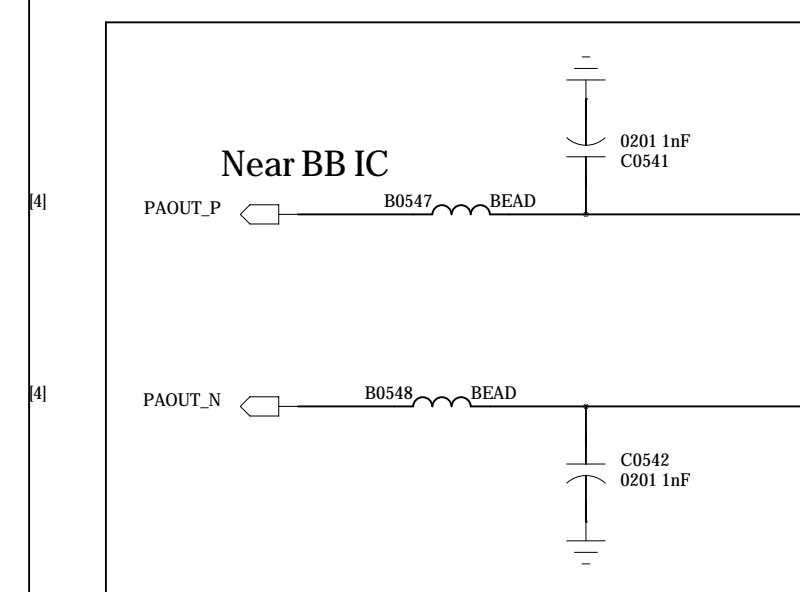
HEADSET MICROPHONE



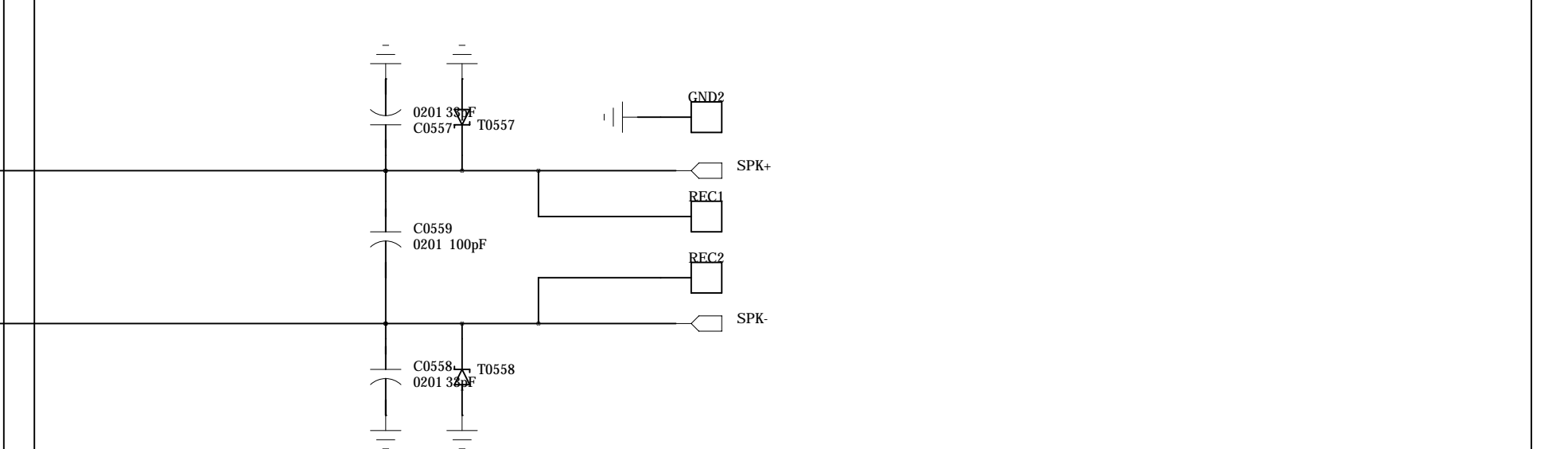
## HEADSET RECEIVER



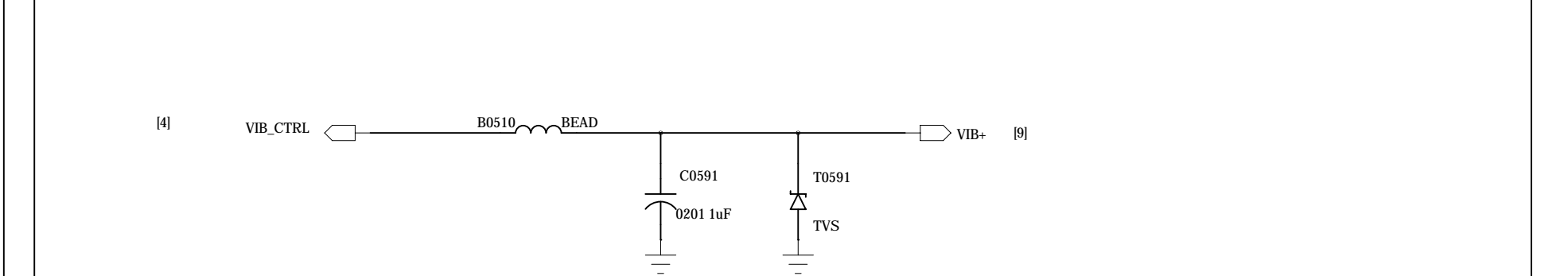
SPEAKER



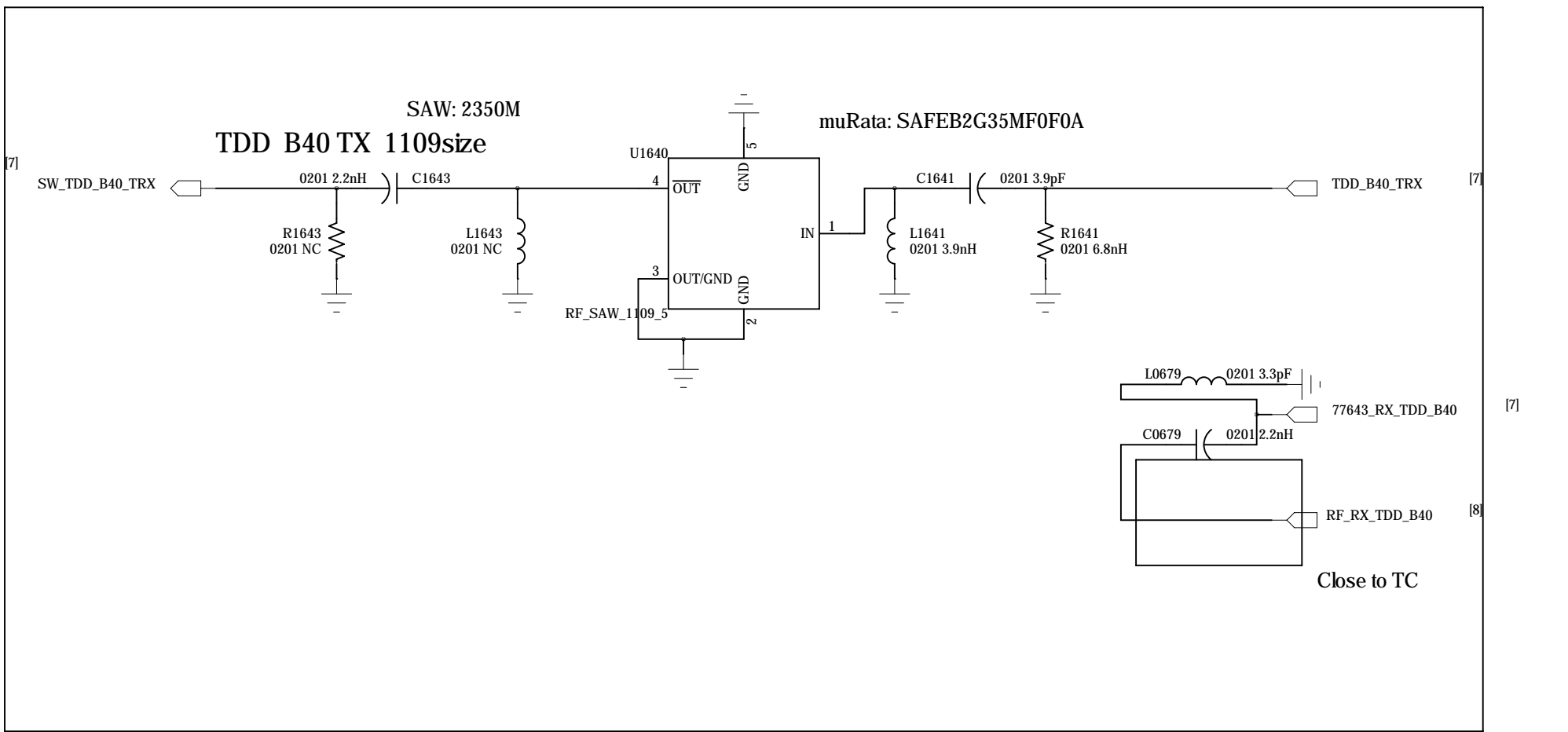
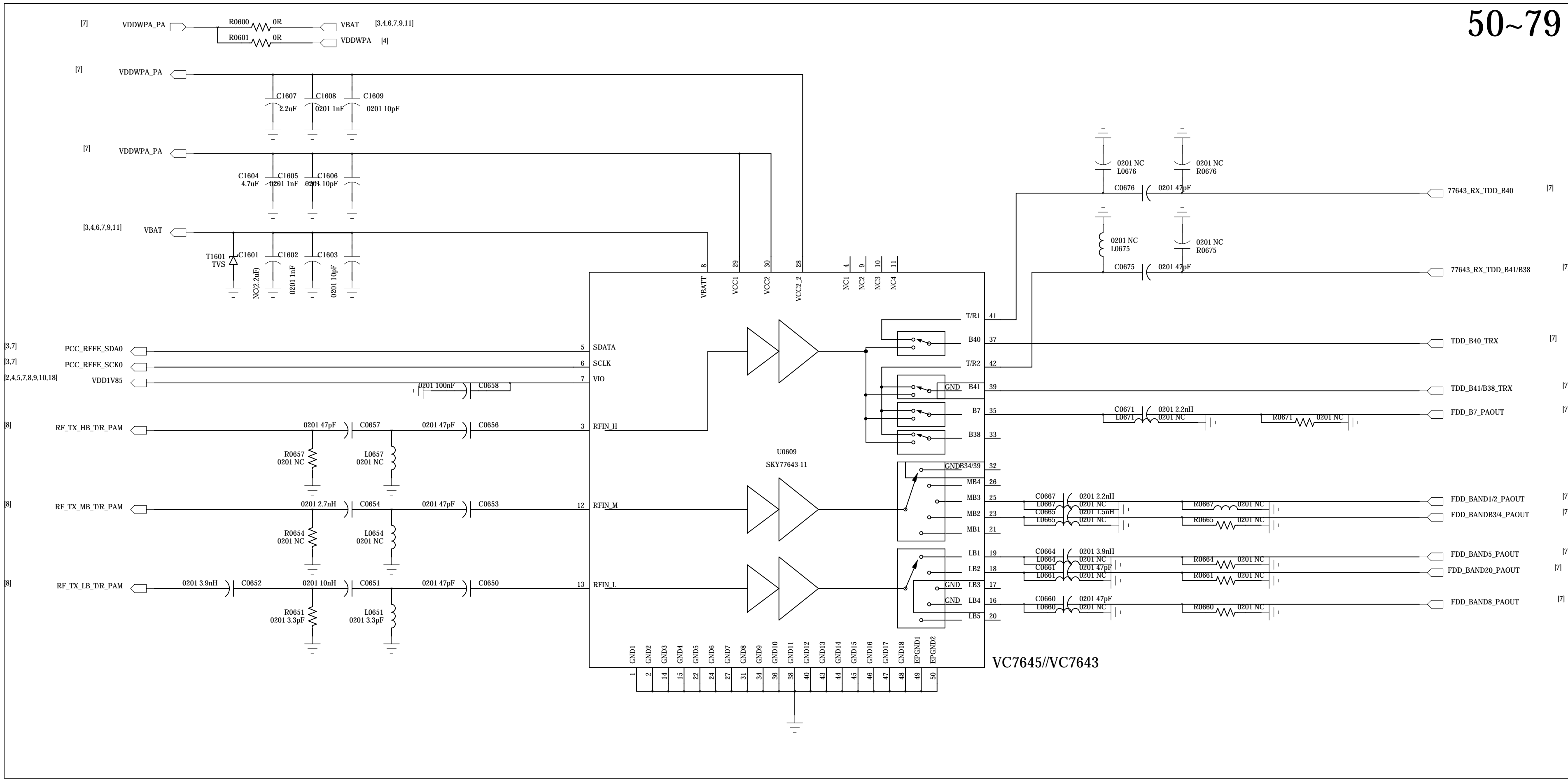
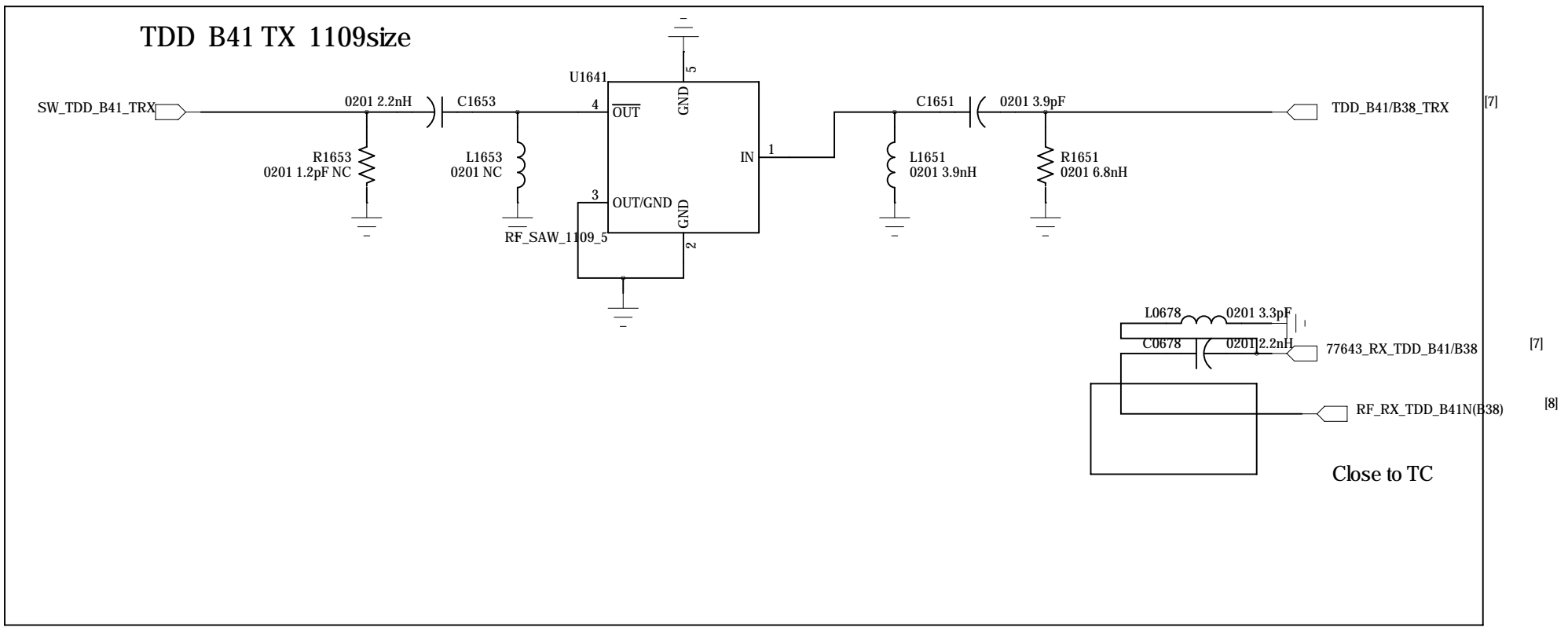
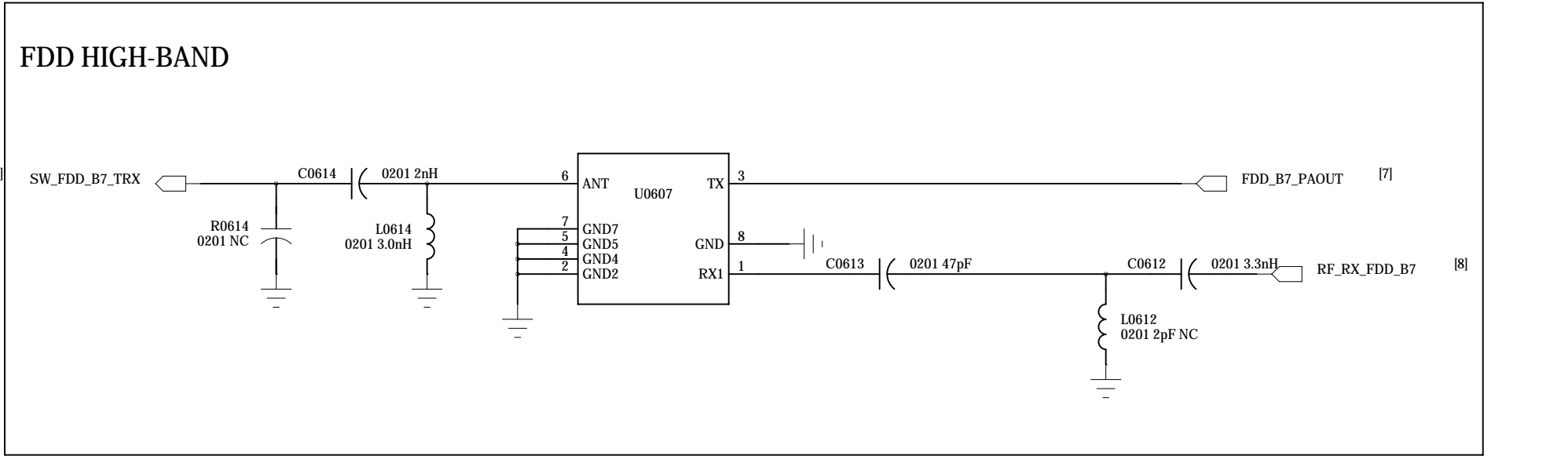
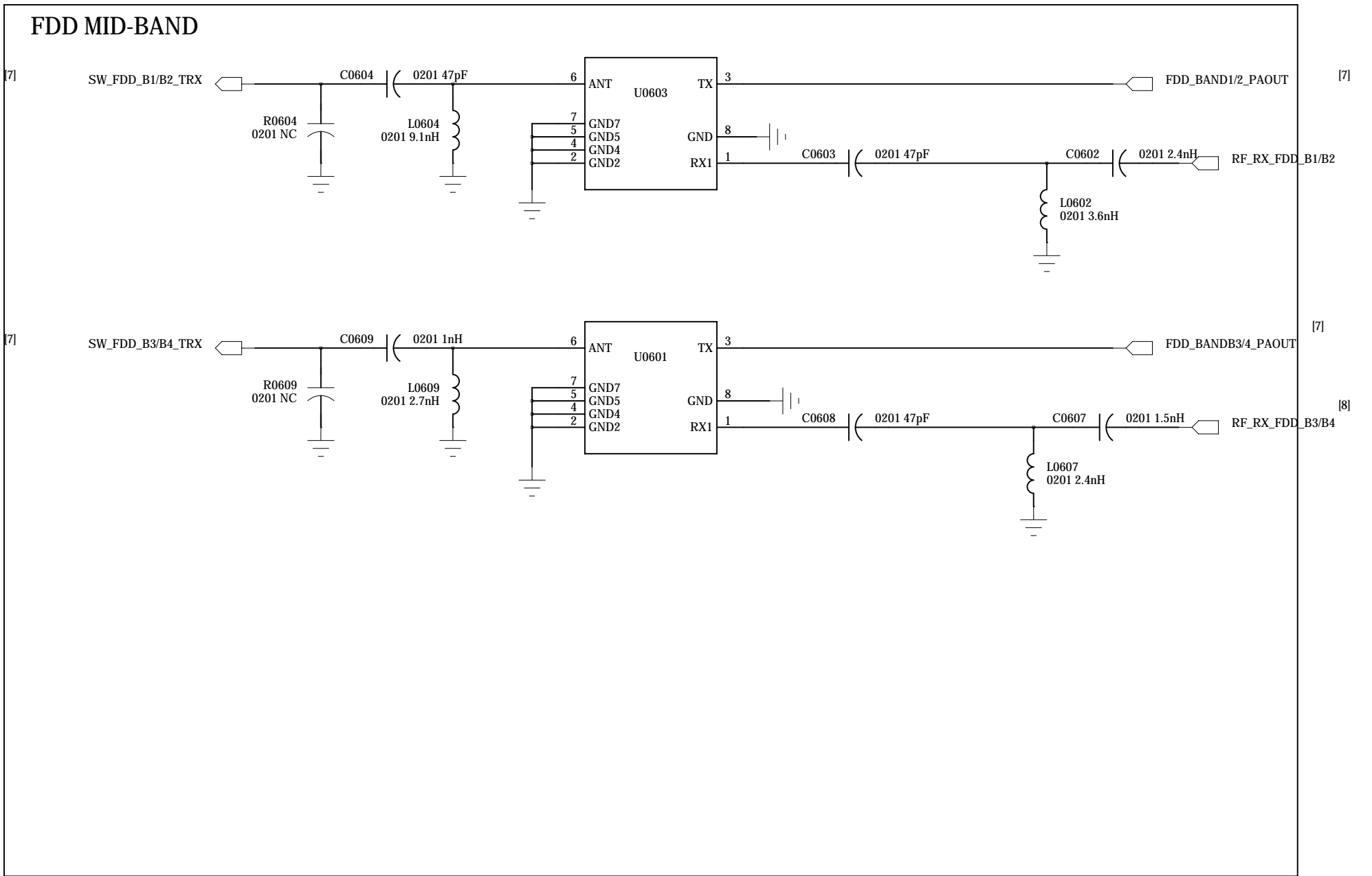
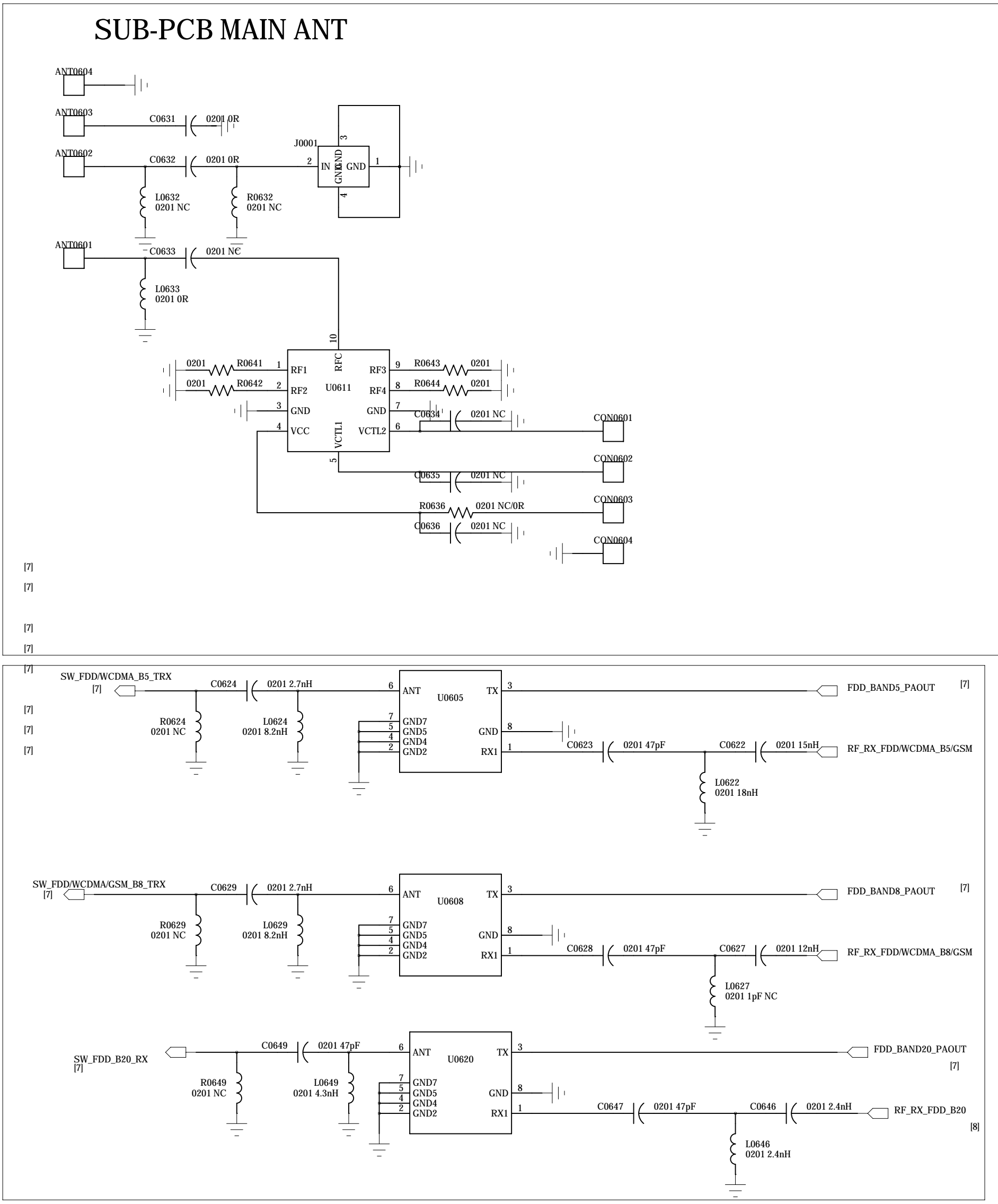
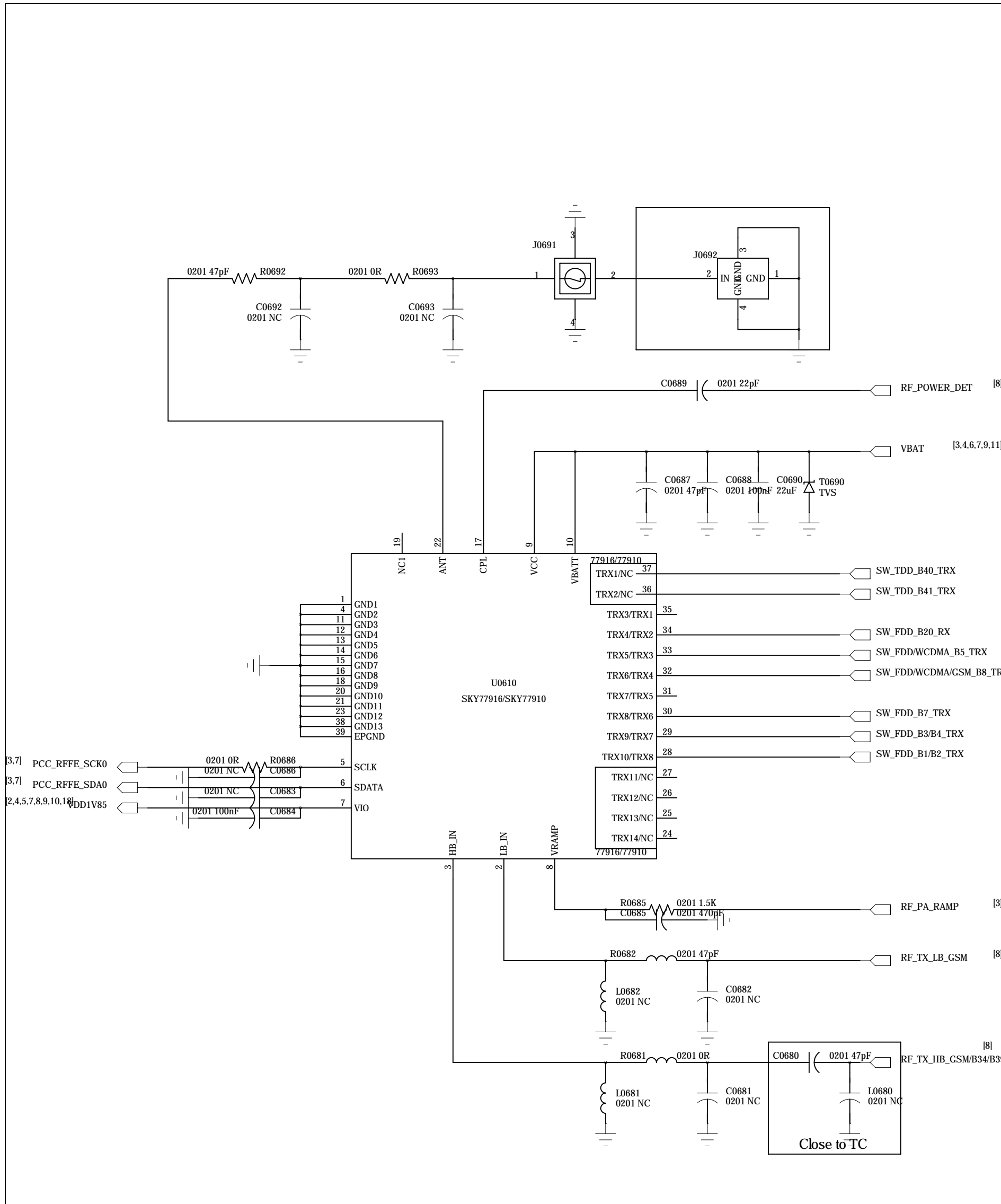
HANDSET RECEIVER+SPK 2 IN 1



## MOT



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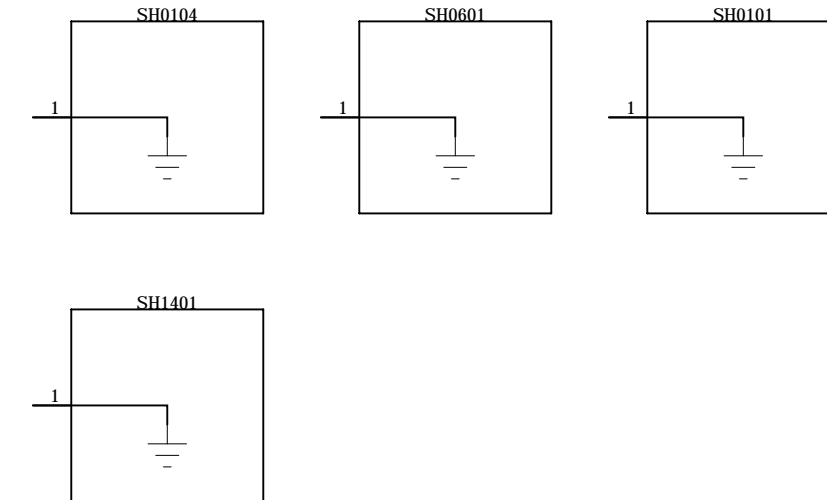
# Diversity Ant

The schematic diagram illustrates a Diversity Antenna circuit. It features three antenna inputs: ANT1462, ANT1461, and ANT1460. Each input is connected through a series of components to a central SP8T switch. ANT1462 is connected through a 0201 NC capacitor, an L1698 0201 OR inductor, and an R1698 0201 NC resistor. ANT1461 is connected through an L1698 0201 OR inductor. ANT1460 is connected through a C1697 0201 NC capacitor, an L1697 0201 NC inductor, an R1697 0201 NC resistor, a J1601 transformer, a C1695 0201 NC capacitor, and an L1695 0201 NC inductor. The outputs of these branches are connected to the SP8T switch, which is controlled by SW\_DRX\_B40 and SW\_DRX\_B41. The switch has 12 pins: 12 (NC) to TRX8, 13 (ANT) to TRX7, 14 (NC) to TRX7, 15 (GND) to PAD15, 1 (NC) to RF5, 2 (RF5) to RF3, 3 (RF3) to RF1, 4 (VDD) to SKY15414, 5 (V3) to V2, 6 (V2) to RF4, 7 (V1) to NC, 8 (NC) to RF5, 9 (RF5) to RF2, 10 (RF2) to RF4, and 11 (NC) to RF4. The switch is also connected to VDDSIM2 and RFCTL\_4, RFCTL\_3, RFCTL\_2, RFCTL\_1, and RFCTL\_0. The circuit includes various components such as resistors (R1621, R1622, R1624, R1635, R1638), capacitors (C1698, C1697, C1695, C1636, C1638), inductors (L1698, L1697, L1695, L1636, L1638), and a transformer (J1601).

# 1660~1679

The diagrams show the following components and values:

- B8/28/20:** SP6T, SW\_DRX\_B28/B20, C1679 (0201 22pF), L1679 (0201 NC), U1621 (RF\_SAW\_1109\_5), C1678 (0201 6.8pF), R1677 (0201 6.8pF), R1678 (0201 NC), RF\_DRX\_B28/B20.
- B5/B20/B17:** SP4T, SW\_DRX\_B5, C1676 (0201 22pF), L1676 (0201 8.2uH), U1622 (RF\_SAW\_1109\_5), C1675 (0201 6.8pF), C1674 (0201 6.8pF), R1675 (0201 NC), RF\_DRX\_B5.
- B1(B4):** SW\_DRX\_B1, C1673 (0201 22pF), L1673 (0201 8.2uH), U1623 (RF\_SAW\_1109\_5), C1672 (0201 6.8pF), C1671 (0201 6.8pF), R1672 (0201 NC), RF\_DRX\_FDD\_B1.
- B3/B2:** SW\_DRX\_B3, C1670 (0201 22pF), L1670 (0201 4.7pF NC), U1624 (RF\_SAW\_1109\_5), C1669 (0201 47pF), C1668 (0201 47pF), R1669 (0201 22pF), RF\_DRX\_FDD\_B3.
- B7/B40:** SW\_DRX\_B7, C1667 (0201 22pF), L1667 (0201 4.7pF NC), U1625 (RF\_SAW\_1109\_5), C1666 (0201 3.3pF), C1665 (0201 3.3pF), R1666 (0201 NC), RF\_DRX\_FDD\_B7.
- B41:** SP6T, SW\_DRX\_B41, C1664 (0201 22pF), L1664 (0201 4.7pF NC), U1626 (RF\_SAW\_1109\_5), C1663 (0201 3.3pF), C1662 (0201 3.3pF), R1663 (0201 2.4nH NC), RF\_DRX\_TDD\_B41.
- B40:** SW\_DRX\_B40, C1681 (0201 22pF), L1681 (0201 4.7pF NC), U1627 (RF\_SAW\_1109\_5), C1682 (0201 3.3pF), C1683 (0201 3.3pF), R1682 (0201 2.4nH NC), RF\_DRX\_TDD\_B40.

[illegible]

# Band INFO

The schematic diagram illustrates the Band INFO section, showing connections for three input signals: [4,8], [3], and [7,8,9,10,18]. Each signal is connected to a pair of pins (BOARD\_ID\_ADC, BOARD\_ID0, BOARD\_ID1) and a corresponding resistor (R1601, R1603, R1605) and capacitor (R1002, R1004, R1006). The resistors are labeled with their values: R1601 (0201 10K NC), R1603 (0201 10K NC), and R1605 (0201 10K NC). The capacitors are labeled with their values: R1002 (0201 56K), R1004 (0201 10K), and R1006 (0201 10K). The connections are as follows:

- [4,8] is connected to VDDRF\_1V8 and BOARD\_ID\_ADC. R1601 connects VDDRF\_1V8 to R1002. R1002 connects to a capacitor.
- [3] is connected to VDD1V85 and BOARD\_ID0. R1603 connects VDD1V85 to R1004. R1004 connects to a capacitor.
- [7,8,9,10,18] is connected to VDD1V8 and BOARD\_ID1. R1605 connects VDD1V8 to R1006. R1006 connects to a capacitor.

ID1	ID1		
0	0	TSX+32Kless	GPS
0	1	DCX0+32Kless	no GPS

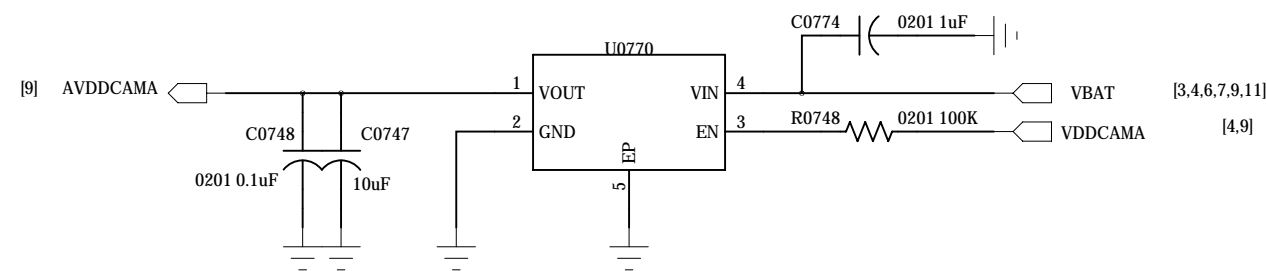
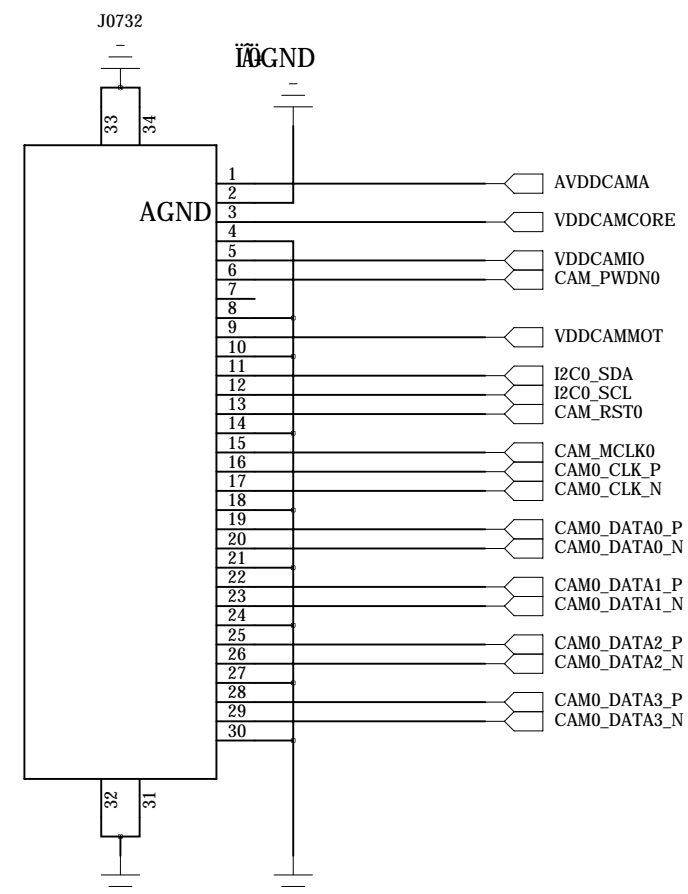
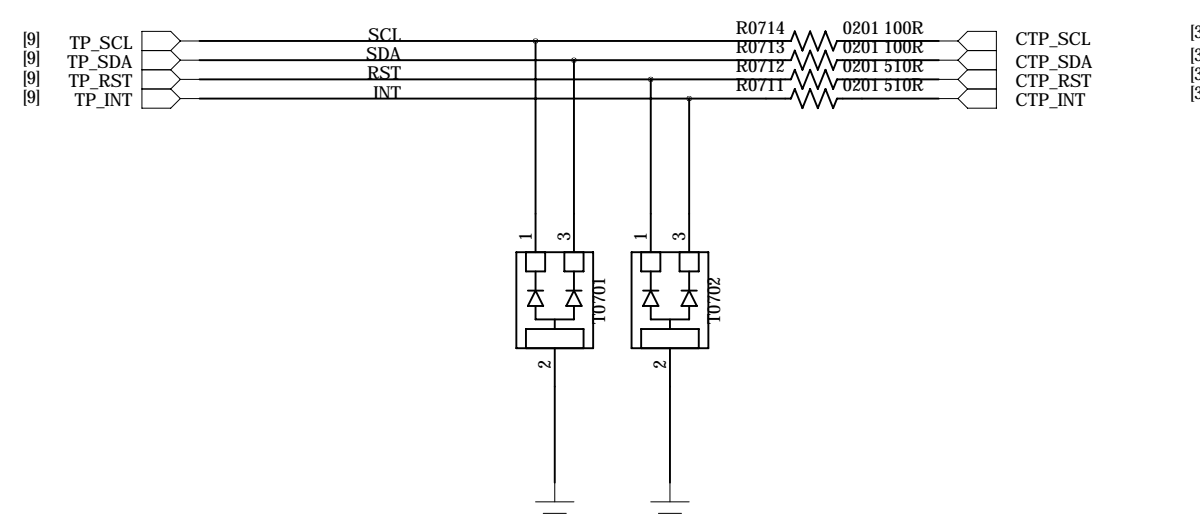
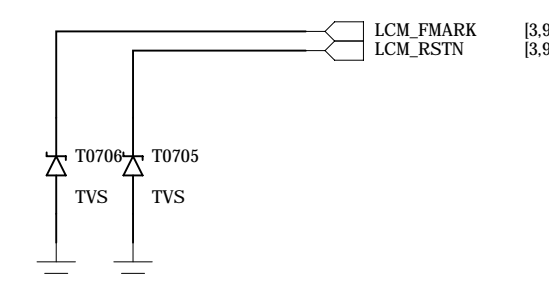
# TCXO/TSX

VALUE			R1614	C1625		R1623		R1624		
TSX+32k less	TSX		100K	1uF		0ohm		0ohm		

Default:TSX+32K less

The diagram illustrates the electrical connection for a TCXO/TSX component. A 32K crystal (X0601) is used, with its pins RT1, XT1, RT2, and XT2. The XT1 pin is connected to the GPS\_TSEN\_VREPP pin of the microcontroller through a 100K resistor (R1614). A 1uF capacitor (C1625) is connected between the XT1 pin and ground. The microcontroller also has pins GPS\_TSEN\_IN and XO\_N. The crystal's XT2 pin is connected to the TRANS\_26M\_IN pin of a module. The module also has a pin labeled GPS\_TSEN\_VREFN.

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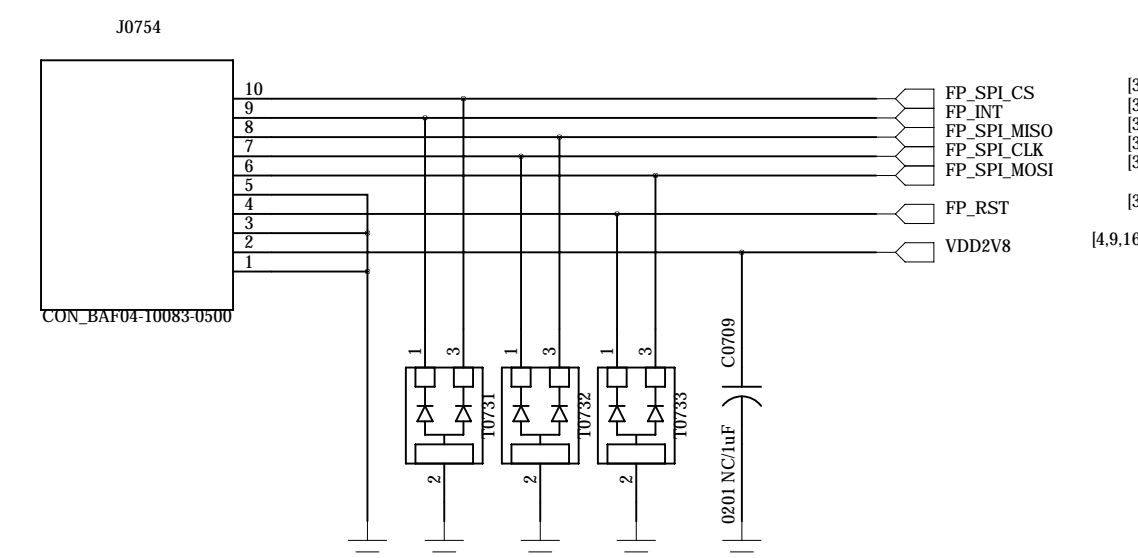
Pin	Signal	Direction
1	VDDCAMMOT	[4,9]
2	MCSIO_DATA0_P	[3]
3	MCSIO_DATA0_N	[3]
4	MCSIO_DATA1_P	[3]
5	MCSIO_DATA1_N	[3]
6	MCSIO_DATA2_P	[3]
7	MCSIO_DATA2_N	[3]
8	MCSIO_CLK_N	[3]
9	MCSIO_CLK_P	[3]
10		
11		
12	CAM_RST1	[3]
13	I2CL_SDA	[3]
14	I2CL_SCL	[3]
15		
16	CAM_PWDN1	[3]
17	AVDDCAMA	[4,9]
18		
19	VDDCAM0	[4,9]
20		
21	VDDCAMCORE	[4,9]
22		
23		
24	CAM_MCLK1	[3]
25		

AGND

27 GND

26 GND

BL125 25SL TAGF



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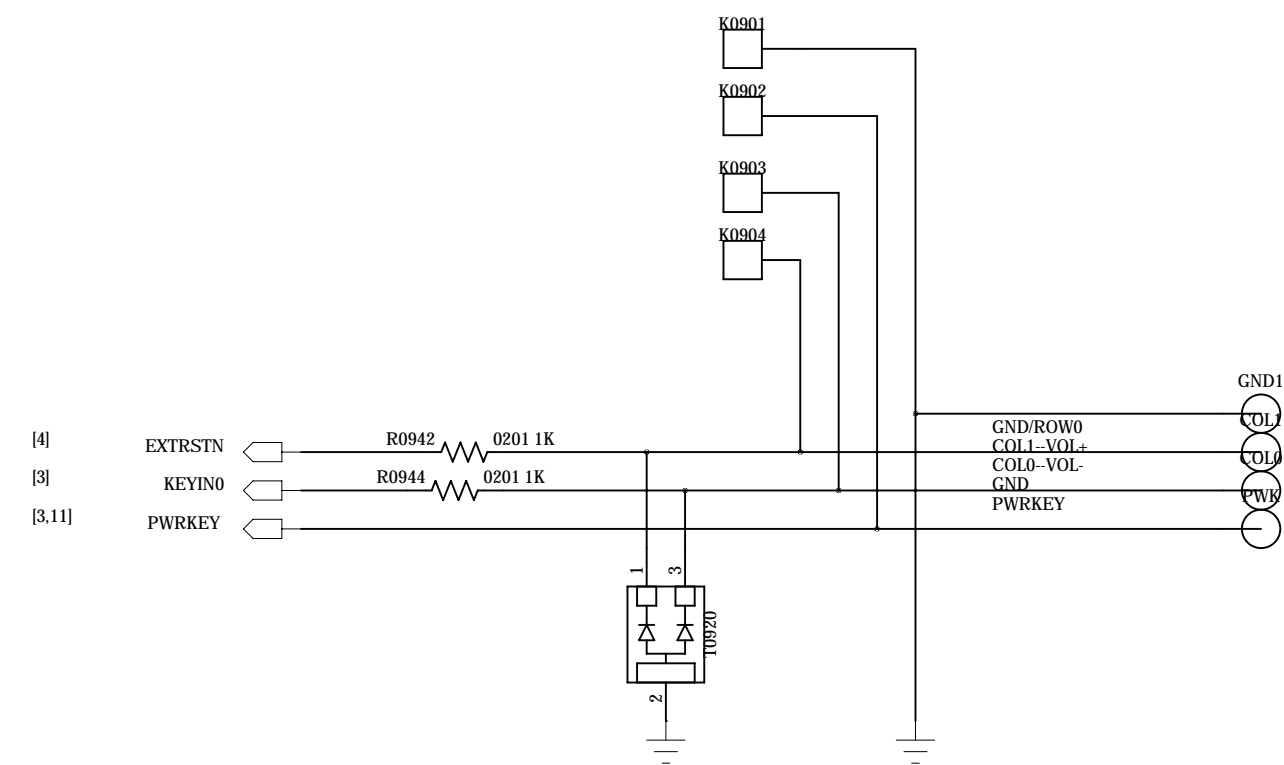
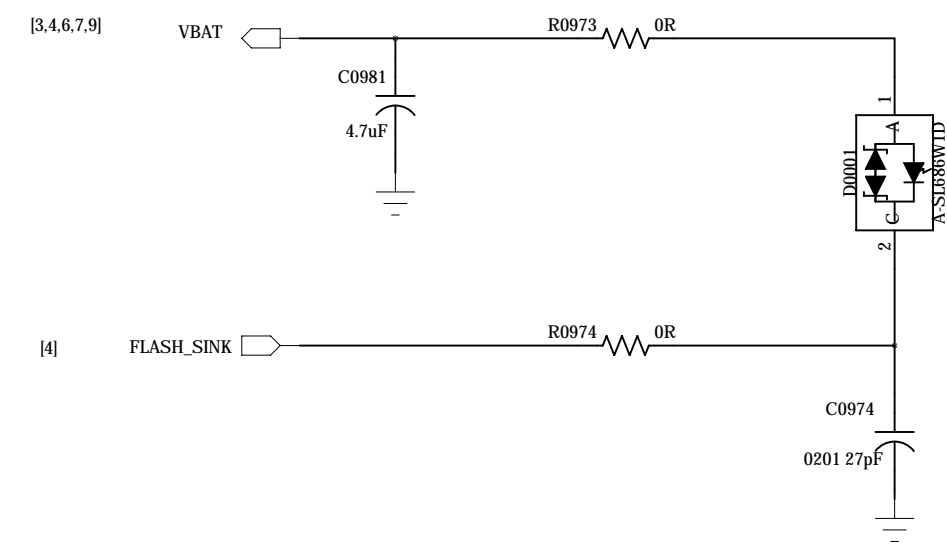






Vfb = (1.26V/Rset)*10.2K Iled = Vfb/Rfb			
Iled(mA)	Vfb(mV)	Rset(ohm)	Rfb(ohm)
441	207	62k	0.47ohm
701	330	39k	0.47ohm
760	357	36k	0.47ohm
827	389	33k	0.47ohm
911	428	30k	0.47ohm

Puke	Vib	Rfb=0.47ohm
1	47	100mA



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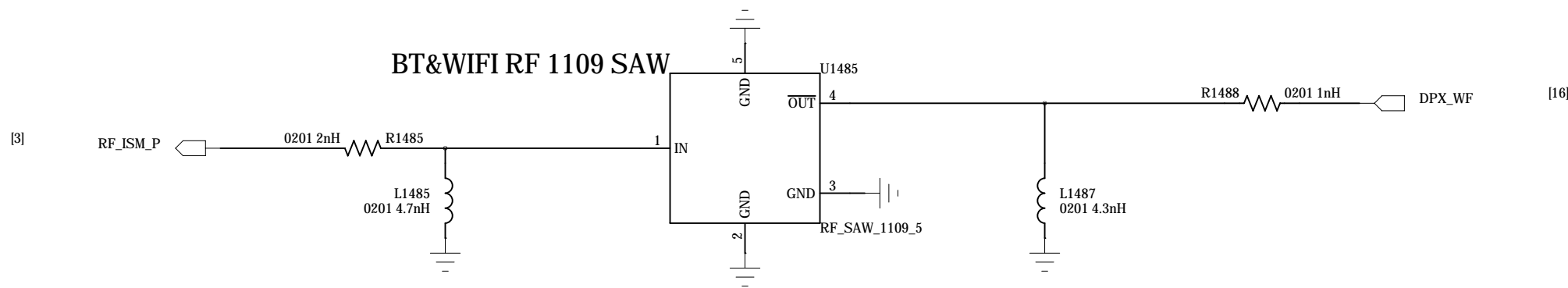
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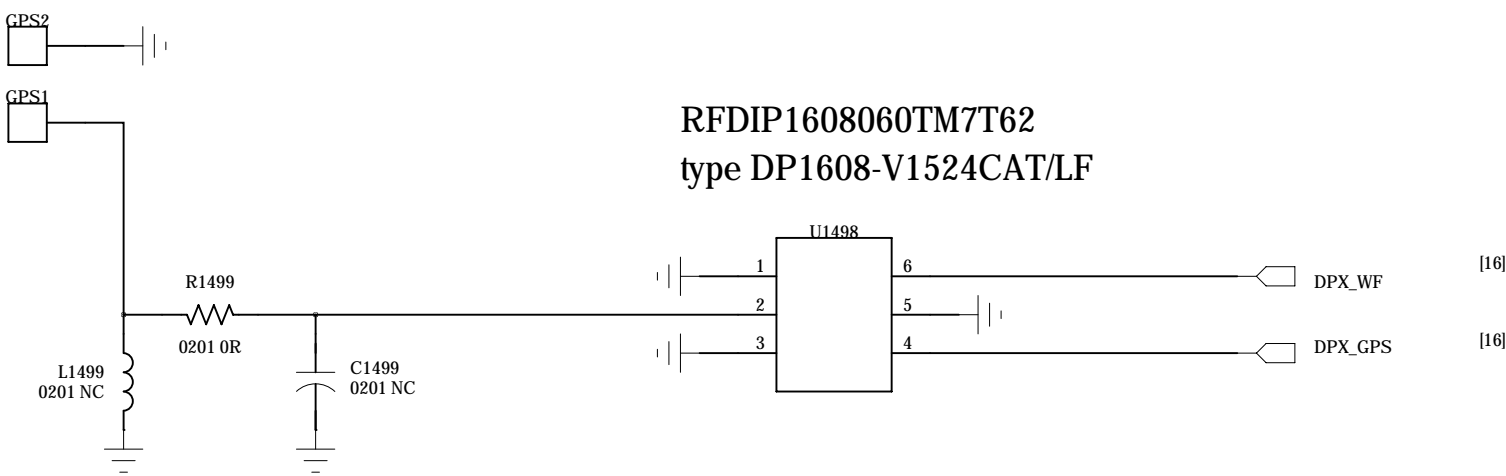




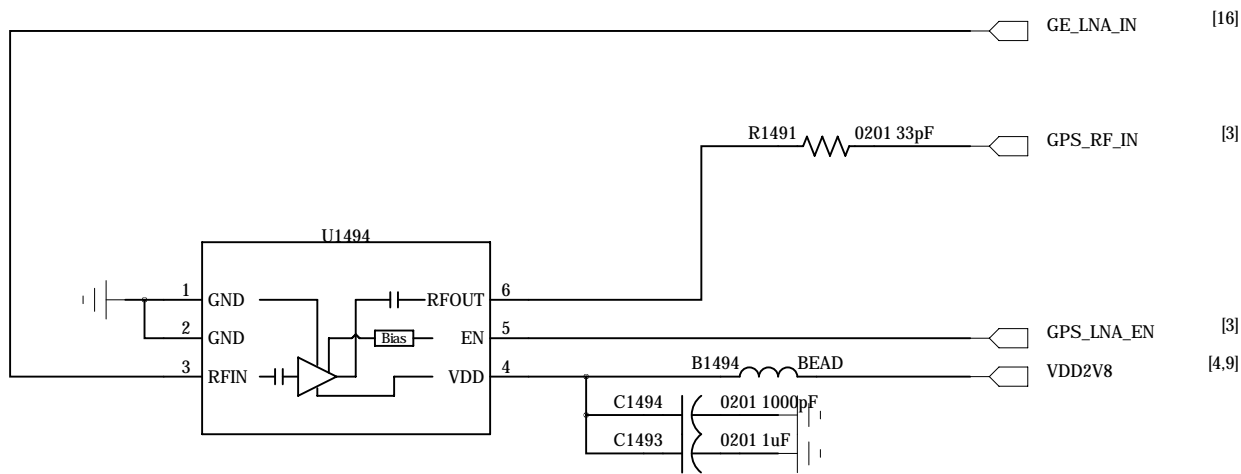
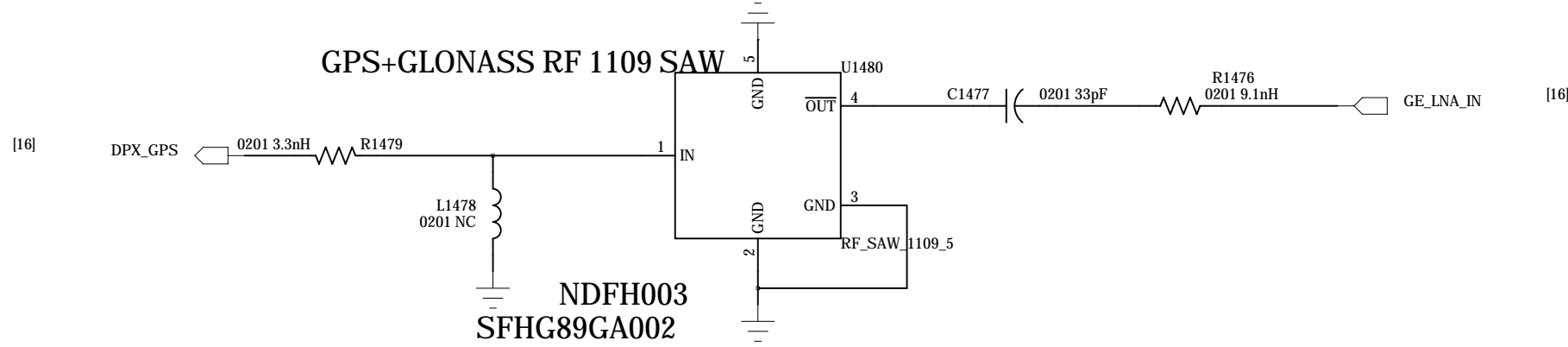
WIFI+BT



BT&WIFI&GPS ANT



GPS



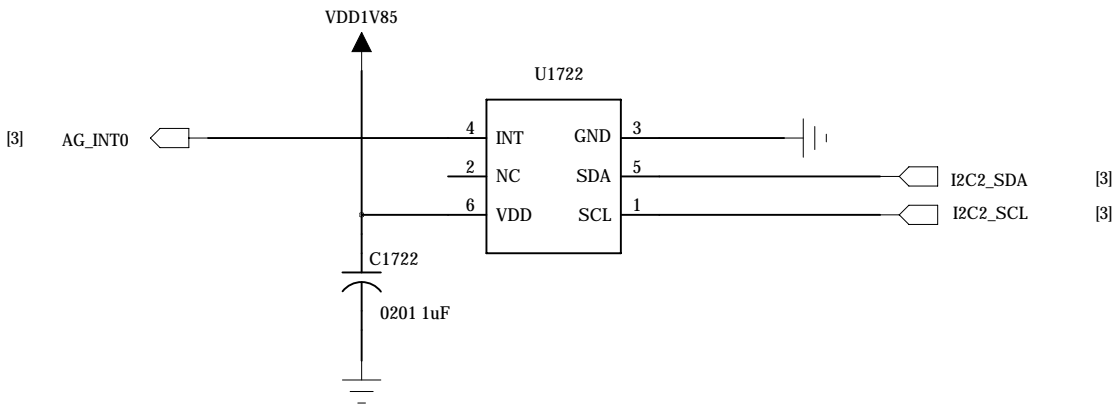
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ALS & PROX

G-Sensor

Type	MC3430	BMA250
I2C Address	0x4C (8bit address is 0xd8)	TBD
	MC3433	
pin6 tie to GND	0x4C (8bit address is 0xd8)	
pin6 tie to VDD	0x5C (8bit address is 0xd8)	



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