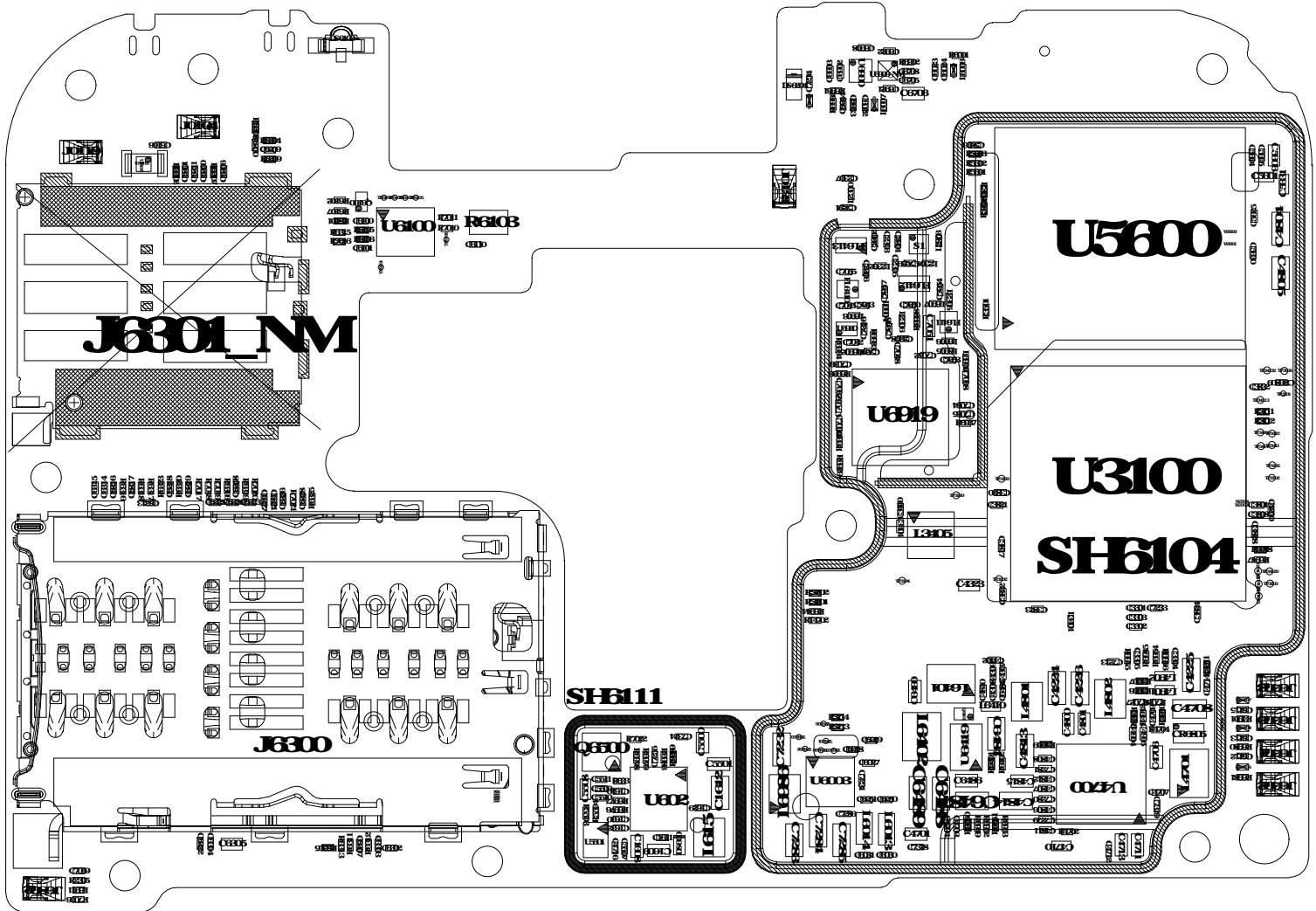
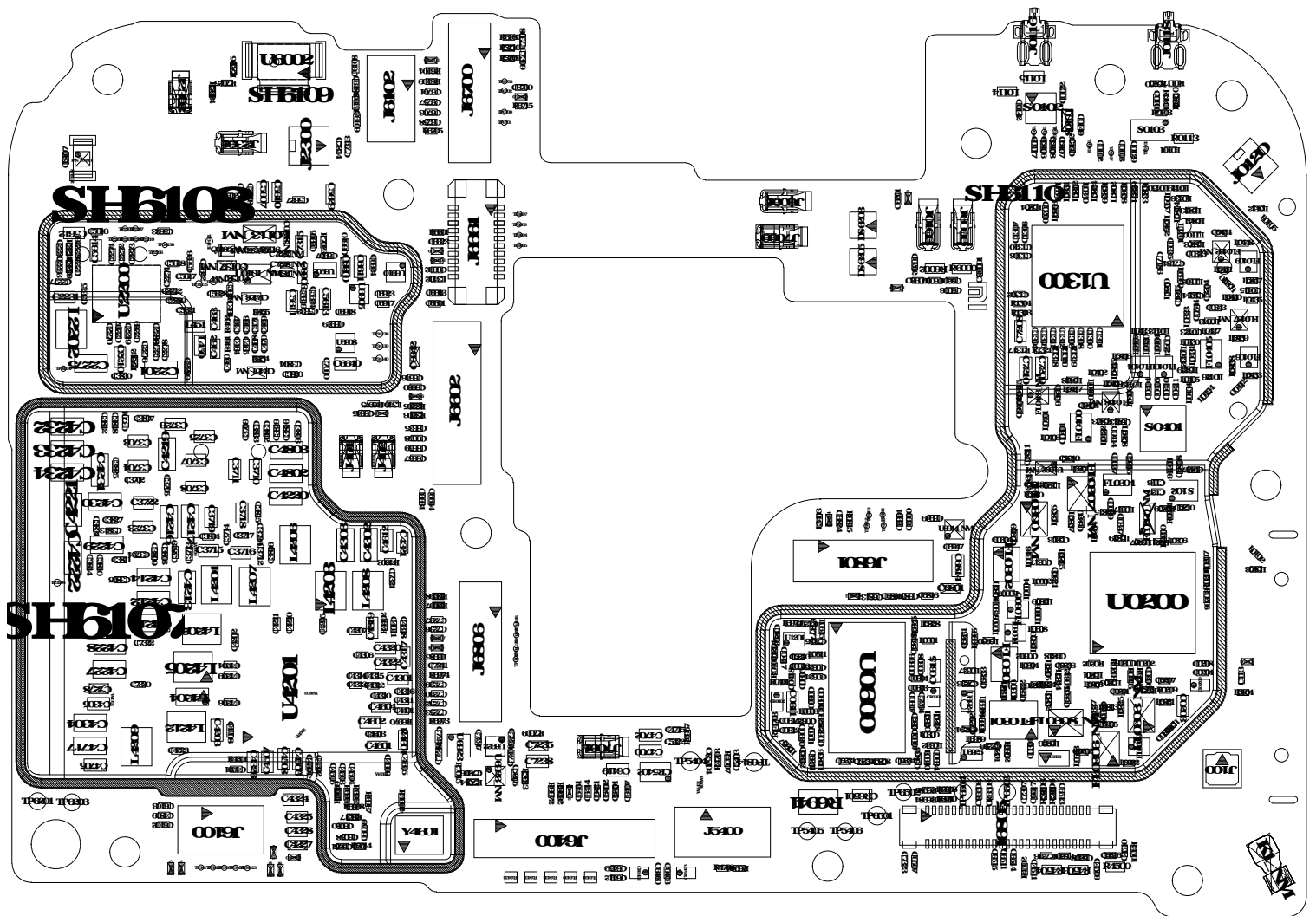


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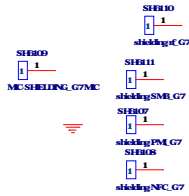
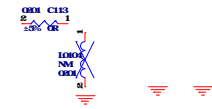
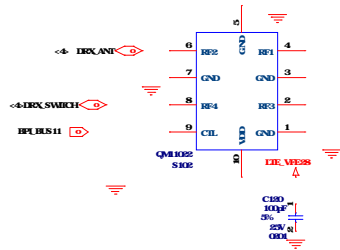


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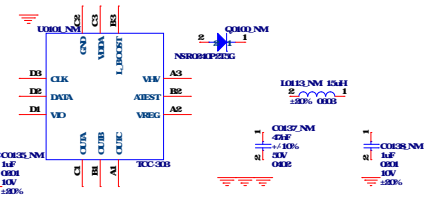
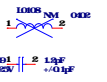
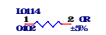
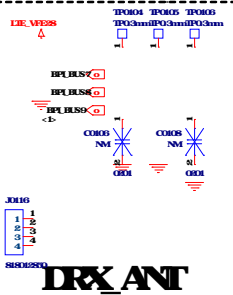
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PAGE02 Reserved(for ANI)	PAGE32 SDMGPI O 1	PAGE01 ALL ANI's
PAGE03 PRX	PAGE33 SDMGPI O 2	PAGE01 ALL ANI's
PAGE04 DRX	PAGE34 SDMMPI	PAGE01 ALL ANI's
PAGE05 Reserved(for PRX/DRX)	PAGE35 Reserved(for MPI)	PAGE01 ALL ANI's
PAGE06 PA1	PAGE36 SDM855 RF INIF	PAGE01 ALL ANI's
PAGE07 PA2	PAGE37 SDMPVR1	PAGE01 ALL ANI's
PAGE08 Reserverd for RF PA)	PAGE38 SDMPVR2	PAGE01 ALL ANI's
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PAGE10 QJN PRX	PAGE40 SDMPVR4	PAGE01 ALL ANI's
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PAGE12 Reserverd(for LAN)	PAGE42 PM855 X08CIRL	PAGE01 ALL ANI's
PAGE13 WR Interface	PAGE43 PM855 BUCK CONERTER1	PAGE01 ALL ANI's
PAGE14 WR PVR	PAGE44 PM855 BUCK CONERTER2	PAGE01 ALL ANI's
PAGE15 Reserverd(for WR)	PAGE45 PM855 LDG	PAGE01 ALL ANI's
PAGE16 Reserved(for 5G sub 6G)	PAGE46 PM855 GPI O&GND	PAGE01 ALL ANI's
PAGE17 Reserved(for 5G MMWave)	PAGE47 PM855A CIRL&GND	PAGE01 ALL ANI's
PAGE18 Reserved(for 5G WR I)	PAGE48 PM855A BUCK CONERTER1	PAGE01 ALL ANI's
PAGE19 Reserved(for 5G WR P)	PAGE49 PM855A BUCK CONERTER2	PAGE01 ALL ANI's
PAGE20 Reserved(for 5G DC DC)	PAGE50 PM855A AMLED&BOB&FLASH	PAGE01 ALL ANI's
PAGE21 Reserved(for 5G OTHERS)	PAGE51 PM855A LDG	PAGE01 ALL ANI's
PAGE22 NFC	PAGE52 PM855B CIRL&GND	PAGE01 ALL ANI's
PAGE23 GPS	PAGE53 PM855B F&PII C&OIG	PAGE01 ALL ANI's
PAGE24 W/FI Interface	PAGE54 PM855B CHARGER	PAGE01 ALL ANI's
PAGE25 W/FI FEM	PAGE55 SMI1390&VL POWER	PAGE01 ALL ANI's
PAGE26 W/FI Decoupling		
PAGE27 Reserved(for 11ad/ 11ax)		
PAGE28 Reserved(for 11ad/ 11ax)		
PAGE29 Reserved(for Digital FM)		
PAGE30 Reserved(for Analog FM)		

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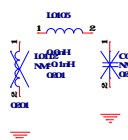
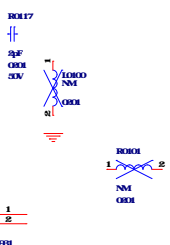


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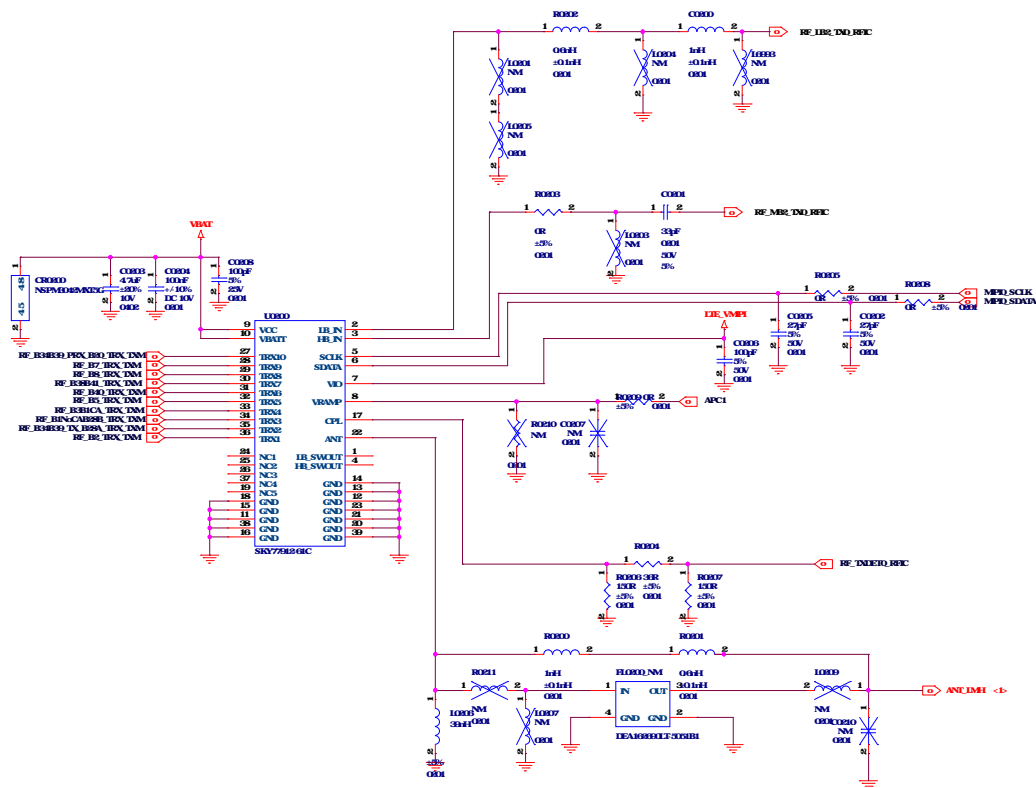
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**CLOSE TO DRX ANT**



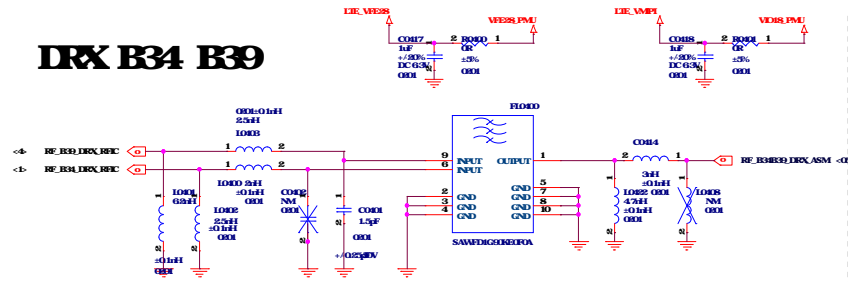
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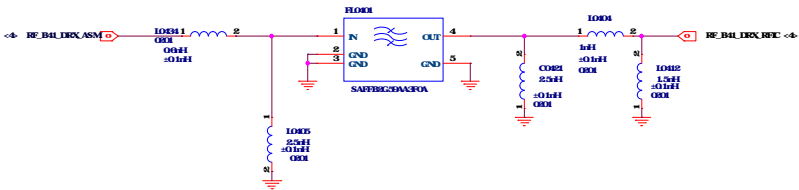
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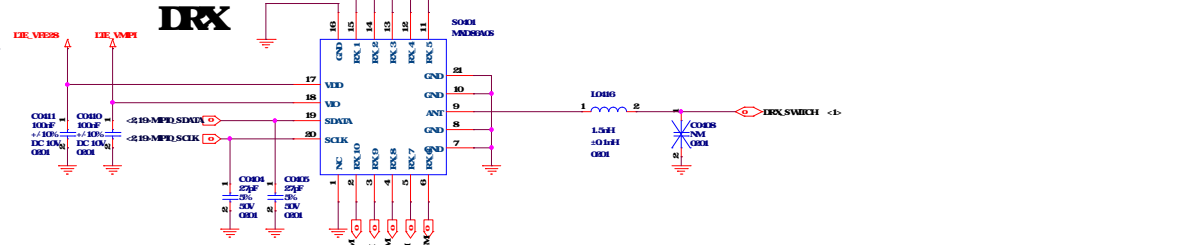
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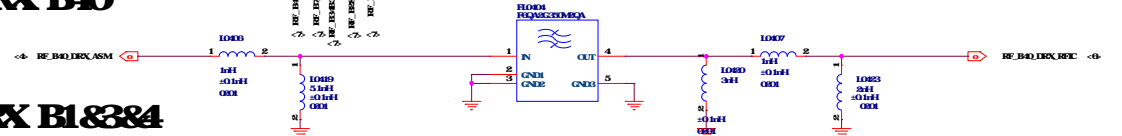
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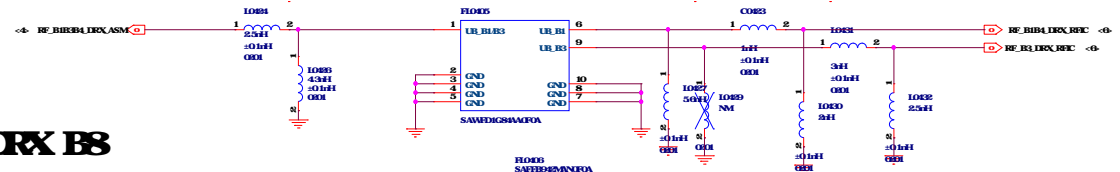
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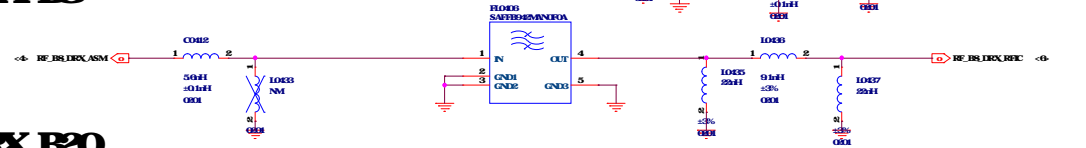
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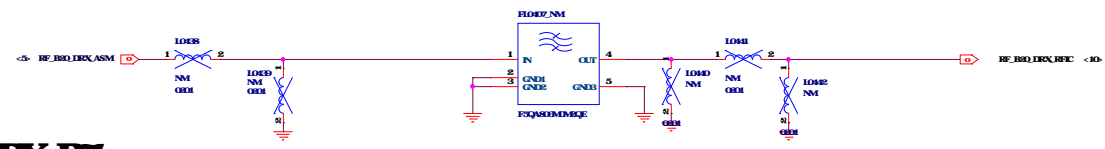
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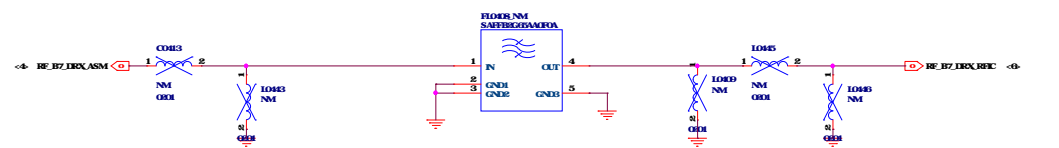
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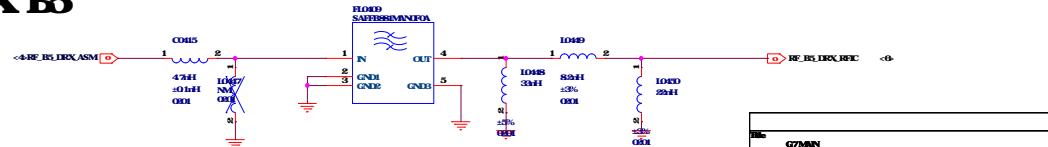
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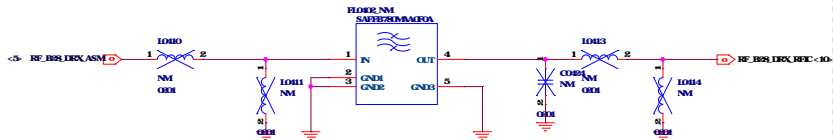
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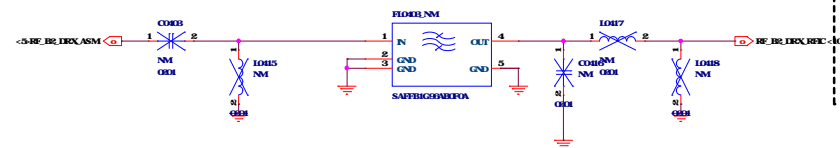
### DRX B5



### DRX B28



### DRX B2



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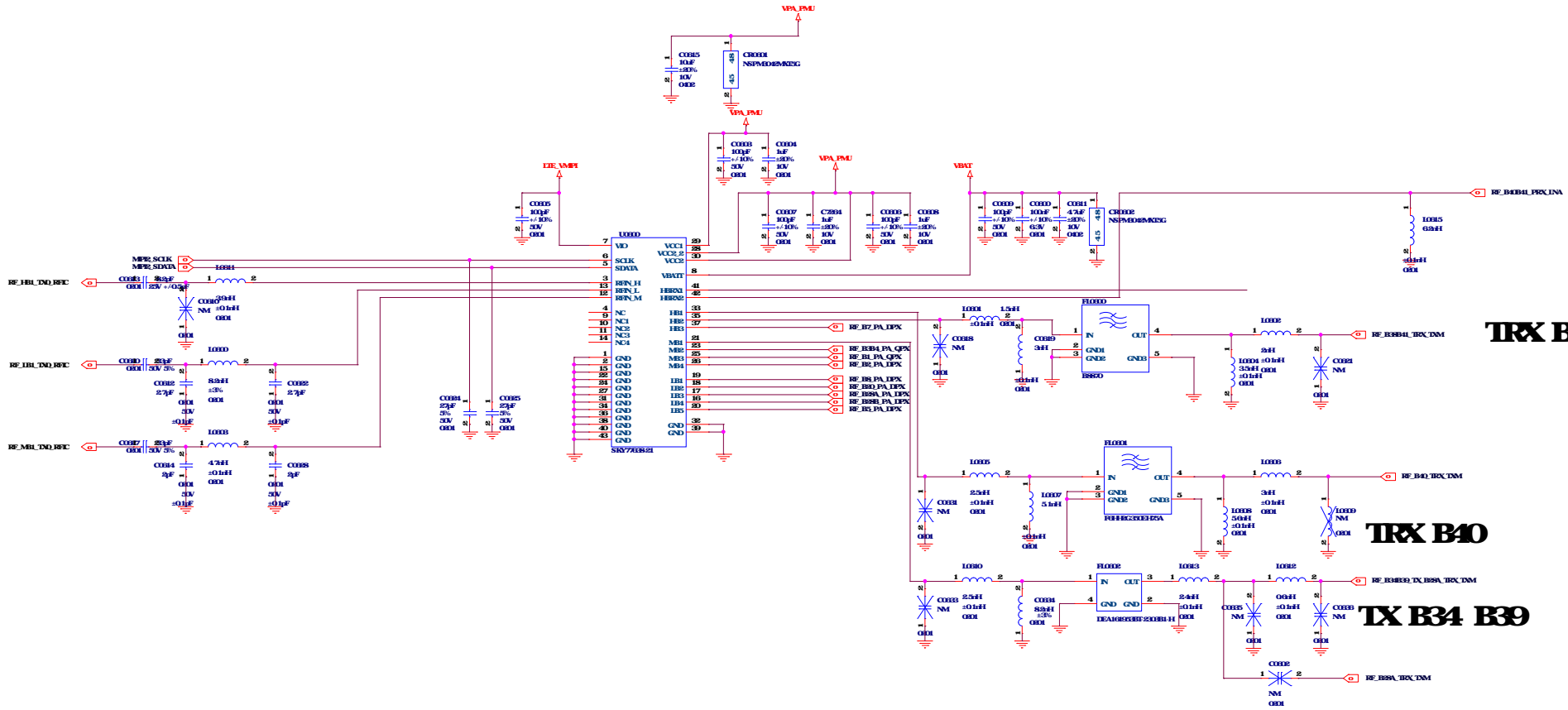
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**TRX B41**

**TRX B40**

**TX B34 B39**

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A3	RFDC-DC	PO
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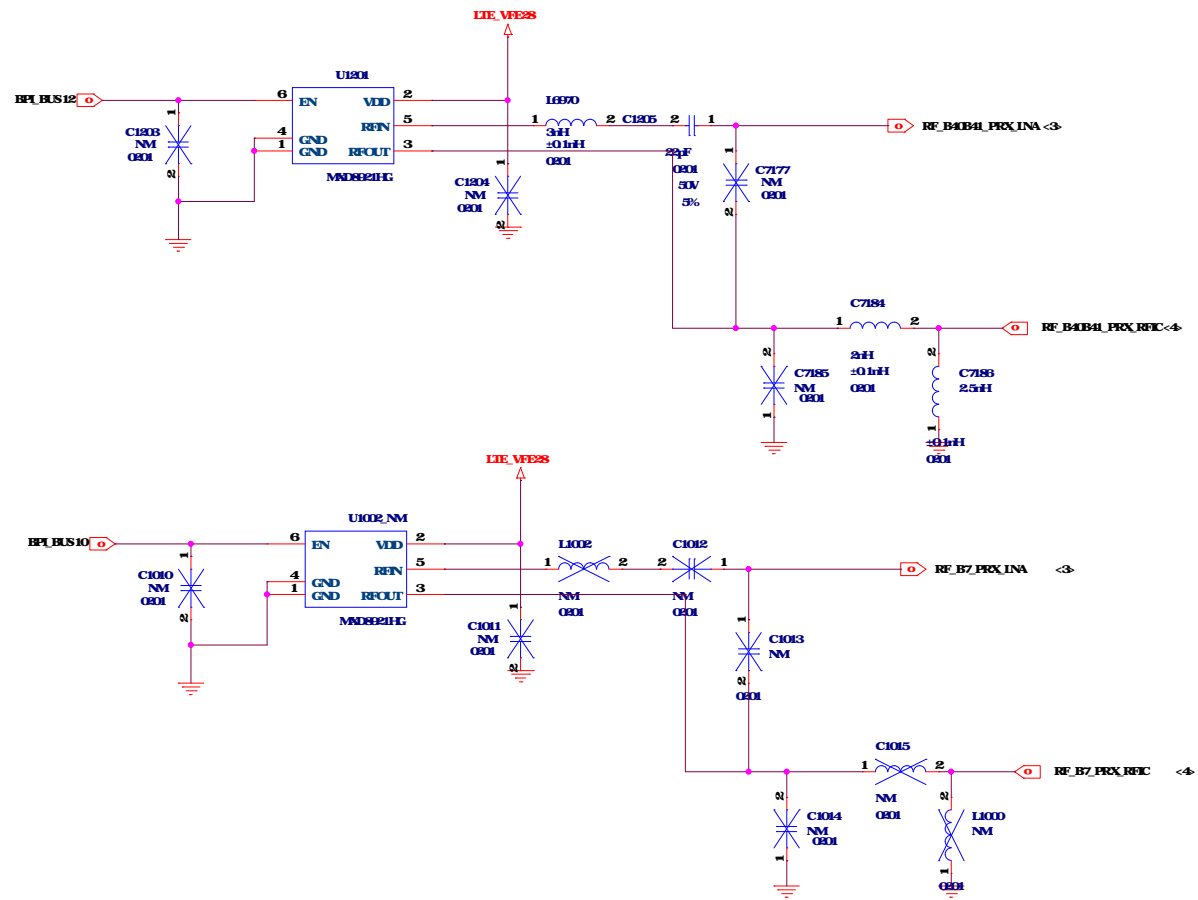
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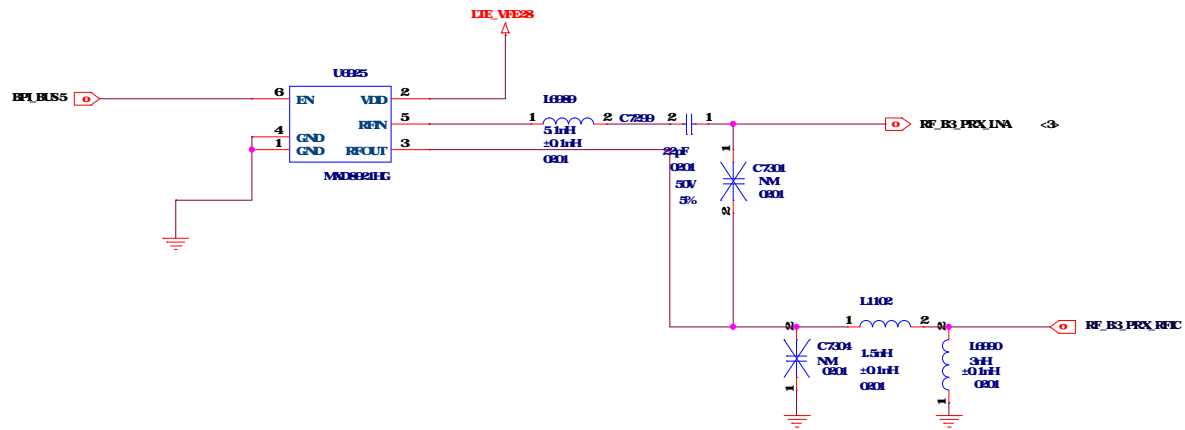
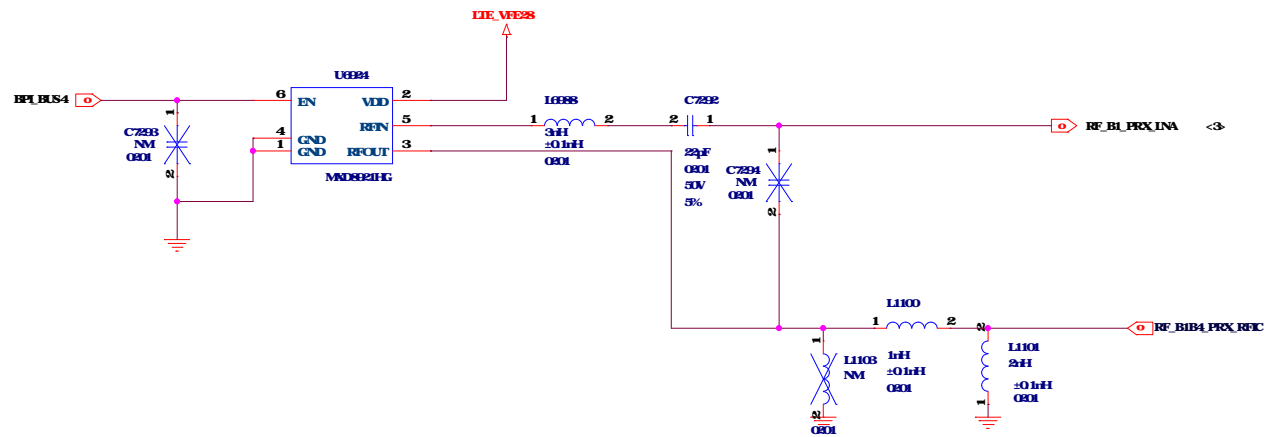
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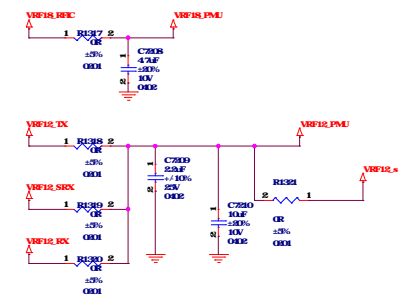
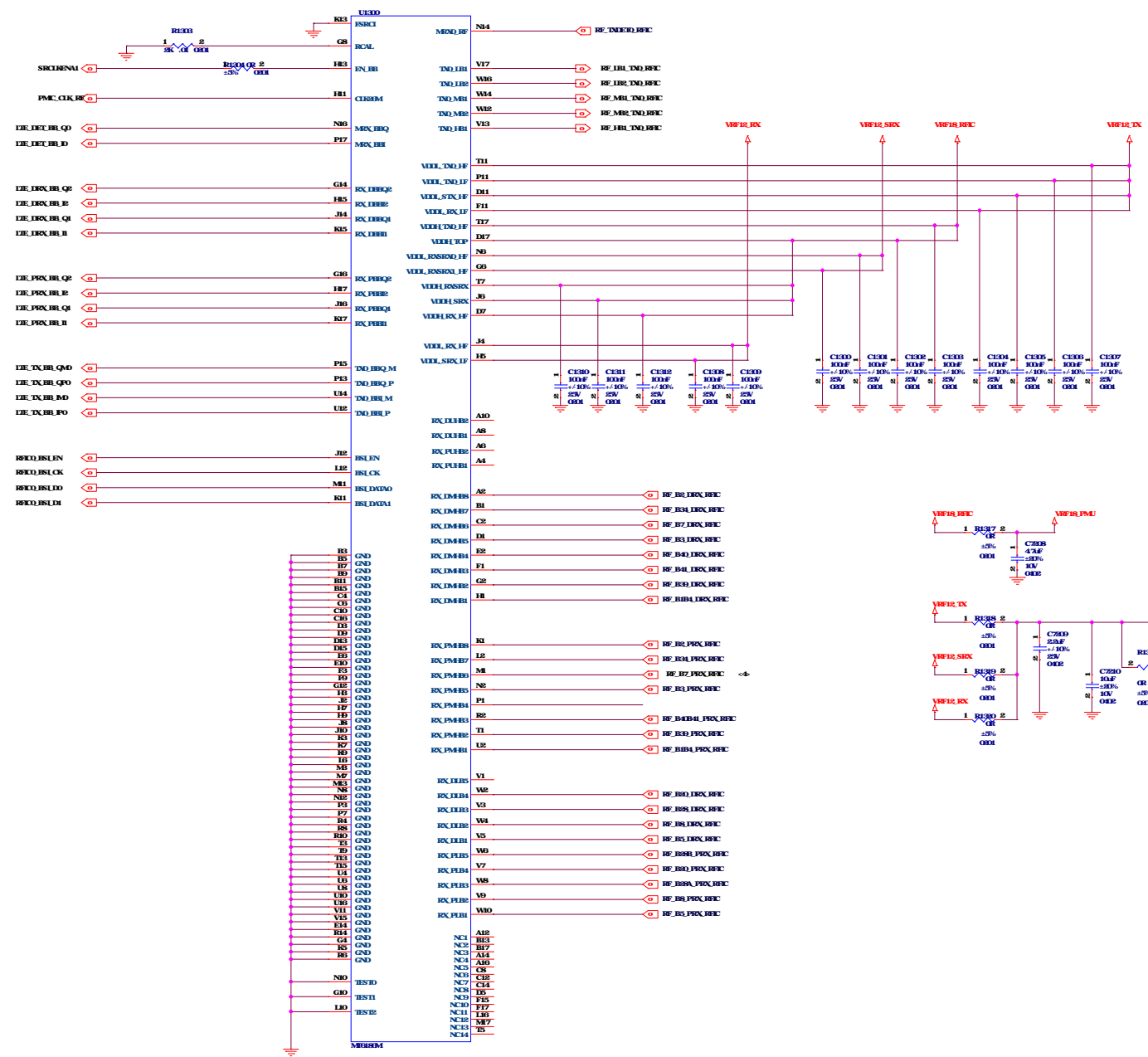
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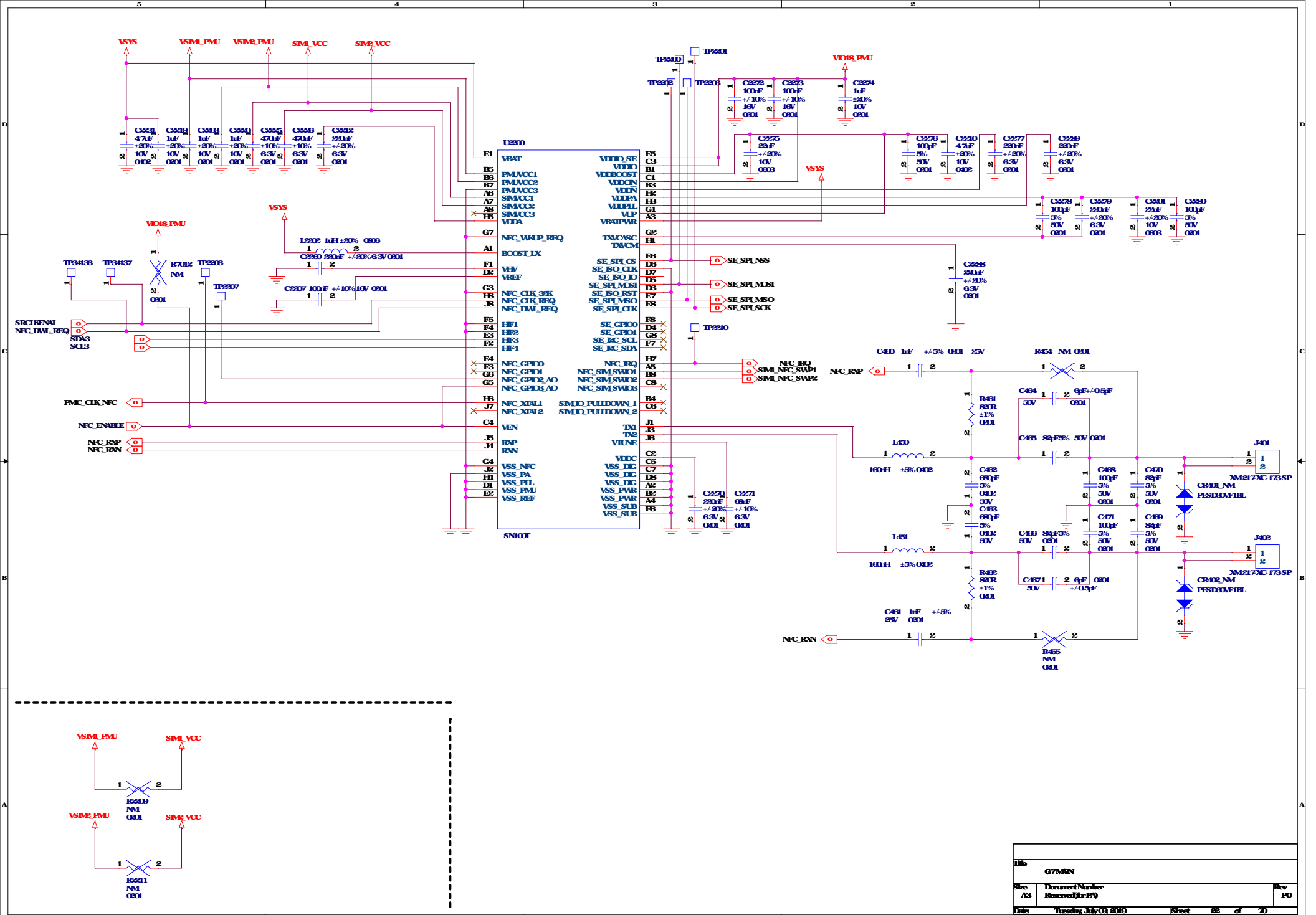
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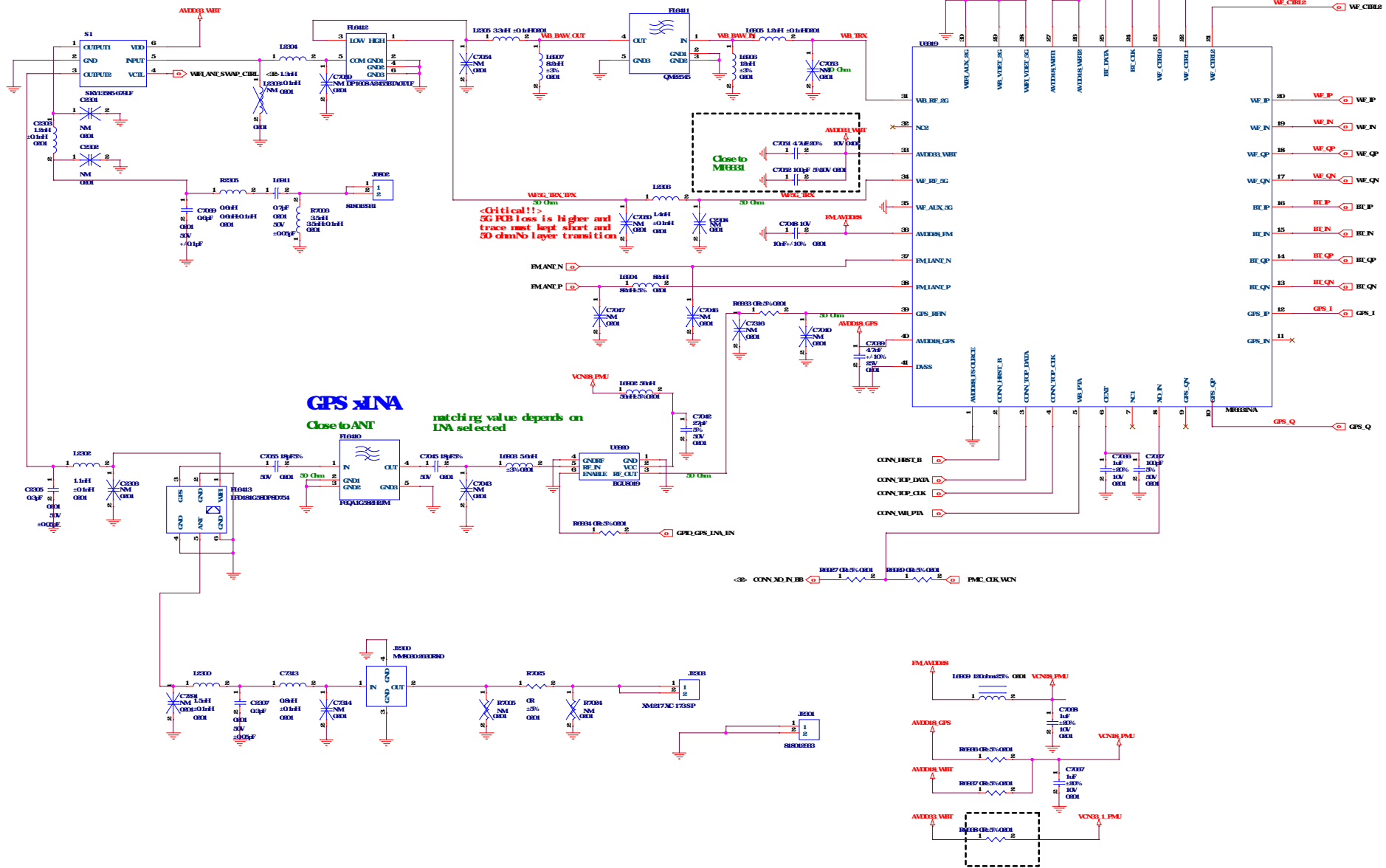
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<Critical !!>  
 SG PCB loss is higher and trace must kept short and 50 Ohm No layer transition

Close to M8631

GPS x LNA  
 Close to ANT  
 matching value depends on LNA selected

Note: For M8631, need to configure the VCN3\_2\_P1MU to 2.8V by EMC SW

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A3	Reserved(Gr-1)ghalRMJ	PO
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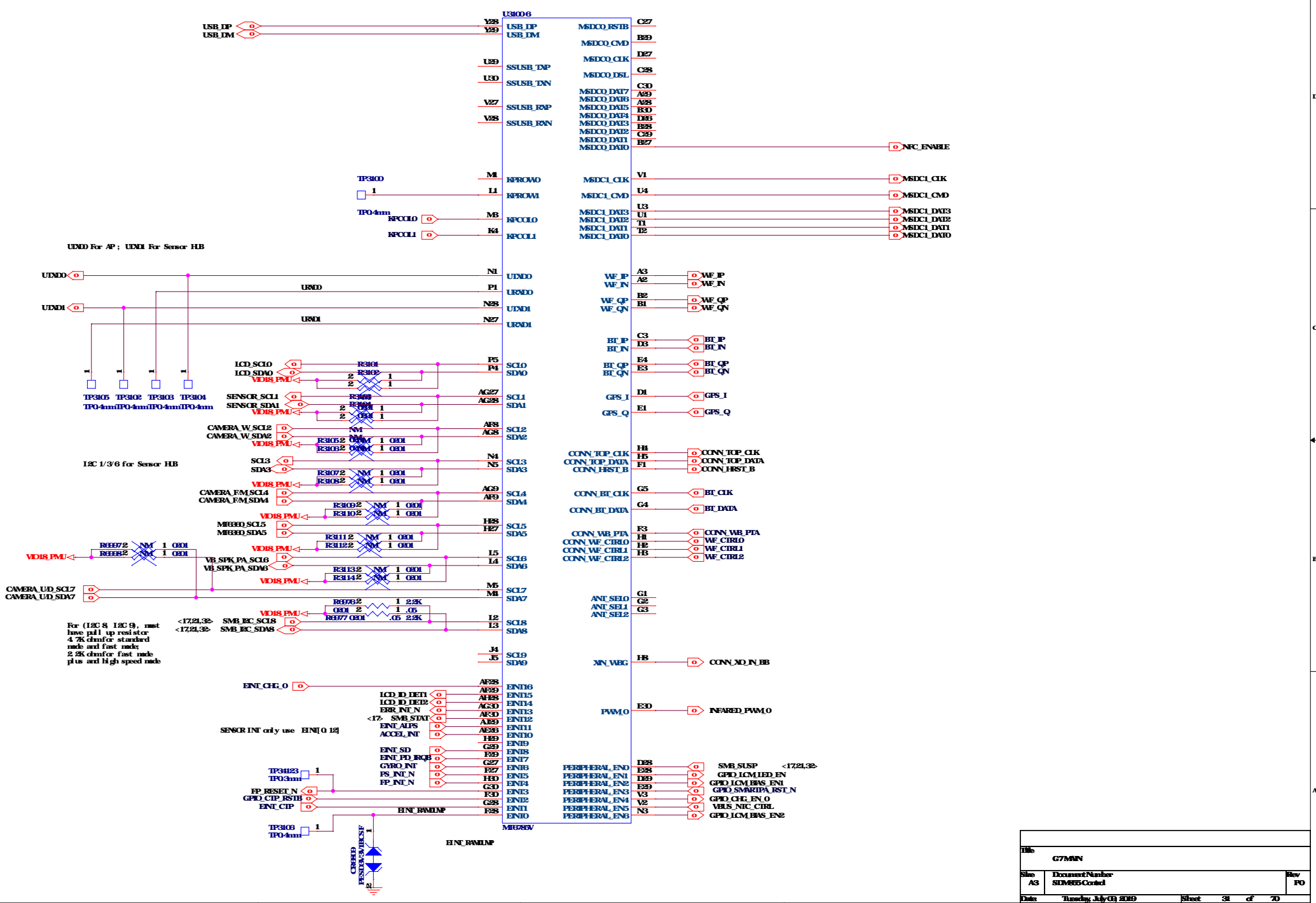
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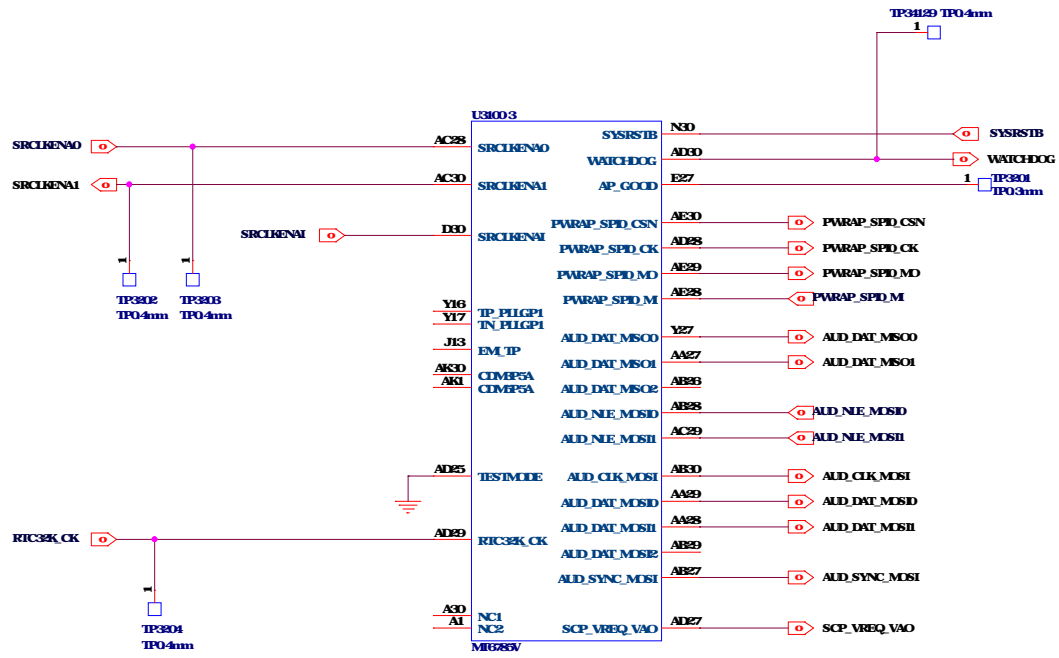
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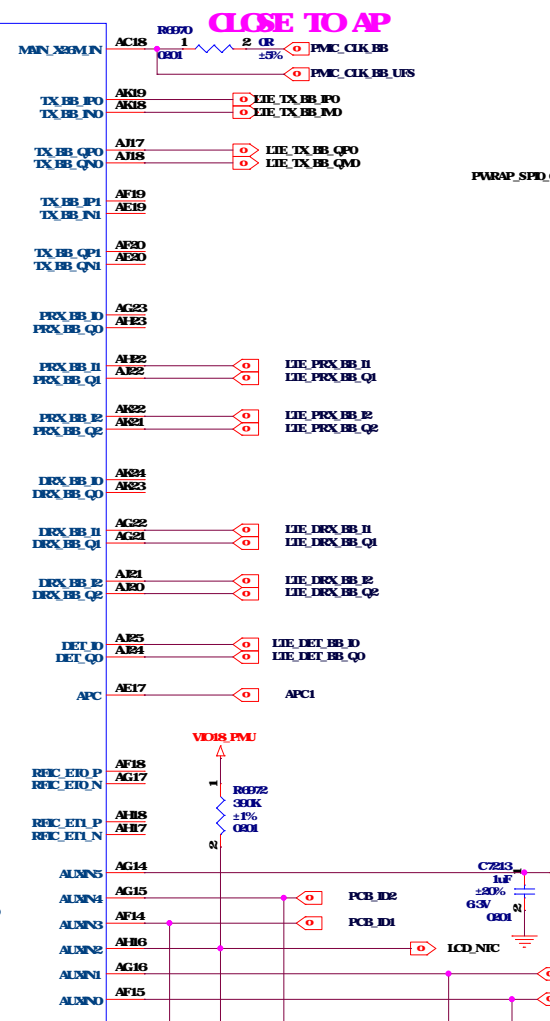
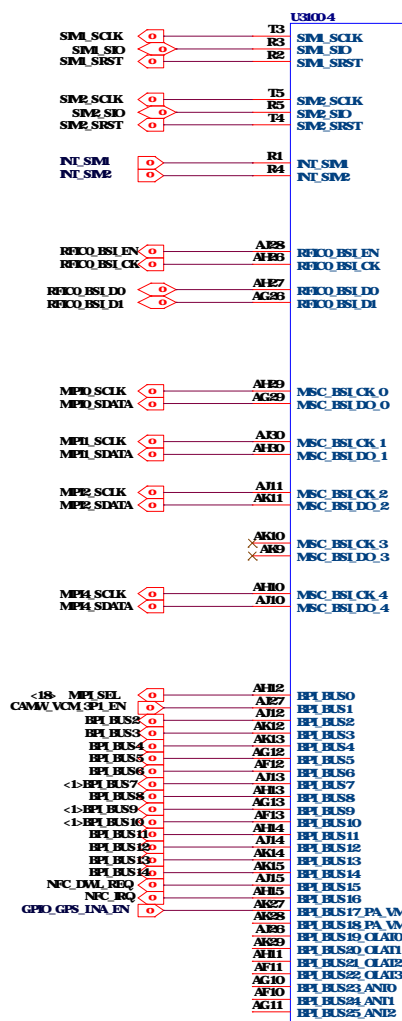
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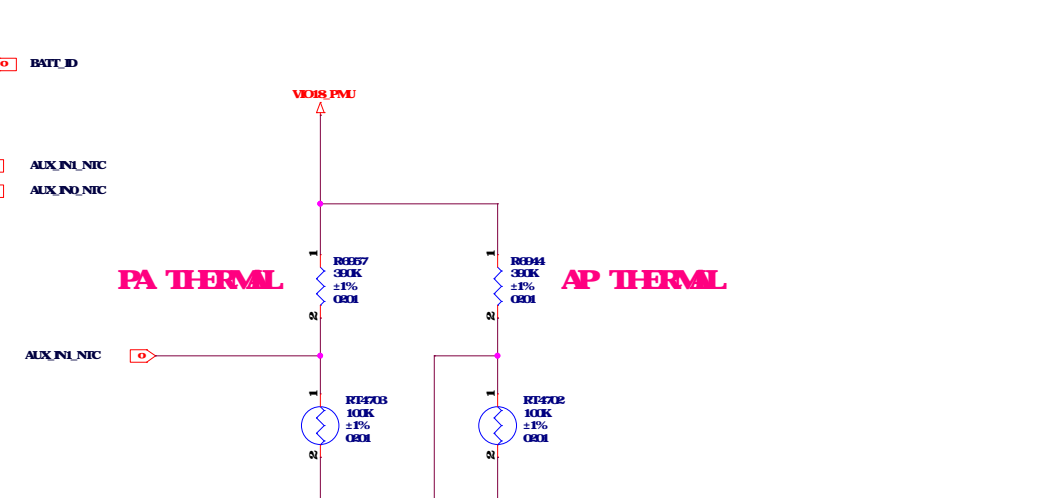
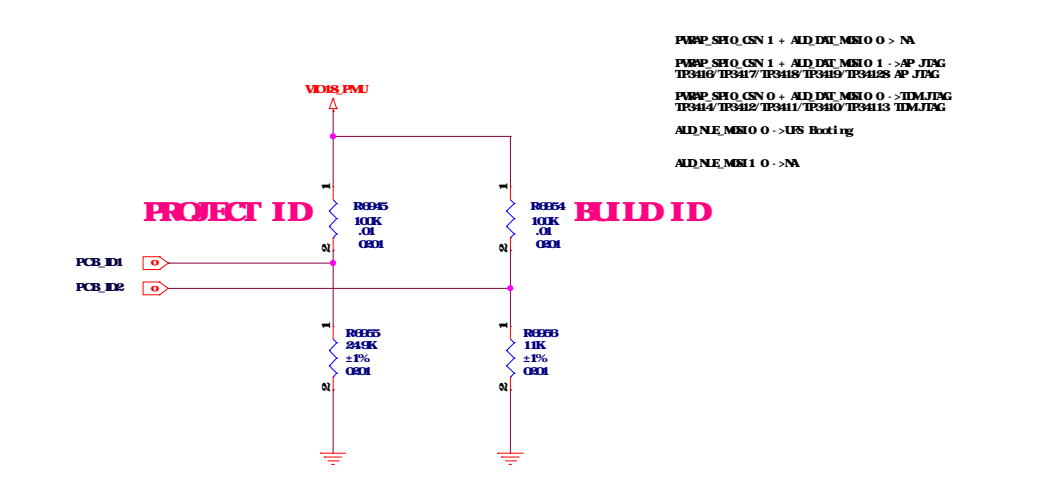
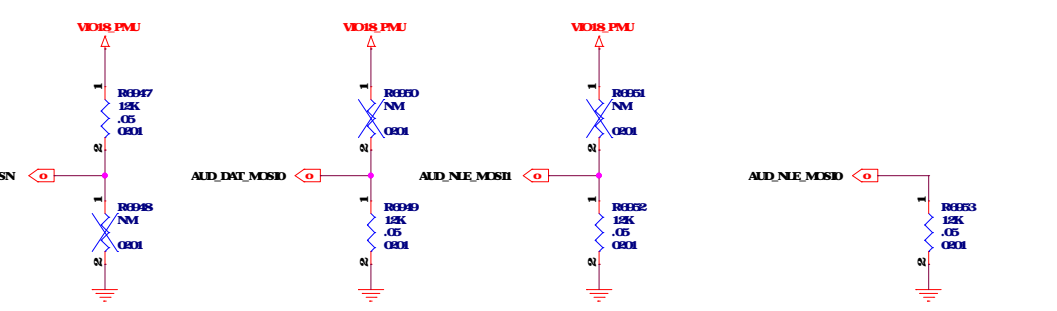
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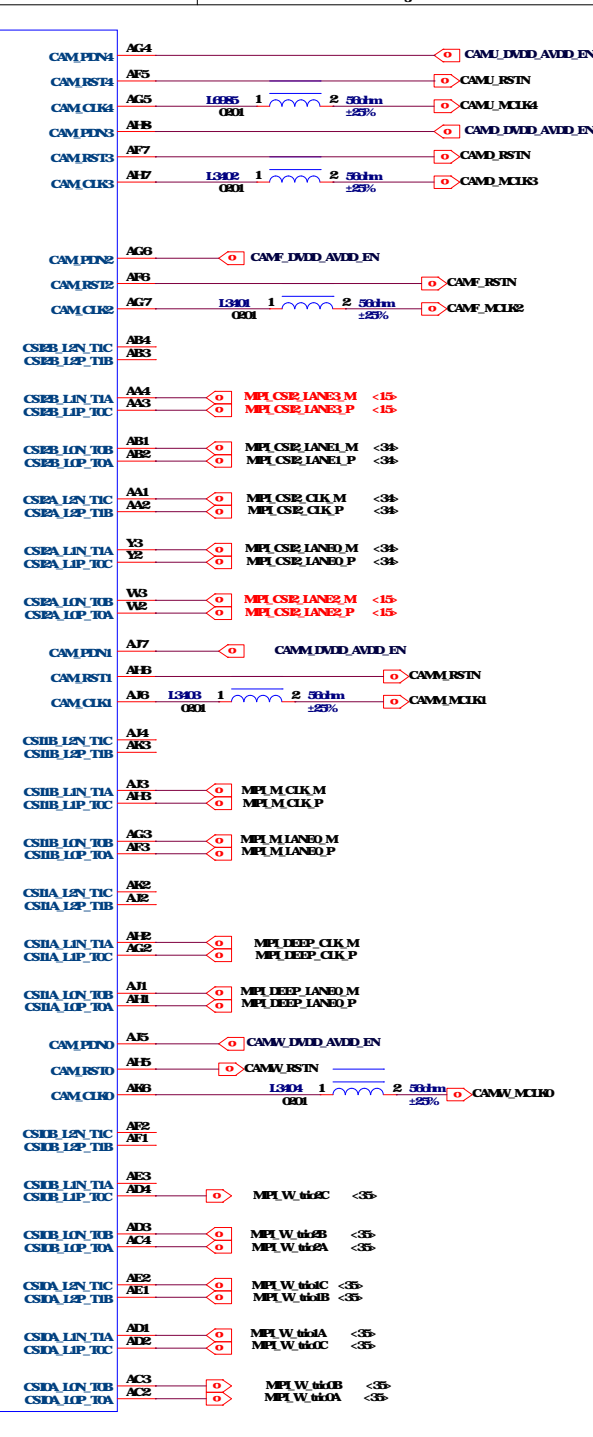
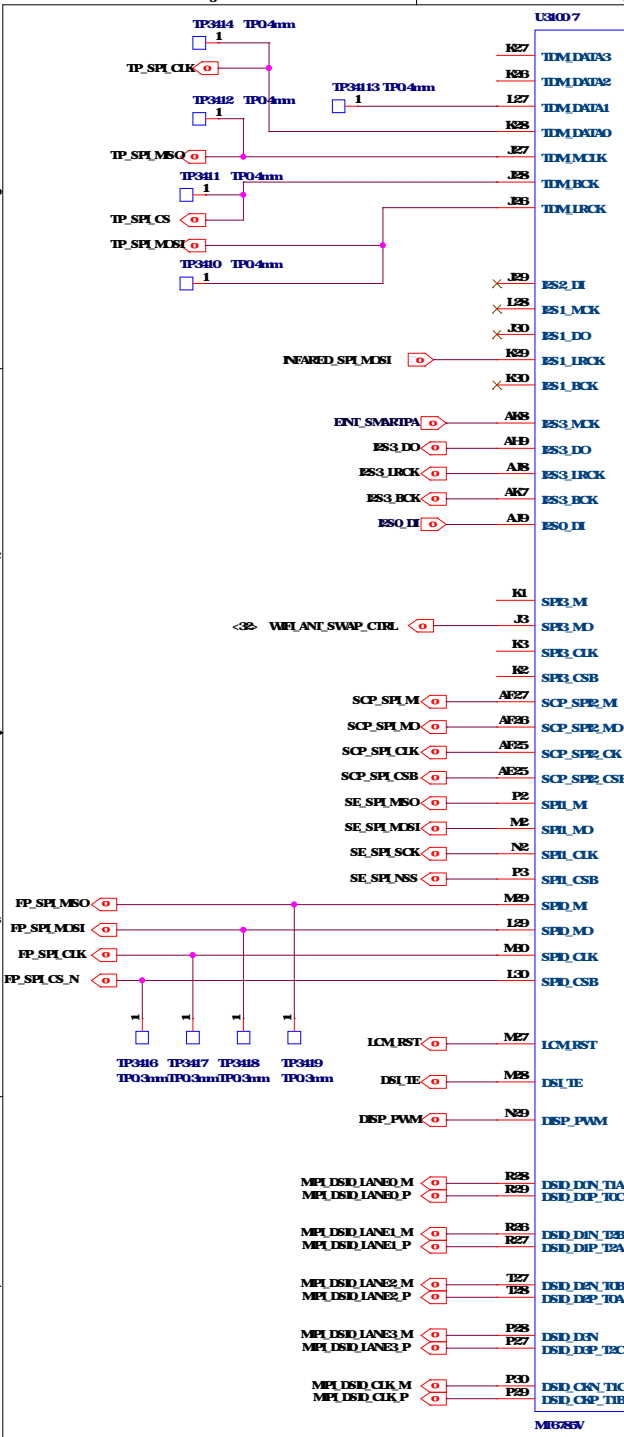
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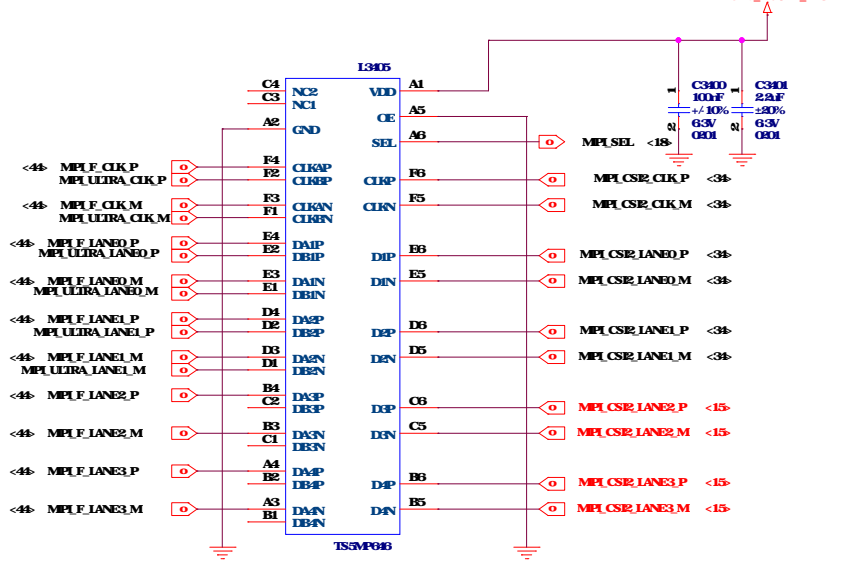
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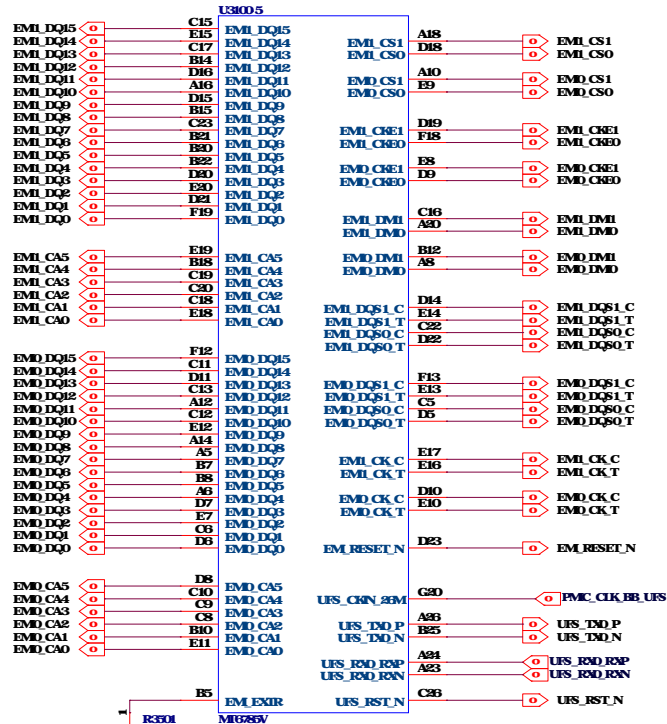
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### FRONT Ultra SWITCH



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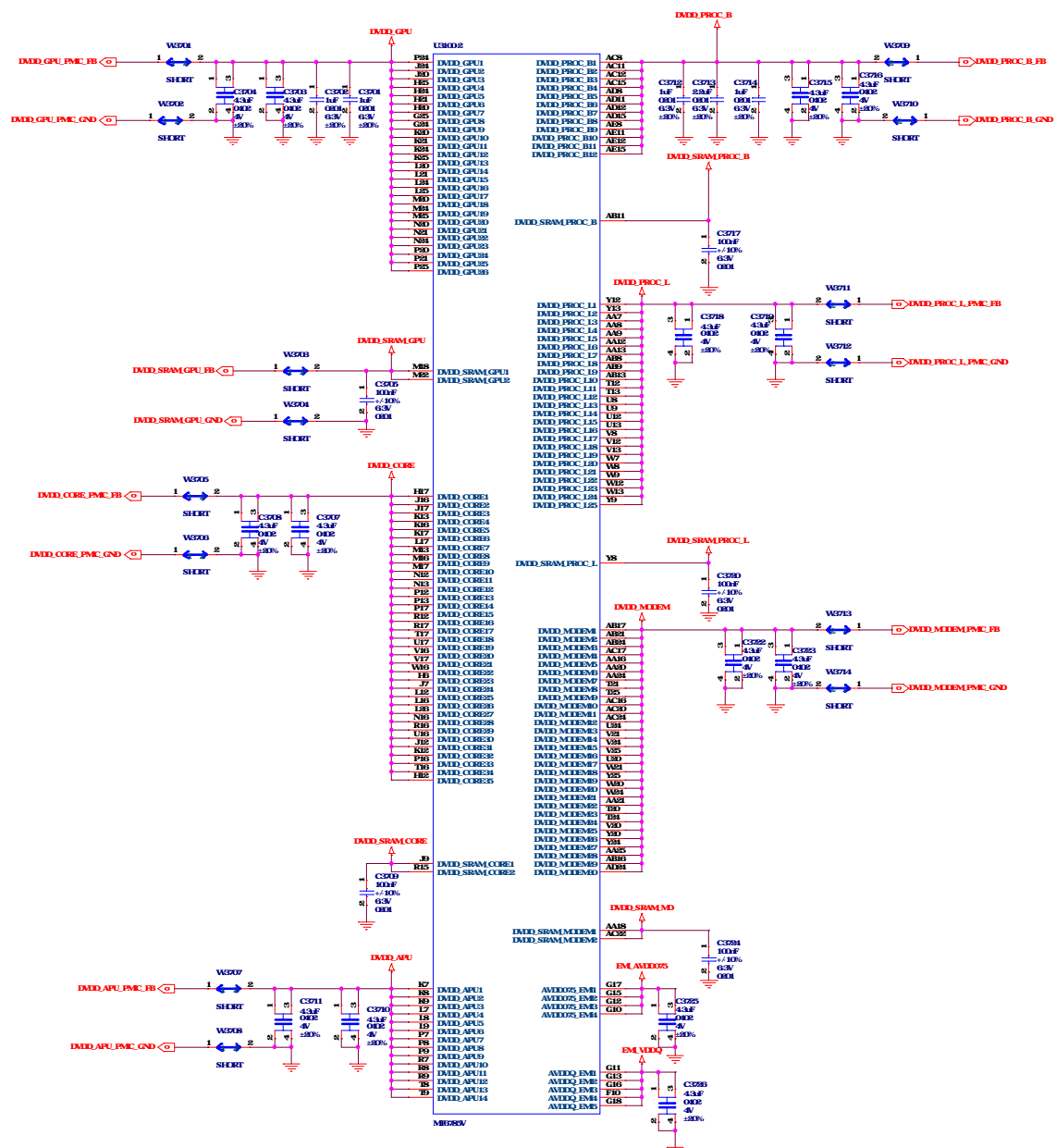
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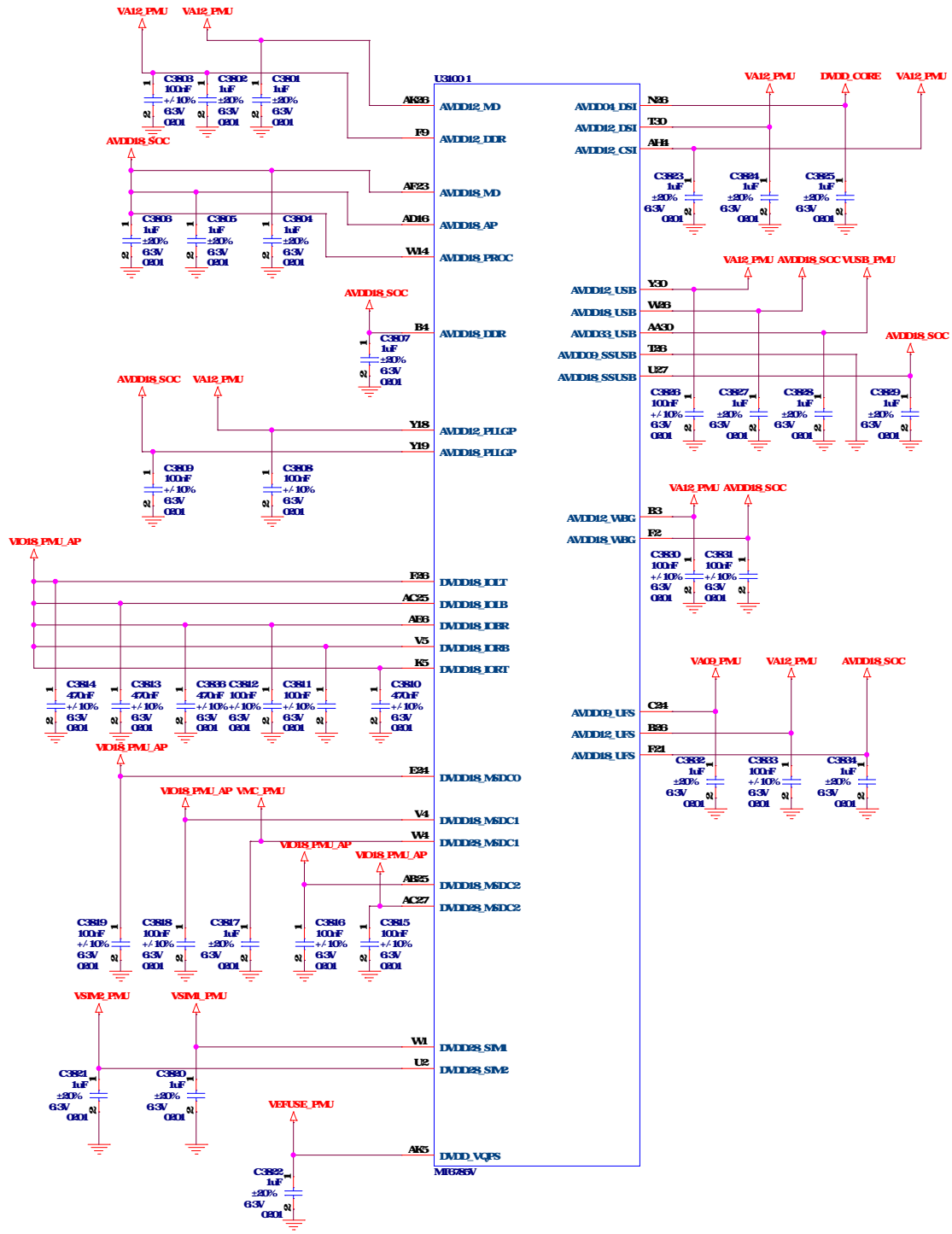
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A3	SDMRE5PM22	PO
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<b>Title</b>		
F1 MMN		
<b>Site</b>	<b>Document Number</b>	<b>Rev</b>
A3	SUMMER PAGES	PO
<b>Date</b>	Tuesday, July 04 2000	<b>Sheet</b> 39 of 70

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<b>Title</b>		
F1MN		
<b>Size</b>	<b>Document Number</b>	<b>Rev</b>
A4	SDM55PWR4	FO
<b>Date</b>	<b>Sheet</b>	<b>of</b>
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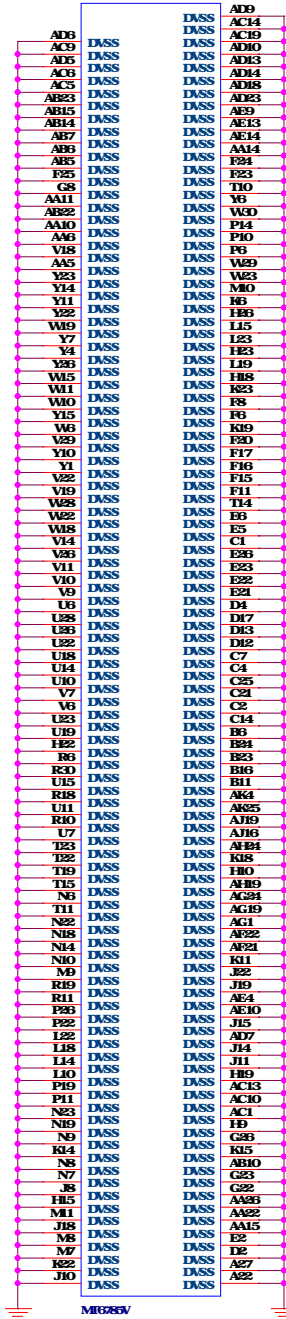
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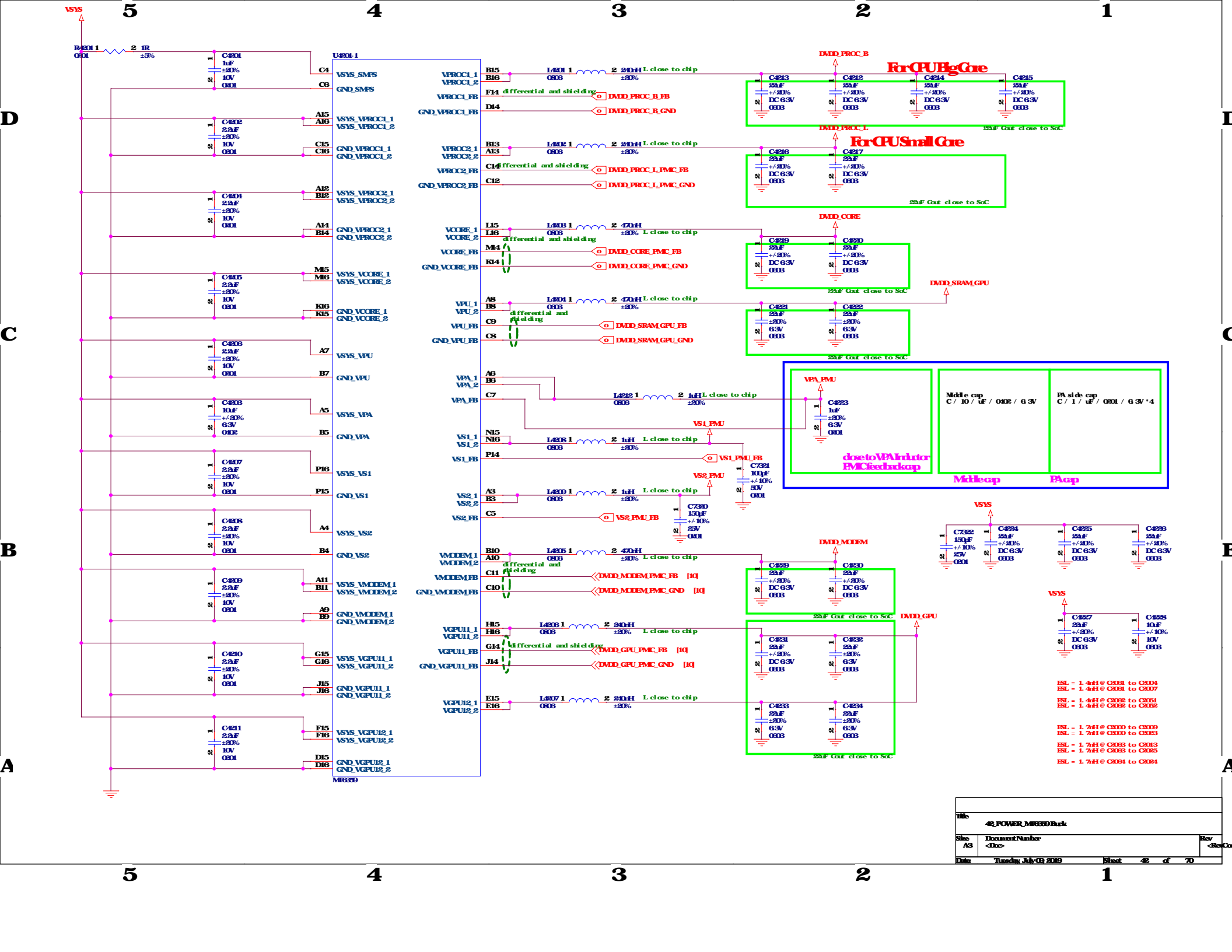
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U31008



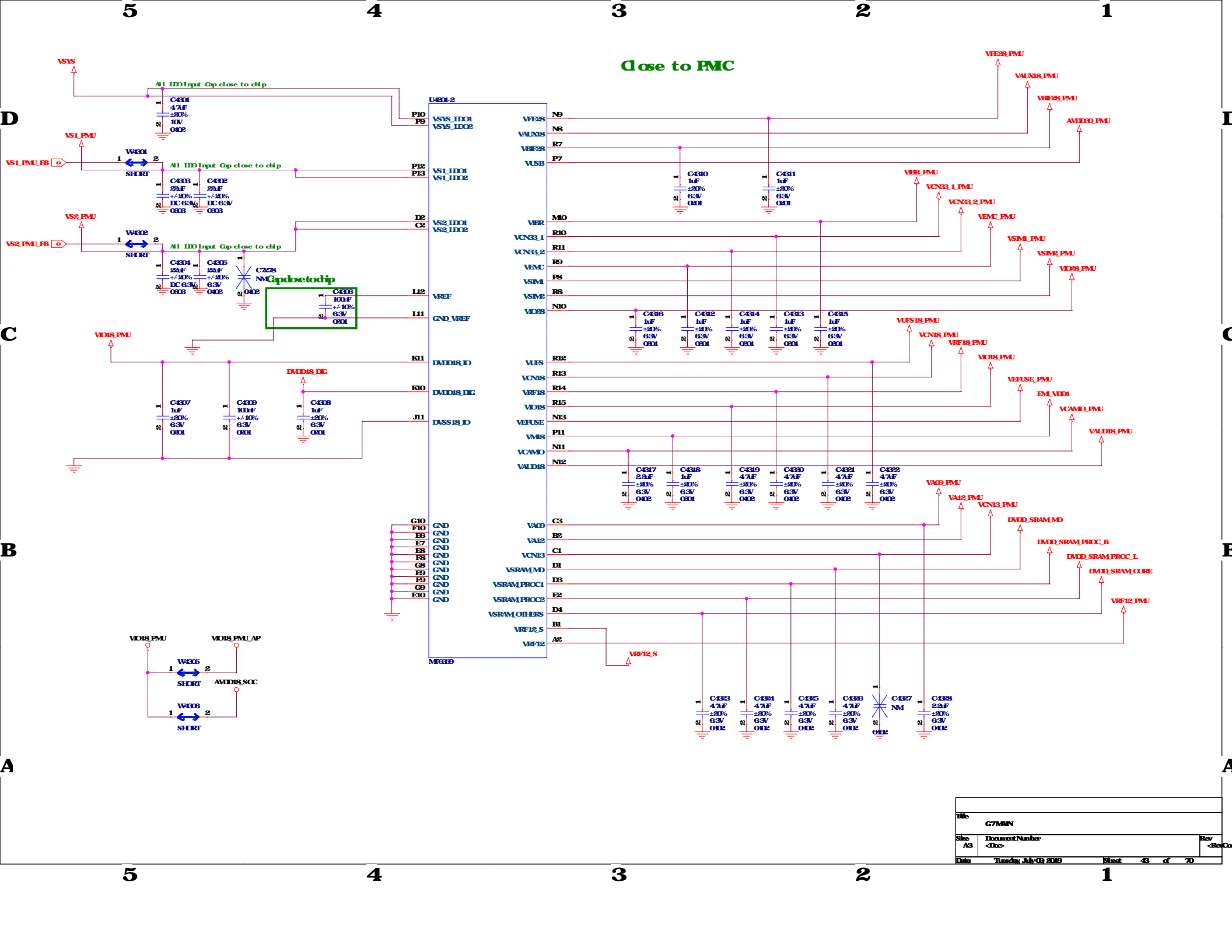
Title		
G77MMN		
Sheet	Document Number	Rev
A3	SUMREGND	PO
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Middle cap  
 $C / 10 / \mu F / 0402 / 6.3V$   
 PA side cap  
 $C / 1 / \mu F / 0801 / 6.3V \times 4$

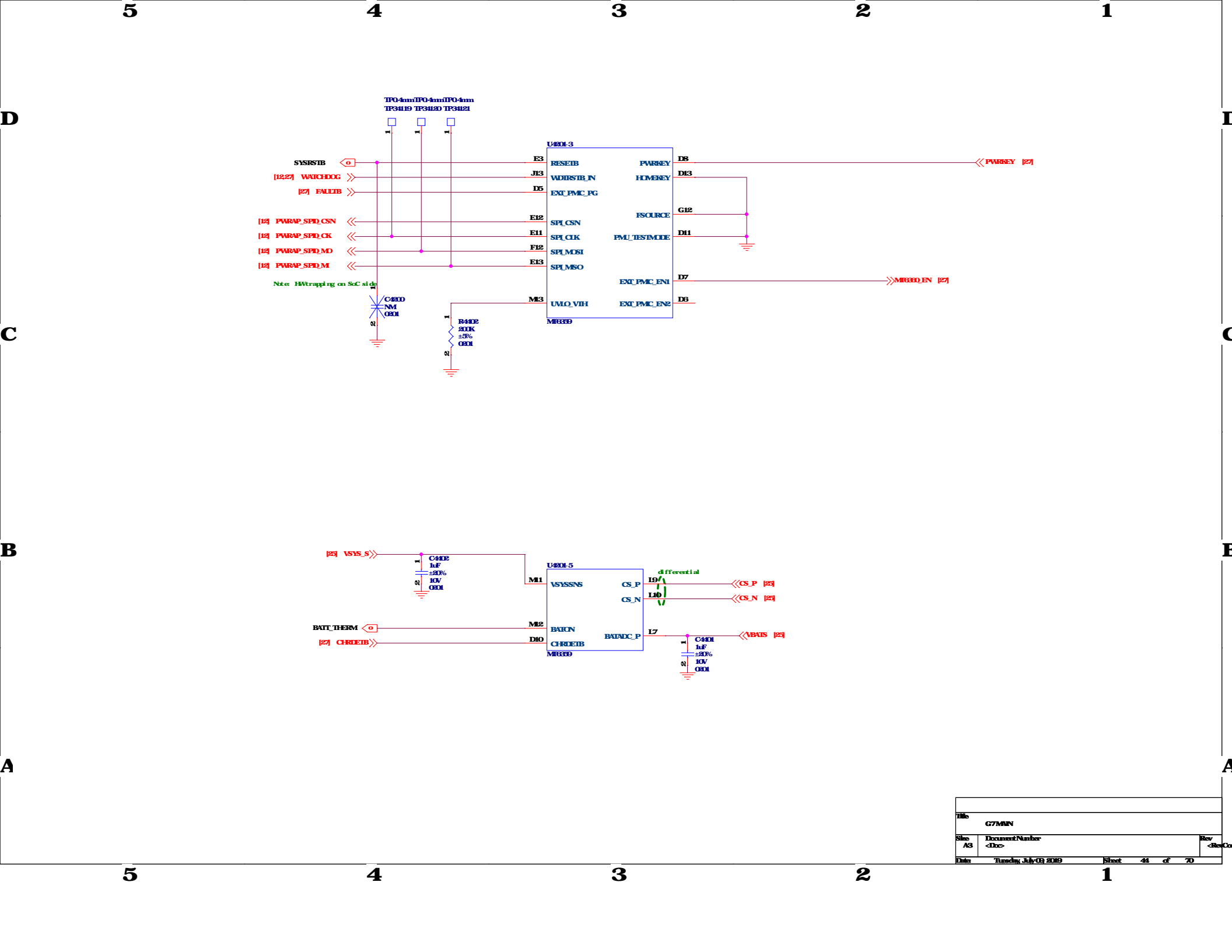
ESL = 1.4nH @ C204 to C204  
 ESL = 1.4nH @ C203 to C207  
 ESL = 1.4nH @ C202 to C201  
 ESL = 1.7nH @ C200 to C200  
 ESL = 1.7nH @ C200 to C203  
 ESL = 1.7nH @ C203 to C203  
 ESL = 1.7nH @ C203 to C205  
 ESL = 1.7nH @ C204 to C204

Title		
42 POWER_MIB889Bark		
Site	Document Number	Rev
AG	<Doc>	<Rev Co
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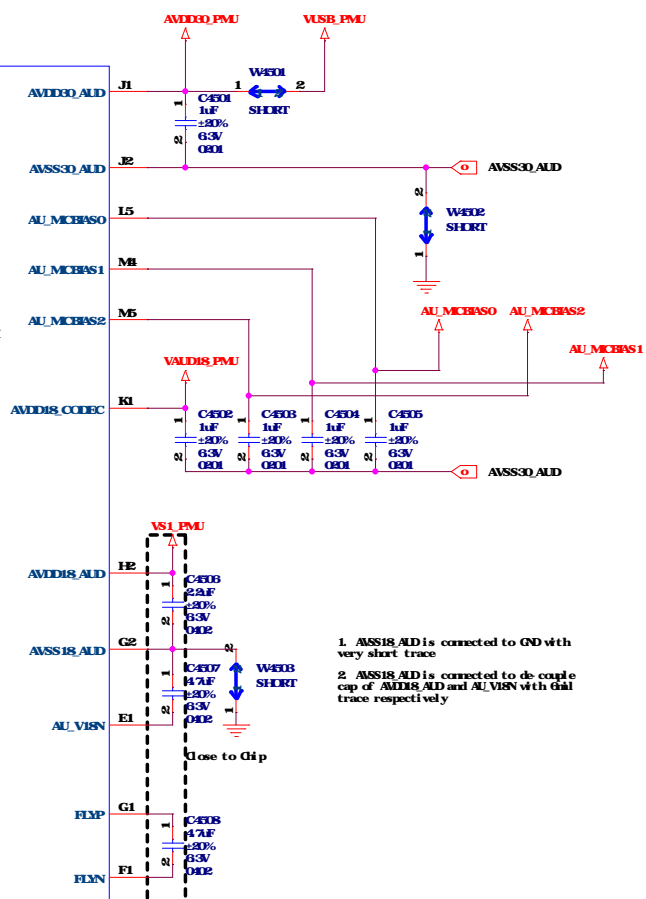
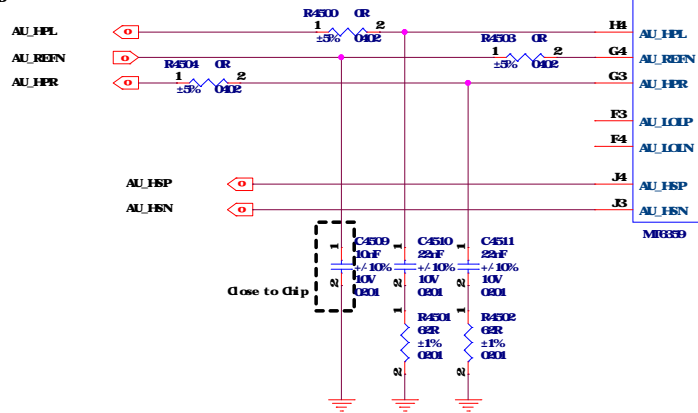
Close to FMC

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G7MMN		
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File			
G7MMN			
Site	Document Number	Rev	
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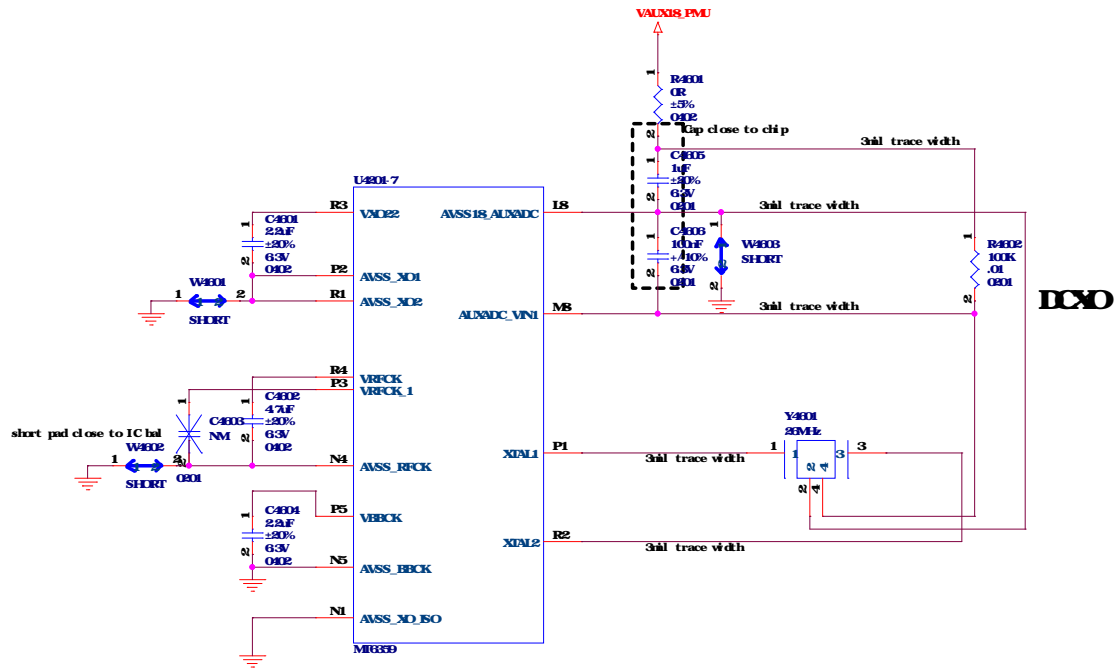
AU\_HPL and AU\_HPR should be routed as single end signal and be guarded by GND up and down, left and right respectively. The suggested layout pattern of AU\_HPL/AU\_HPR/AU\_REIN/AU\_REN is " GND/AU\_HPL/AU\_REIN/AU\_HPR/GND".



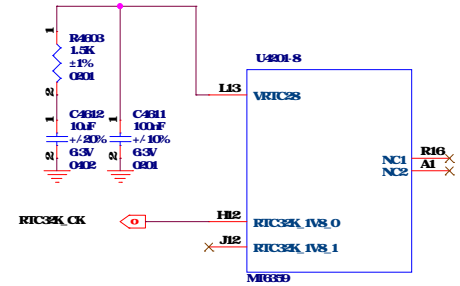
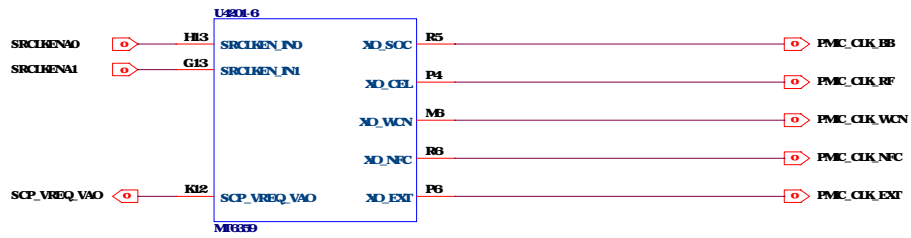
1. AVSS18\_ALD1 is connected to GND with very short trace
2. AVSS18\_ALD1 is connected to de couple cap of AVDD18\_ALD and AU\_V18N with Gnd trace respectively

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G77MMN		
Site	Document Number	Rev
A3	PM4853X00218L	PO
Date	Tuesday, July 04 2010	Sheet 45 of 70

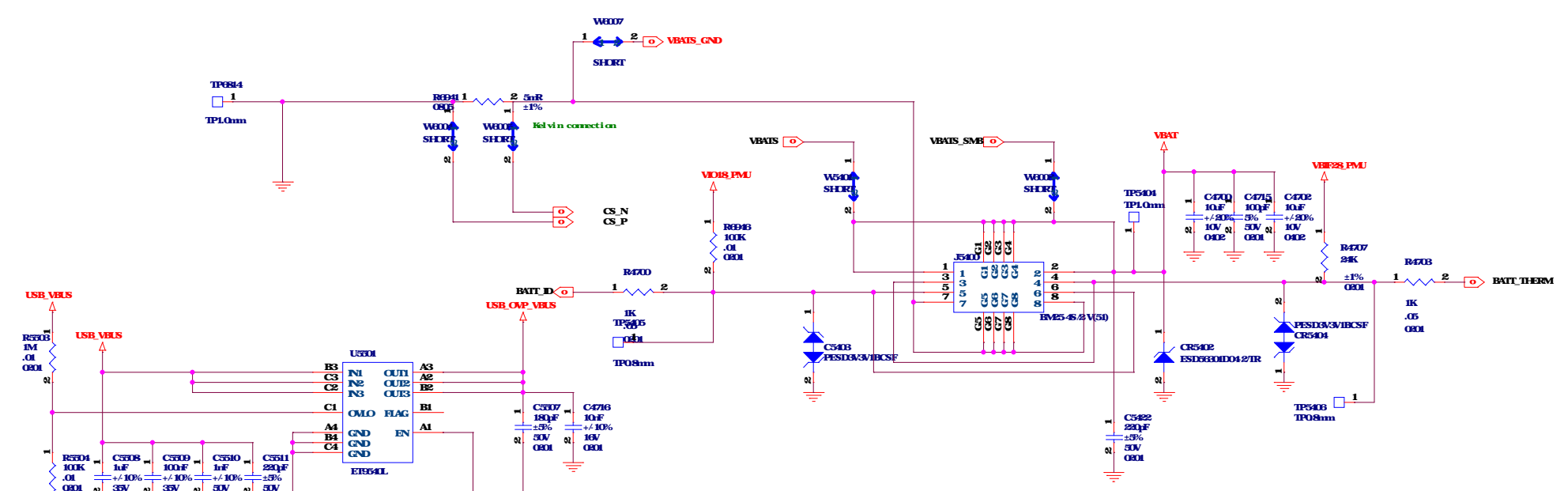
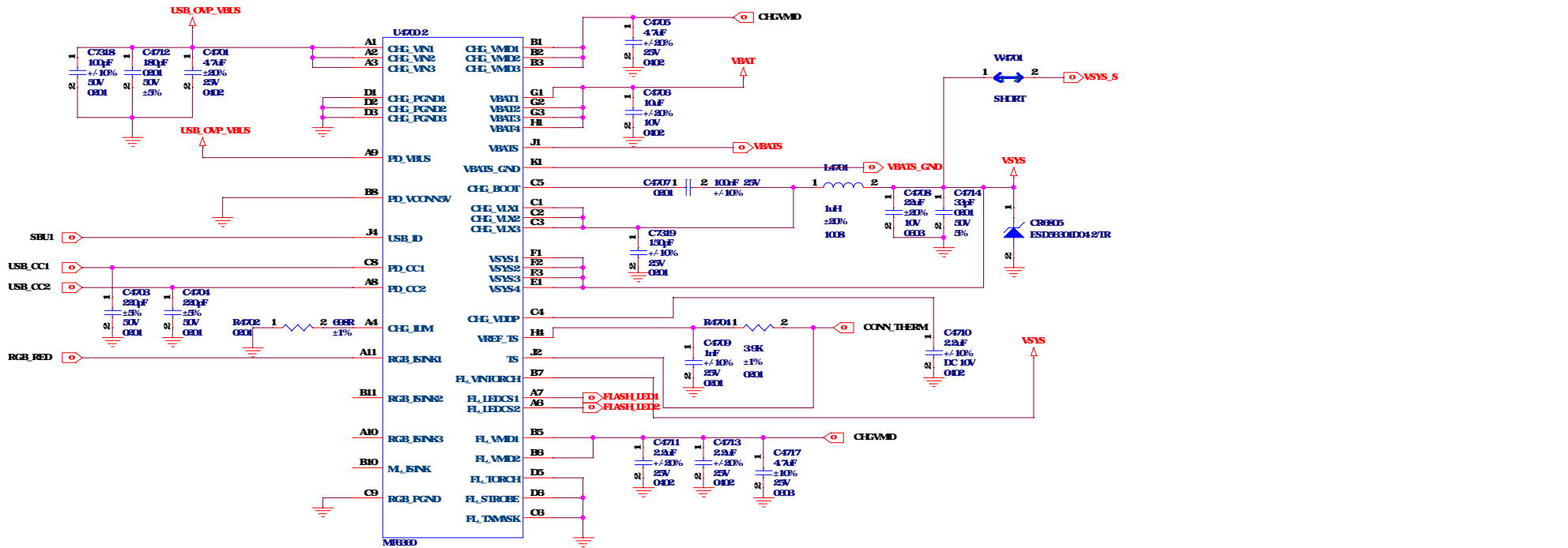




**DCO**



File: <b>G77MMN</b>		
Shee: <b>A3</b>	Document Number: <b>PM355BUCKCONVERTER1</b>	Rev: <b>P0</b>
Date: <b>Tuesday, July 09, 2008</b>	Sheet: <b>49</b>	of: <b>70</b>



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Site	Docuement Number	PM085A-CIR1.&GND
Rev	PO	
Date	Tuesday, July 08 2009	Sheet 47 of 70

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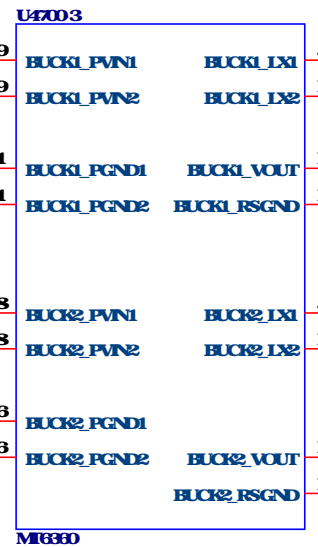
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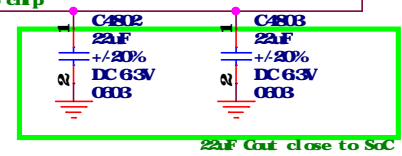
1

VSYS



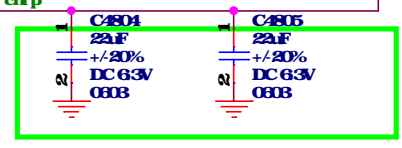
DVDD\_APU

L4801 1 2 330H ±20% L close to chip



EM\_VDD2

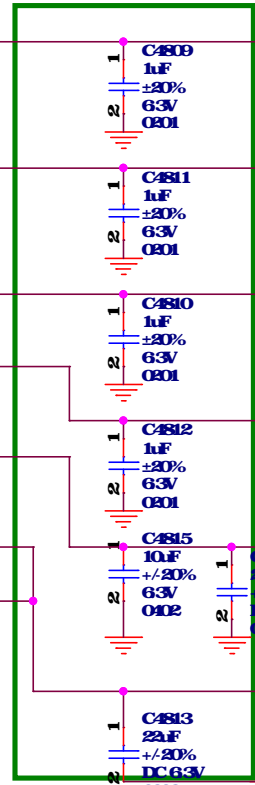
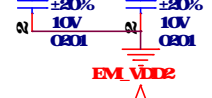
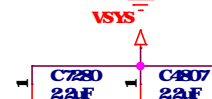
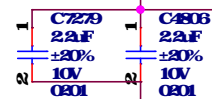
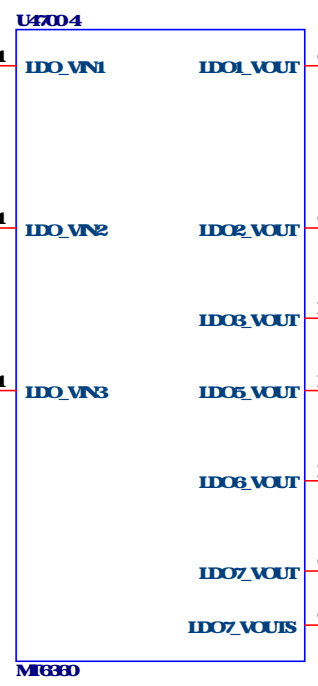
L4802 1 2 330H ±20% L close to chip



### Close to PMC

22uF Out close to SoC

VSYS



IDO1\_VOUT

CAMM\_VCM\_2PS

VMC\_PMU

VMCH\_PMU

EM\_AVDD075

EM\_VDDQ

IDO1	VEP,	150nA
IDO2	VIP,	200nA
IDO3	VMC,	200nA
IDO5	VMCH,	800nA
IDO6	VMDR,	300nA
IDO7	VDRAM2,	600nA

Title		
G7MAN		
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A4	MI6880BUCKCONVERTER1	FO
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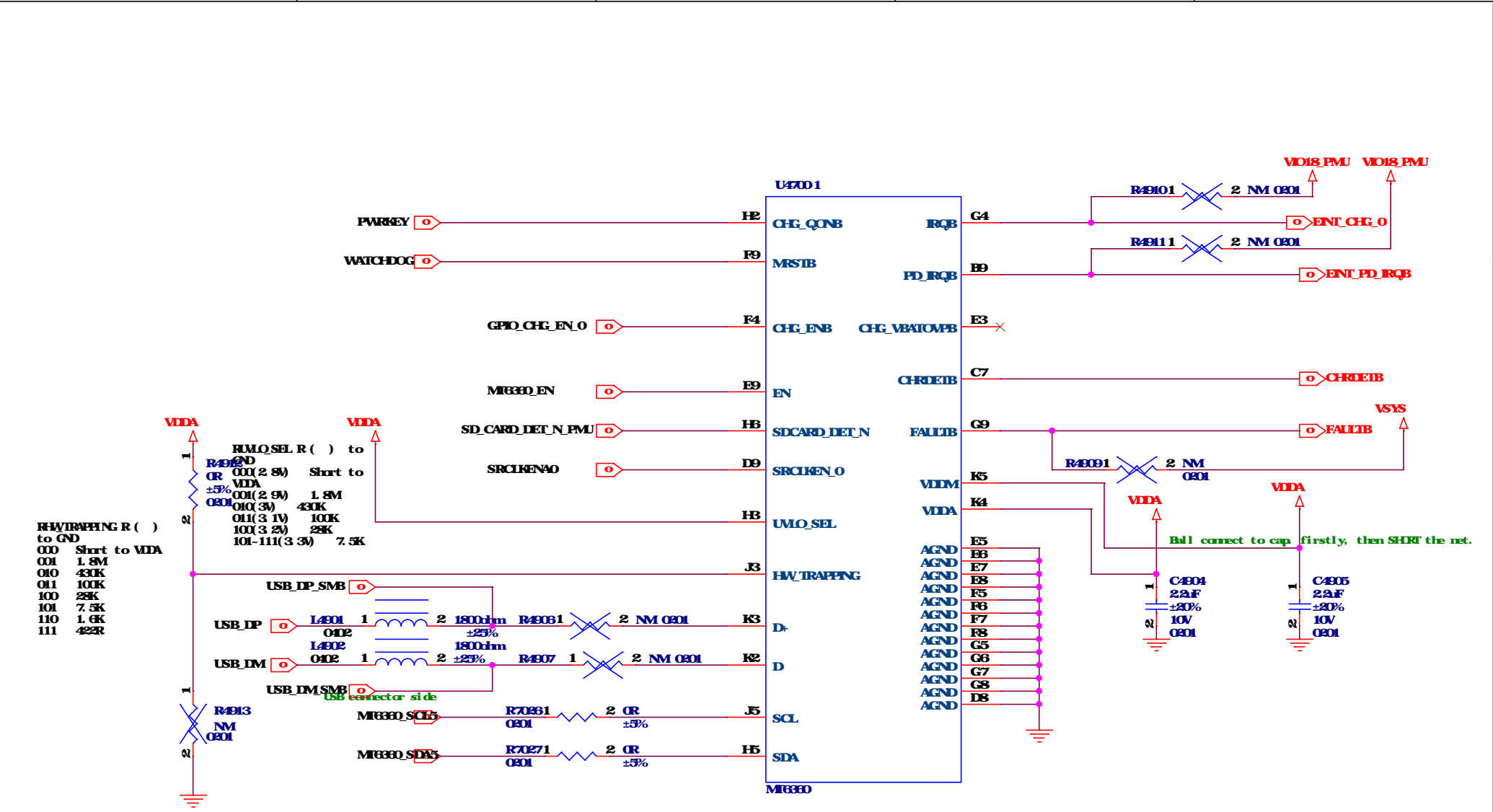
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Title		
G7MAN		
Size	Document Number	Rev
A4	PM655A BUCK CONVERTER2	FO
Date	Tuesday, July 09 2019	Sheet 49 of 70

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Title		
G77MMN		
Size	Document Number	Rev
A3	FMRESA.AMCI.HMBOB.FLASH	PO
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<b>Title</b>		
G7MAN		
<b>Size</b>	<b>Document Number</b>	<b>Rev</b>
A4	PM666ALDOs	FO
<b>Date</b>	<b>Sheet</b>	<b>of</b>
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<b>Title</b>			
G7MAN			
<b>Size</b>	<b>Document Number</b>	<b>Rev</b>	
A4	PM66BCIRLGND	FO	
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<b>Title</b>		
G7MAN		
<b>Size</b>	<b>Document Number</b>	<b>Rev</b>
A4	PM66B-FAPIC	FO
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<b>Title</b>		
G77MMN		
<b>Size</b>	<b>Document Number</b>	<b>Rev</b>
A3	FW6558 Change	PO
<b>Date</b>	Tuesday, July 13, 2010	<b>Sheet</b> 58 of 70

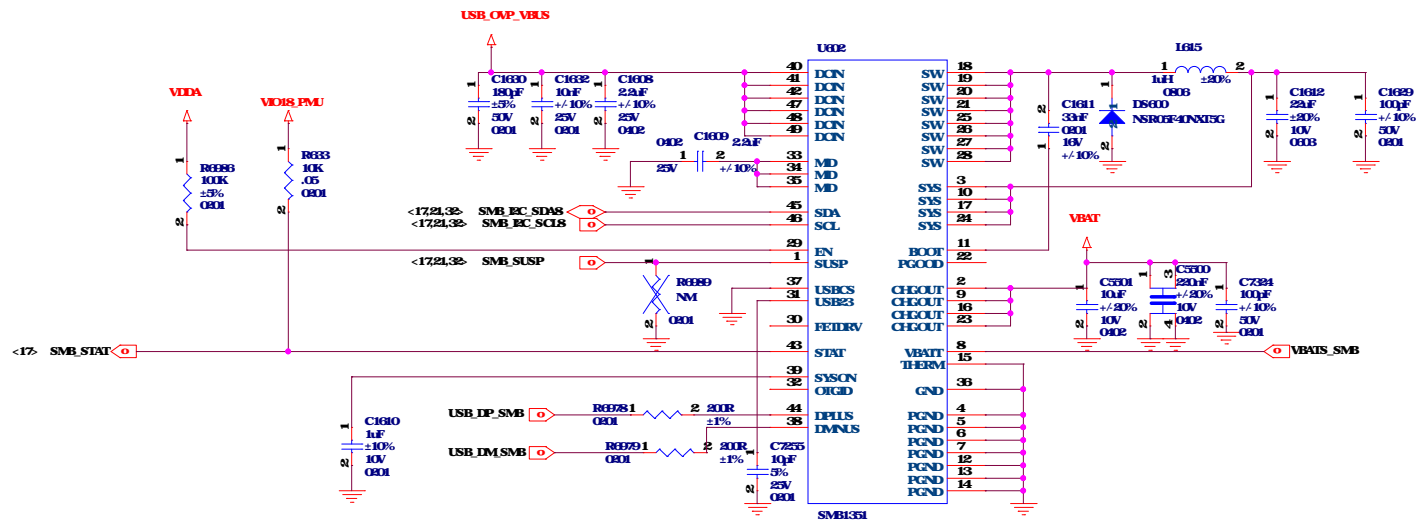
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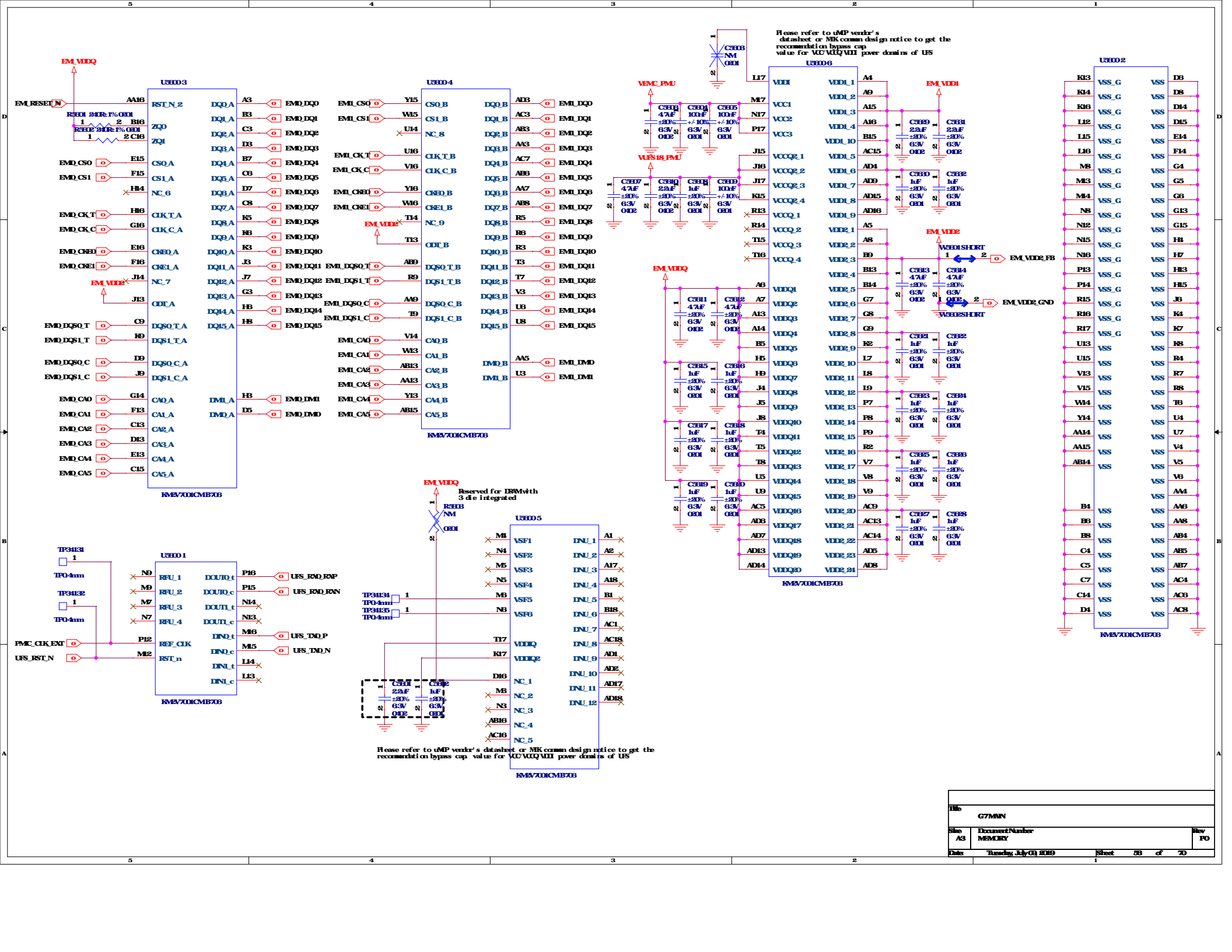
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Title		
G77MMN		
Sheet	Document Number	Rev
A3	SMB3E00V1.POWER	PO
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Please refer to uMP vendor's datasheet or MK custom design notice to get the recommendation bypass cap value for VCC/VCCQ/VDD1 power domains of US

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<b>Title</b>		
G7MAN		
<b>Size</b>	<b>Document Number</b>	<b>Rev</b>
A4	MEMORY-2	FO
<b>Date</b>	<b>Sheet</b>	<b>of</b>
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Title		
G77MMN		
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A3	WCEBHD 1	PO
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Title		
G77MMN		
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A3	WCEBHD2	PO
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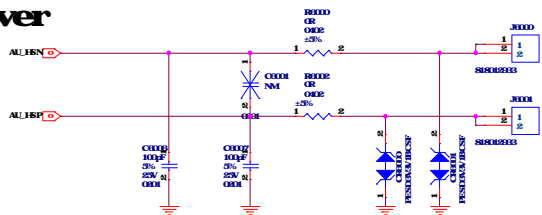
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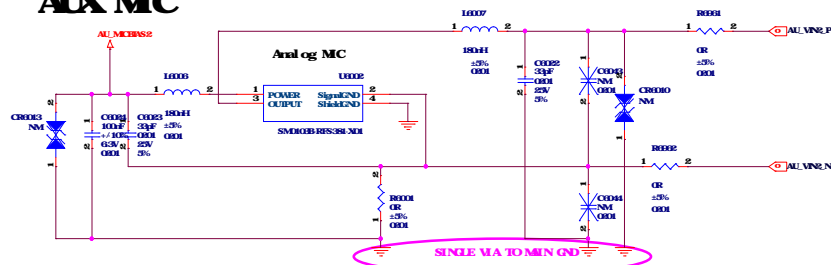
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# Recei ver

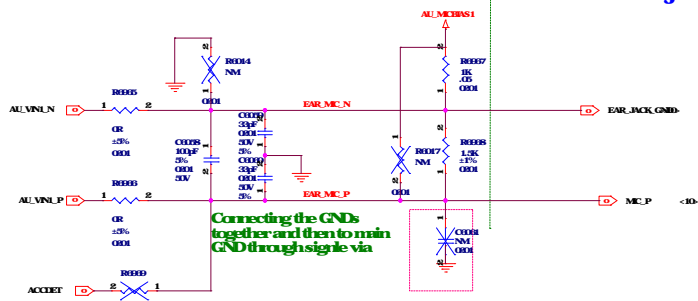


# AUX MIC

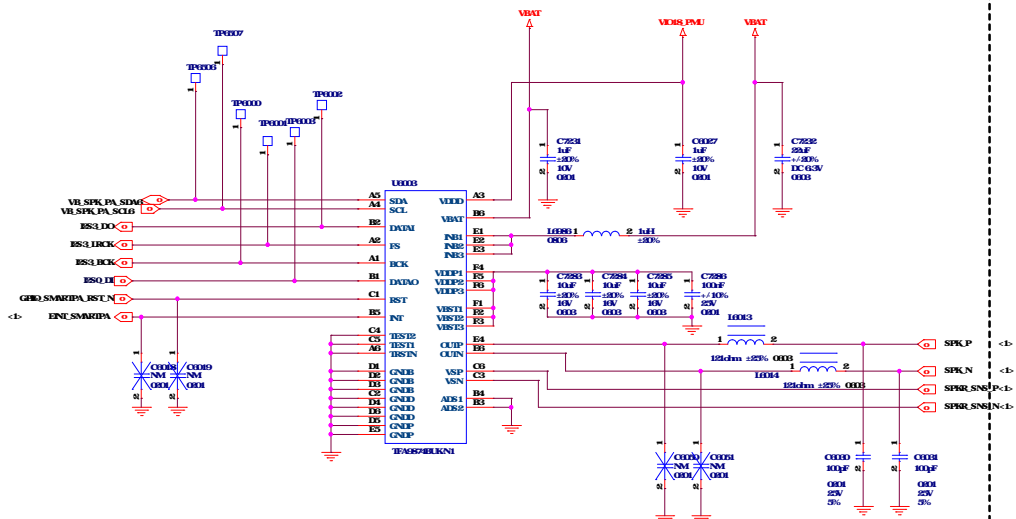


# Earphone Microphone

Close to FMC Close to aux o jack

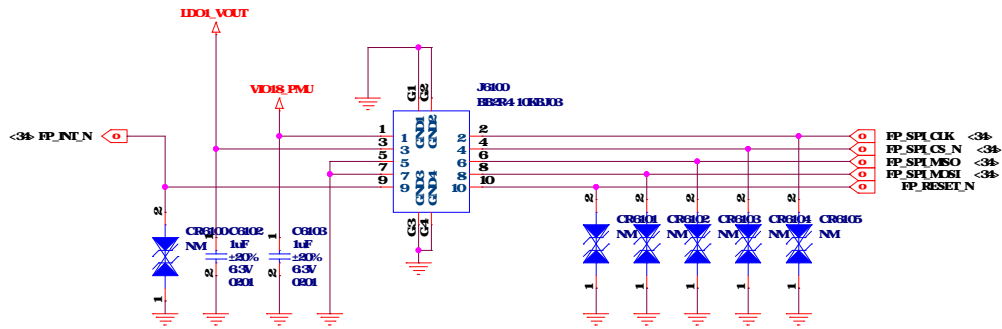


# SPKR PA

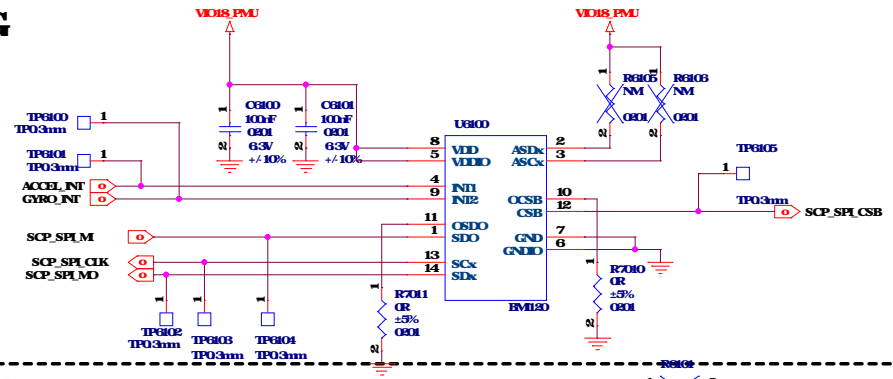


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As	Revision (to ALDM)		
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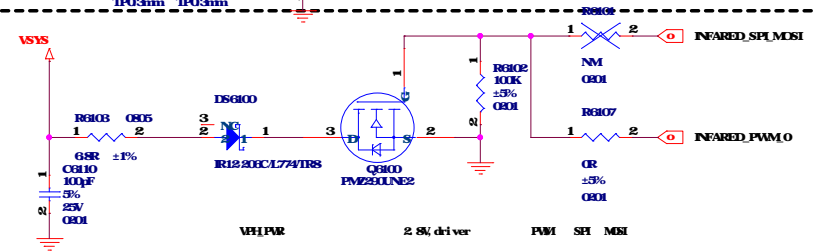
# FP



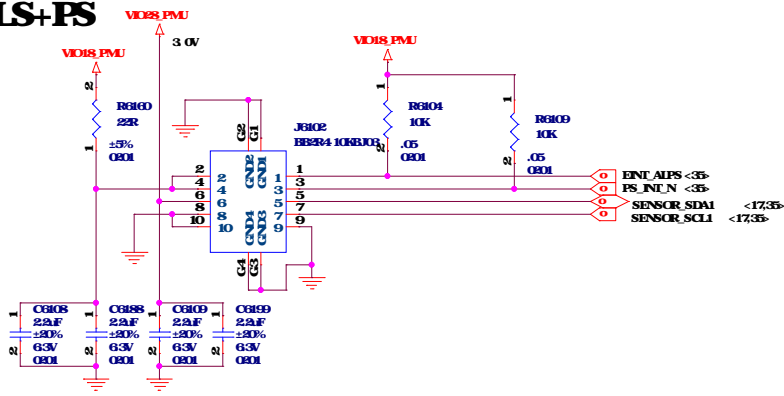
# A+G



# INFRARED



# ALS+PS



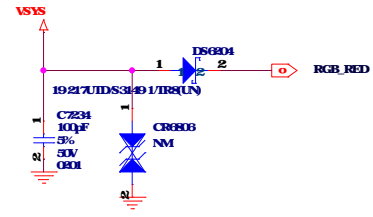
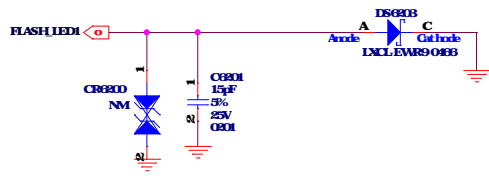
# COMPASS



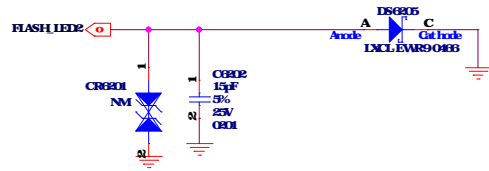
Title	G7MMN	
Site	Drawn/Number	Rev
A3	SENSOR	PO
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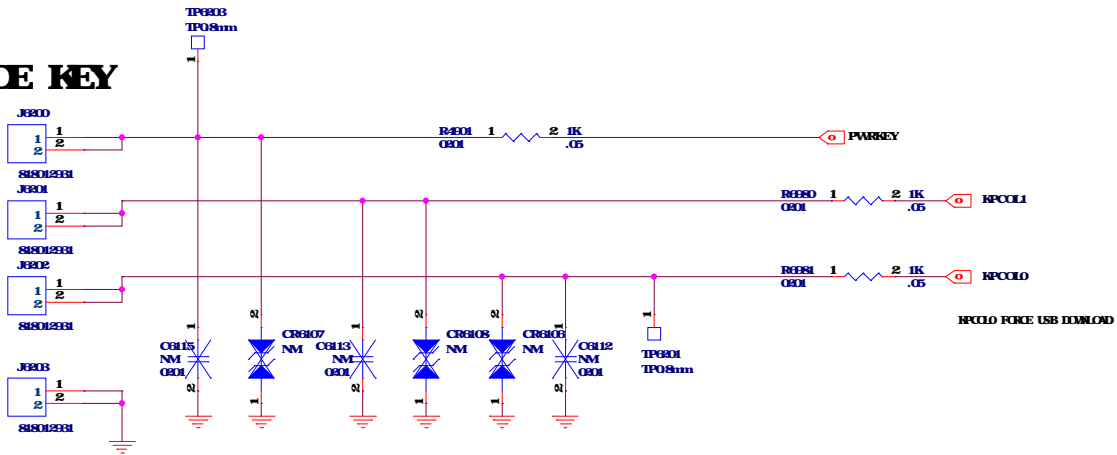
# Flash



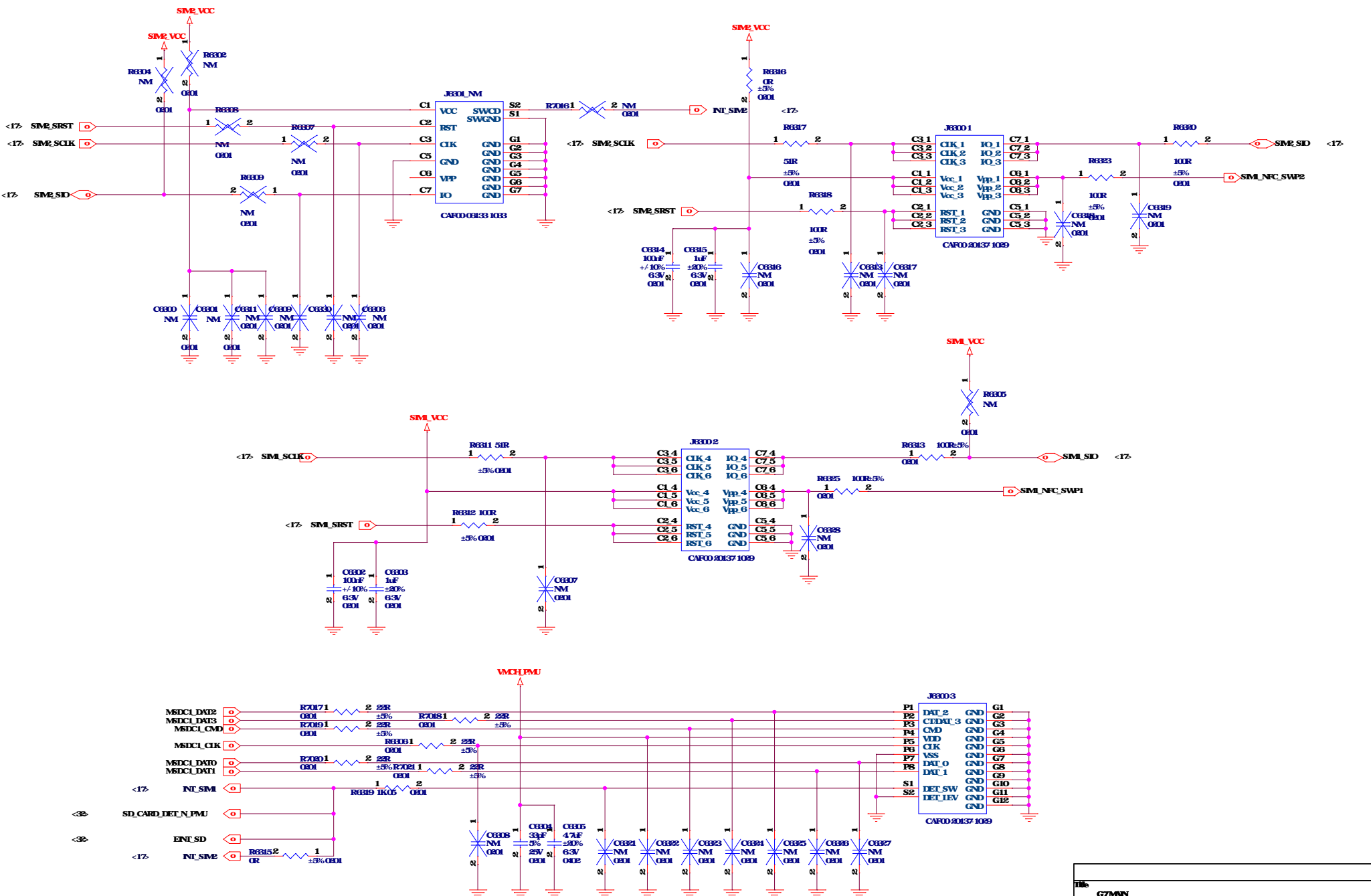
# Flash2



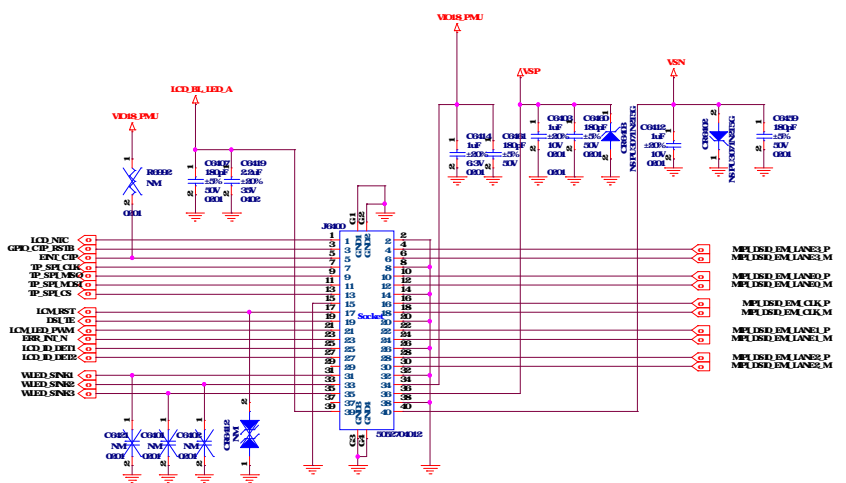
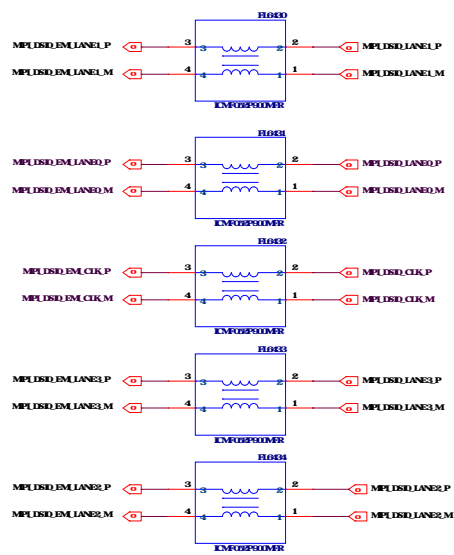
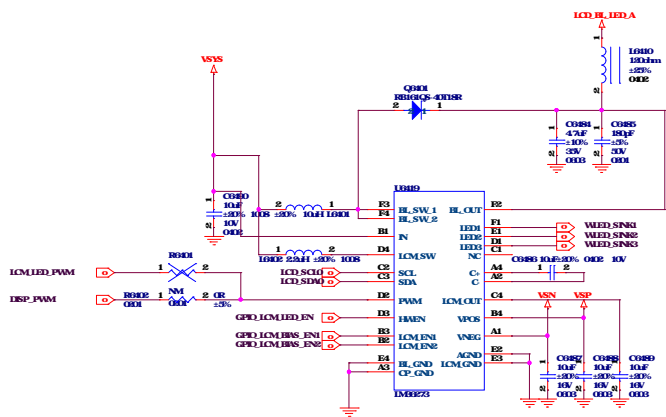
# SIDE KEY



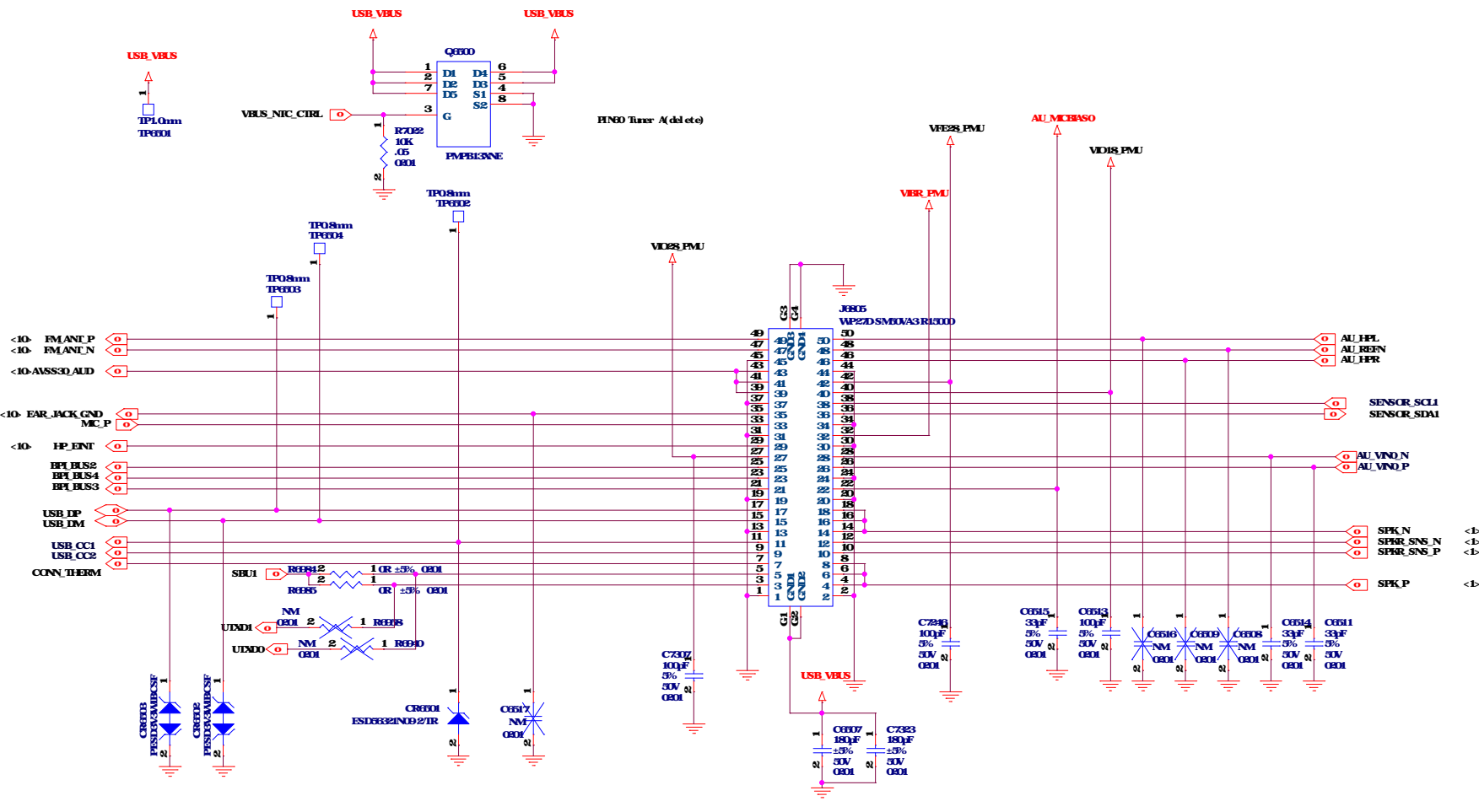
Title		
G7MMN		
Size	Document Number	Rev
A3	SENSOR.2	PO
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Title		G7/MIN
Size	Drawn/Number	Rev
A3	SIMCONNECTOR	PO
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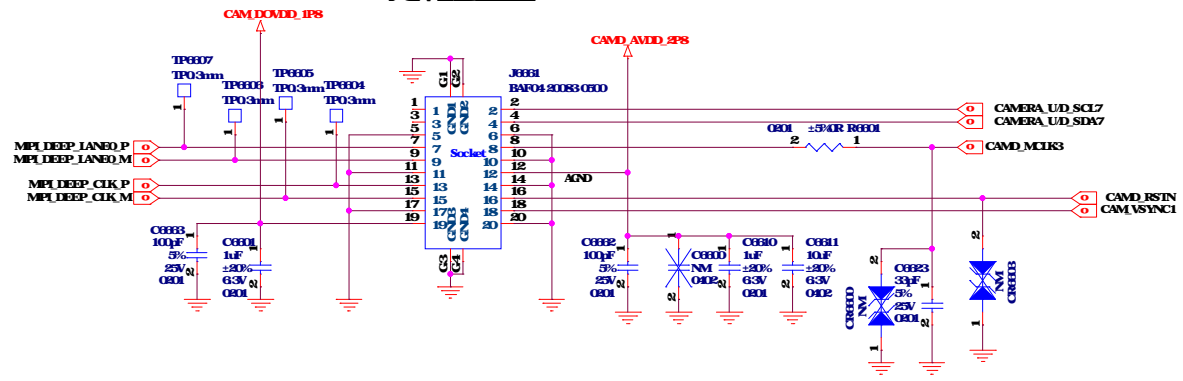


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Doc	Document Number AMC000	Sheet	04 of 20
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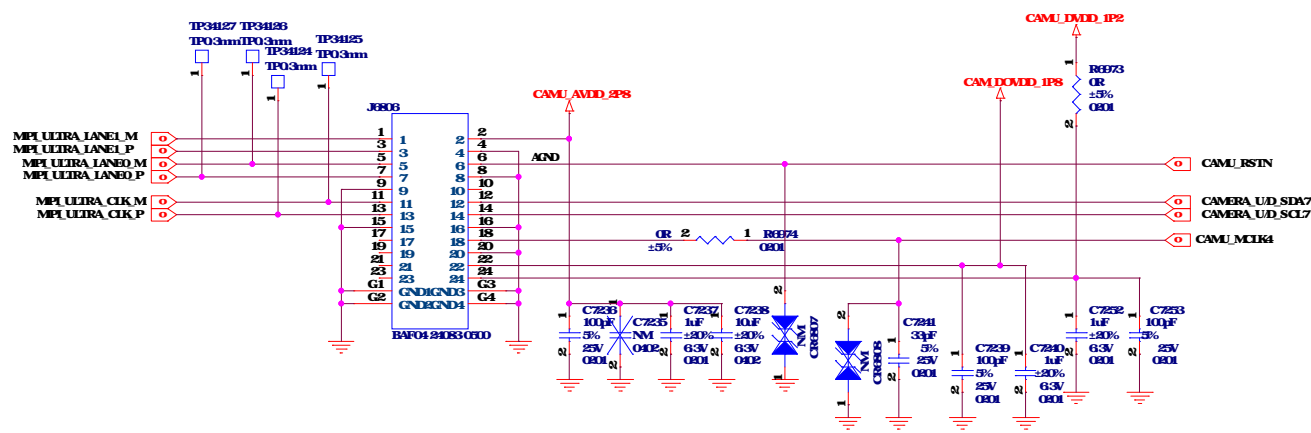


Title		
G77MIN		
Sheet	Drawn/Number	Rev
A3	HJB	PO
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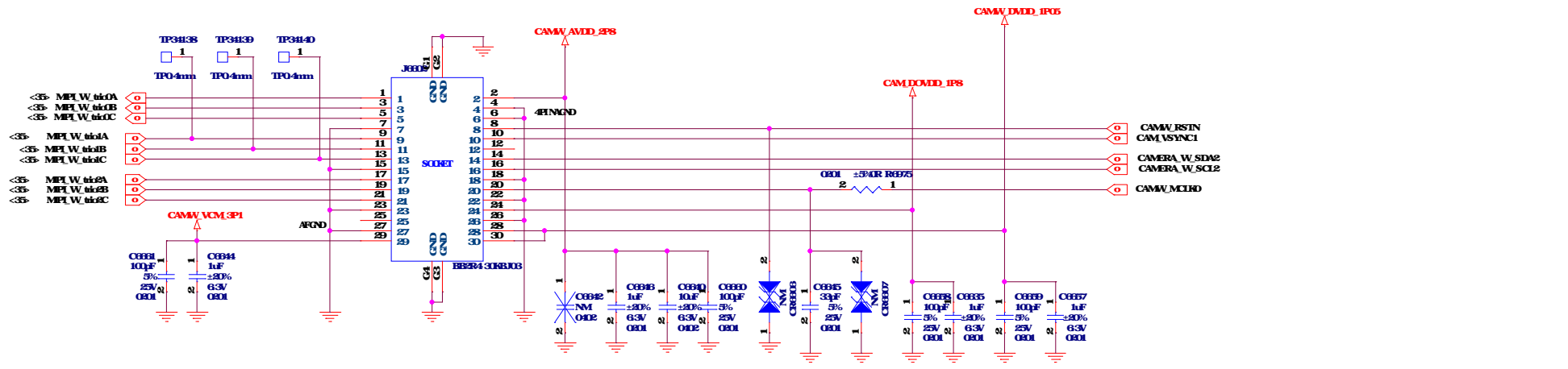
# 2MDEEP



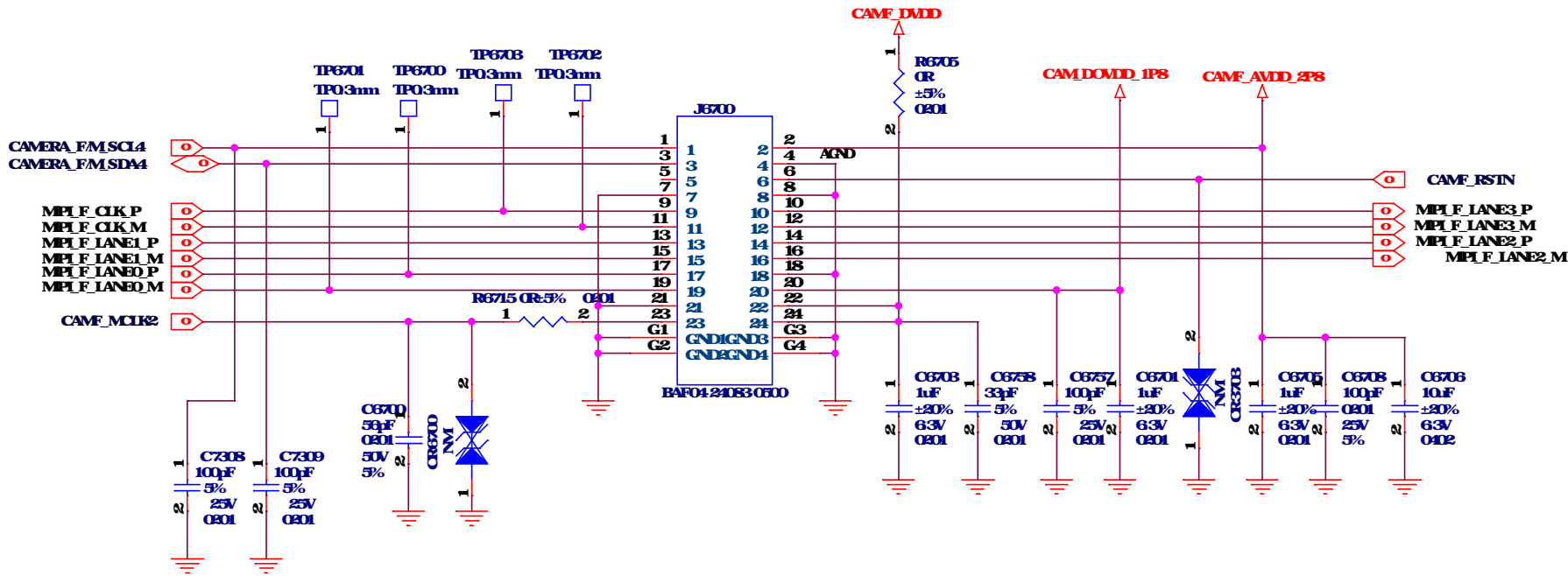
# 8MULTRA



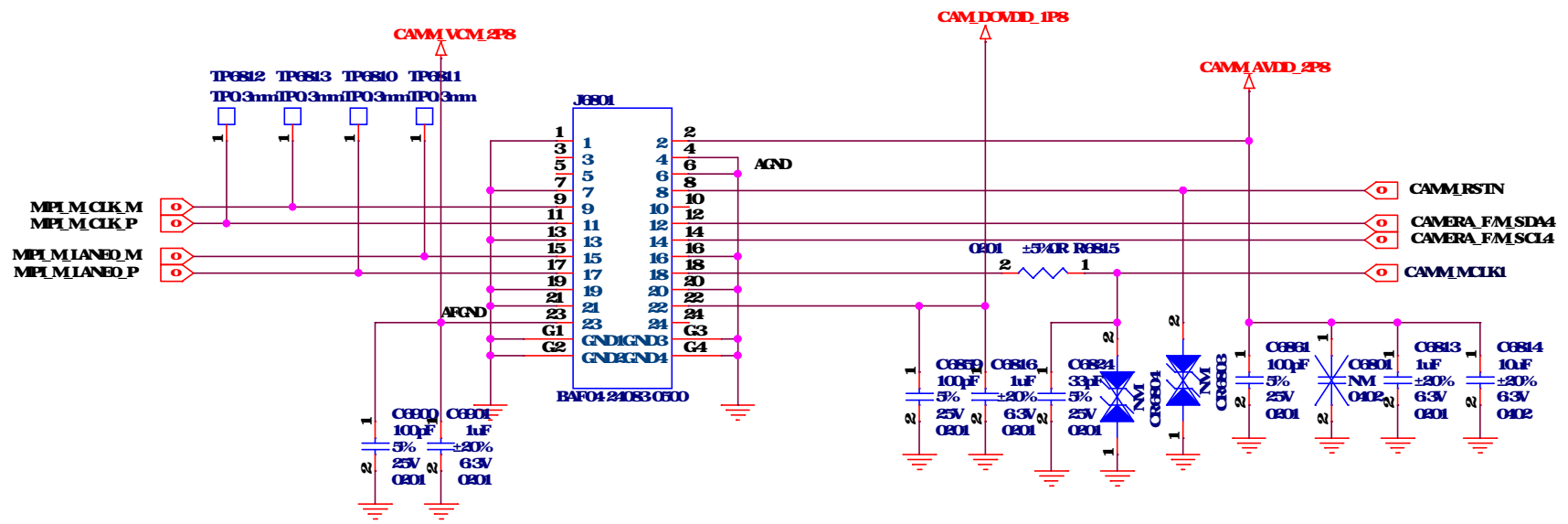
Title		
G7MMN		
Site	Document Number	Rev
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Title		
G7MMN		
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A3	WIDEFIELD CAMERA	PO
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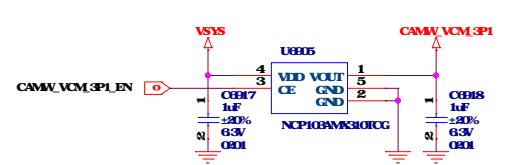
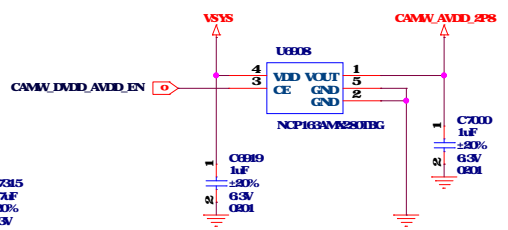
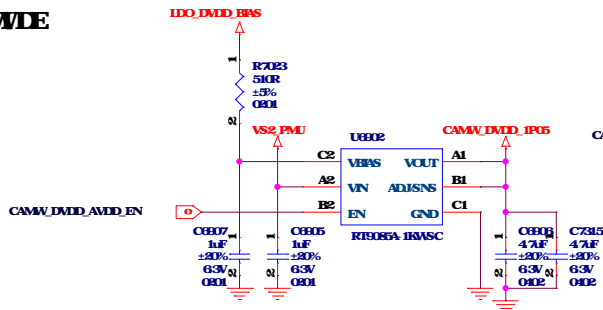
Title		
G7MAN		
Size	Document Number	Rev
A4	Reserved for Analog FM	FO
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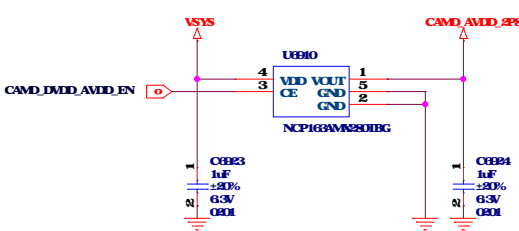
Title		
G7MAN		
Size	Document Number	Rev
A4	ULTRA CAMERA	FO
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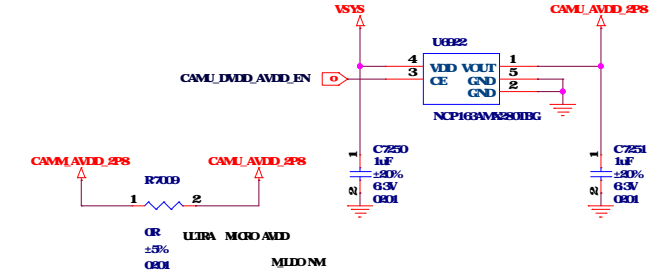
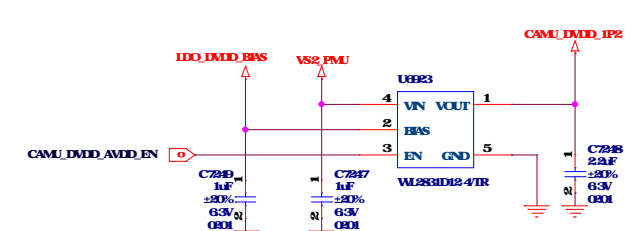
**WDE**



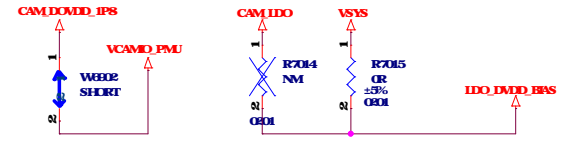
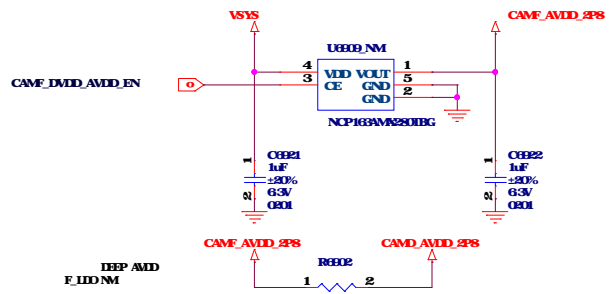
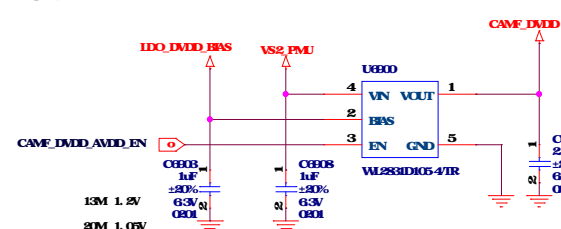
**DEEP**



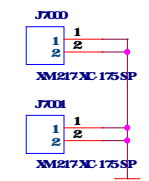
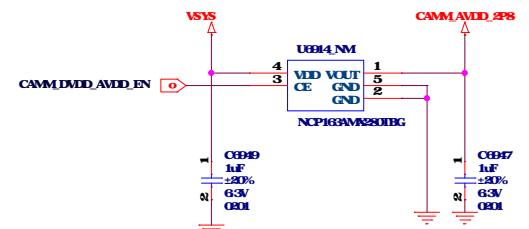
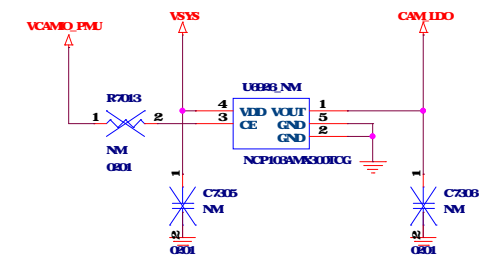
**ULTRA**



**FRONT**



**MICRO**



VIETMOBILE.VN

Title		G77MM
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A3	CAMERA LDOs	FO
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