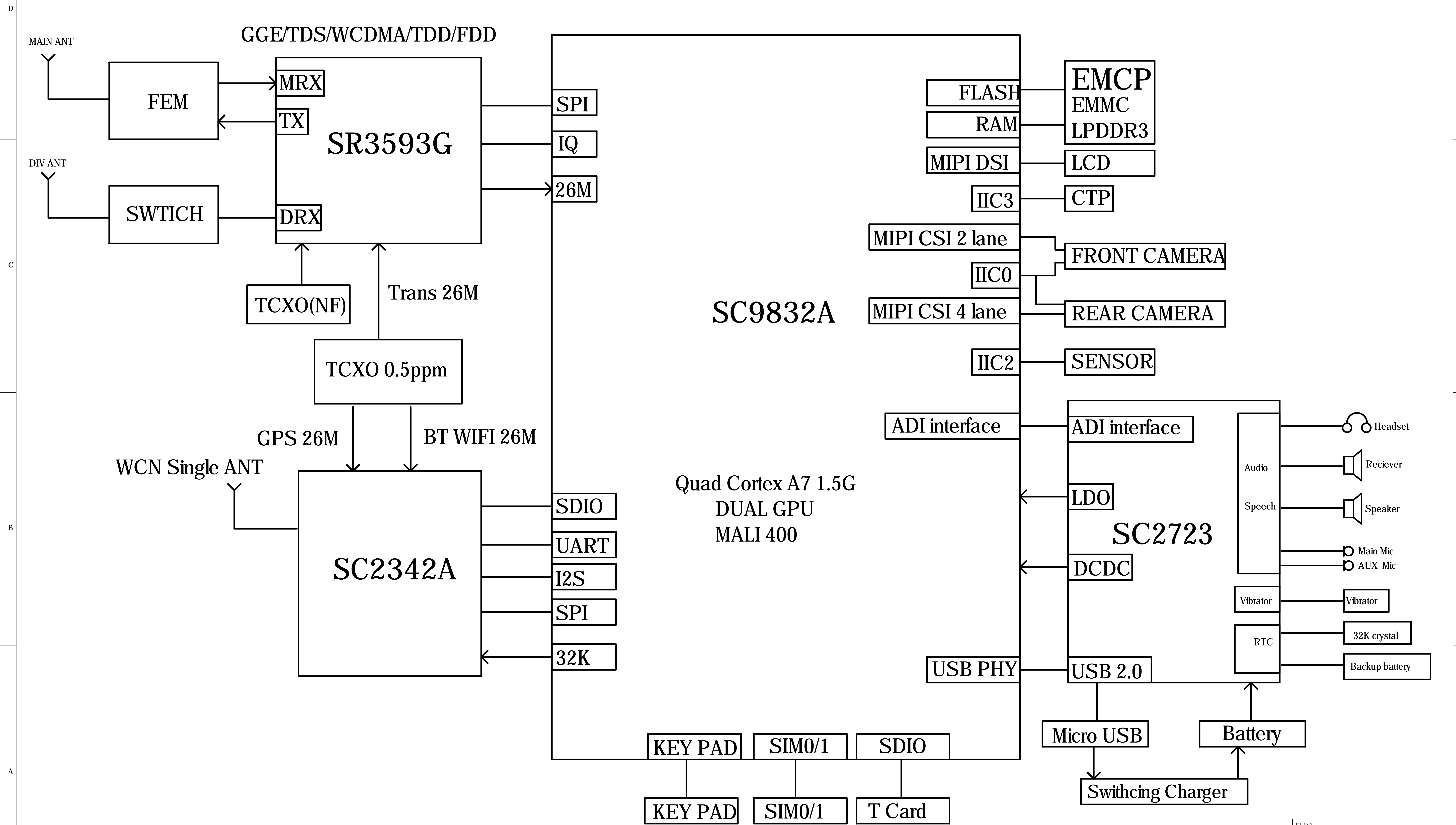


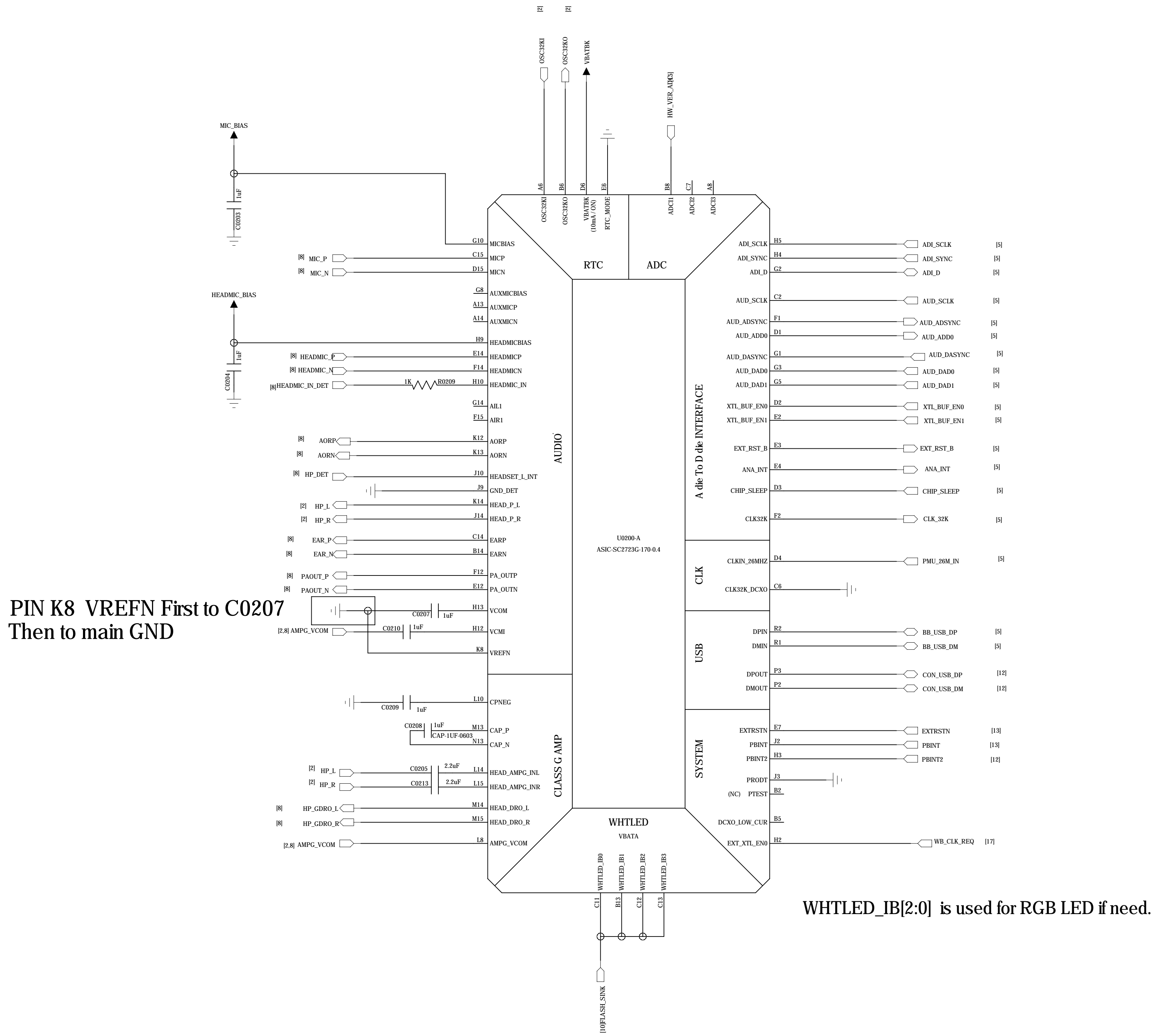
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LTR	ECO NO	APPROVED	DATE

BLOCK DIAGRAM

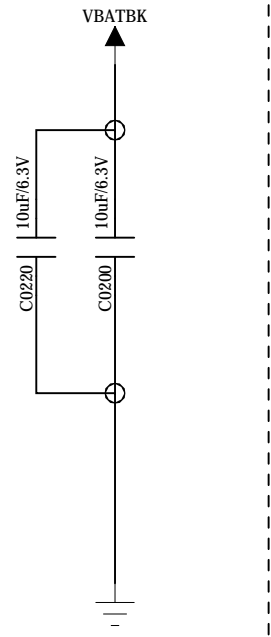


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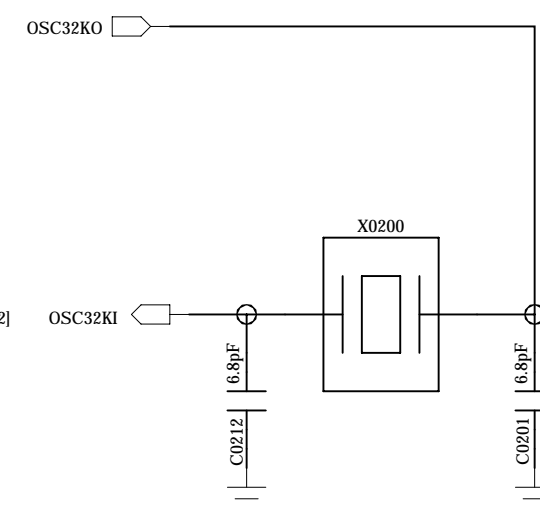
PMU_ANALOG



BKBT



RTC

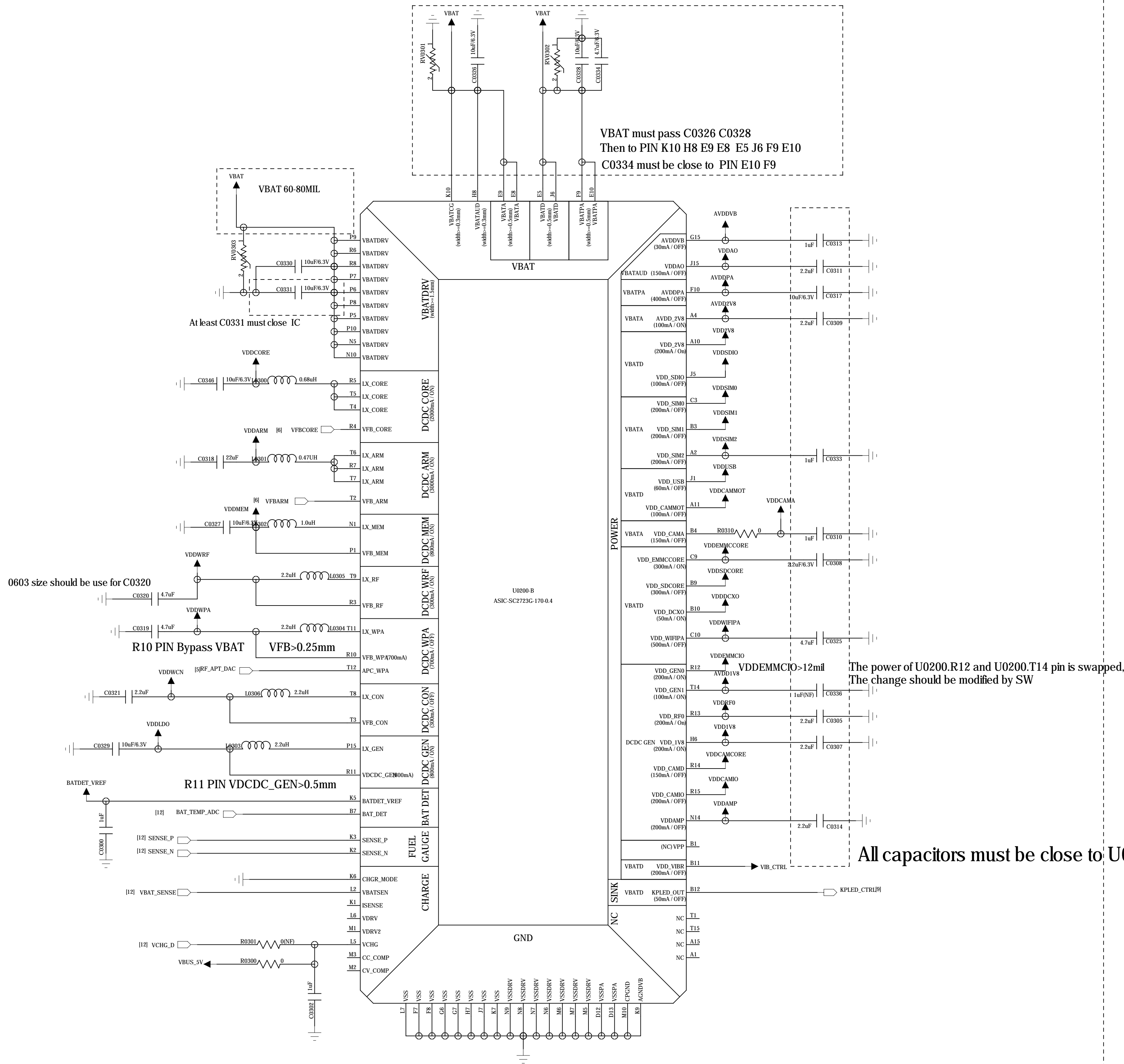


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DOCUMENT NO:		

DESIGNER:	DATE:	DEPARTMENT:
River Du	2015-10-12	Hardware DEPT.
EDA:	DATE:	COMPANY:
Mukler Han	2015-10-12	Spreadtrum communications, Inc.
APPROVED:	DATE:	CONFIDENTIAL AND PROPRIETARY
Gang Xu	2015-10-12	SHEET: 1 OF 21

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PMU_POWER

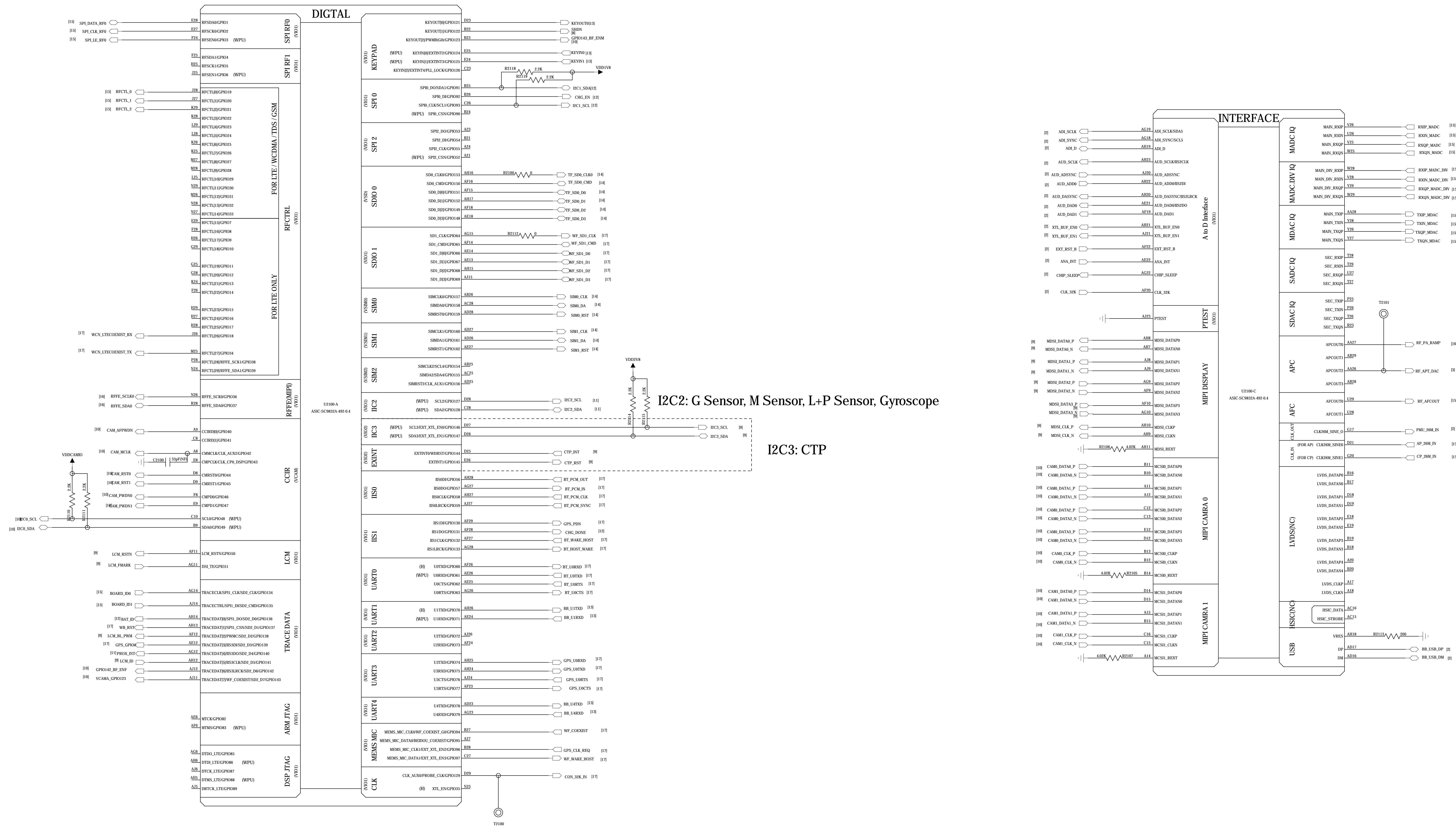


SC2723 LDO ouput

Power	Output Voltage (V)	Output Current	Default Voltage	Default
VDD1V8	1.2~1.8	200mA	1.8V	ON
AVDD28	1.8~3.3	100mA	2.8V	ON
VDD2V8	1.8~3.3	200mA	2.8V	ON
VDDSIM0	1.8~3.3	200mA		OFF
VDDSIM1	1.8~3.3	200mA		OFF
VDDSIM2	1.8~3.3	200mA		OFF
VDDRF0	1.2~1.8	200mA	1.8V	ON
VDDCAMA	1.8~3.3	150mA		OFF
VDDCMMOT	1.8~3.3	100mA		OFF
VDDEMMCCORE	1.8~3.3	300mA	3.0V	ON
VDDSDCORE	1.8~3.3	300mA		OFF
VDDSDIO	1.8~3.3	100mA		OFF
VBATBK	2.6/2.8/3.0/3.2	10mA	2.8V	ON
VDDVIB	1.8~3.3	200mA		OFF
KPLED_OUT	1.8~3.3	50mA		OFF
VDD_WIFIPA	1.8~3.3	500mA		OFF
VDD_DCXO	1.8~3.3	50mA	1.8V	ON
VDDCAMD	1.2~1.8	150mA		OFF
VDDUSB	1.8~3.3	60mA	3.3V	OFF
VDDAO	2.9/3.1/3.2/3.3	150mA		OFF
AVDDVB	2.9/3.1/3.2/3.3	30mA		OFF
AVDDPA	2.9/3.1/3.2/3.3	400mA		OFF
VDDCAMIO	1.2~1.8	200mA		OFF
AVDDAMP	1.2~1.8	200mA		OFF
VDDGEN0	1.2~1.8	200mA	1.8V	ON
VDDGEN1	1.2~1.8	100mA	1.8V	ON
DCDCARM	0.6~3.7	3000mA	0.9V	ON
DCDCCORE	0.6~3.7	2000mA	0.9V	ON
DCDCMEM	0.6~3.7	800mA	1.2V	ON
DCDC_GEN	0.6~3.7	800mA	2.2V	ON
DCDCRF	0.6~3.7	300mA	1.5V	ON
DCDCWPA	0.6~3.7	700mA		OFF
DCDCCON	0.6~3.7	300mA		OFF

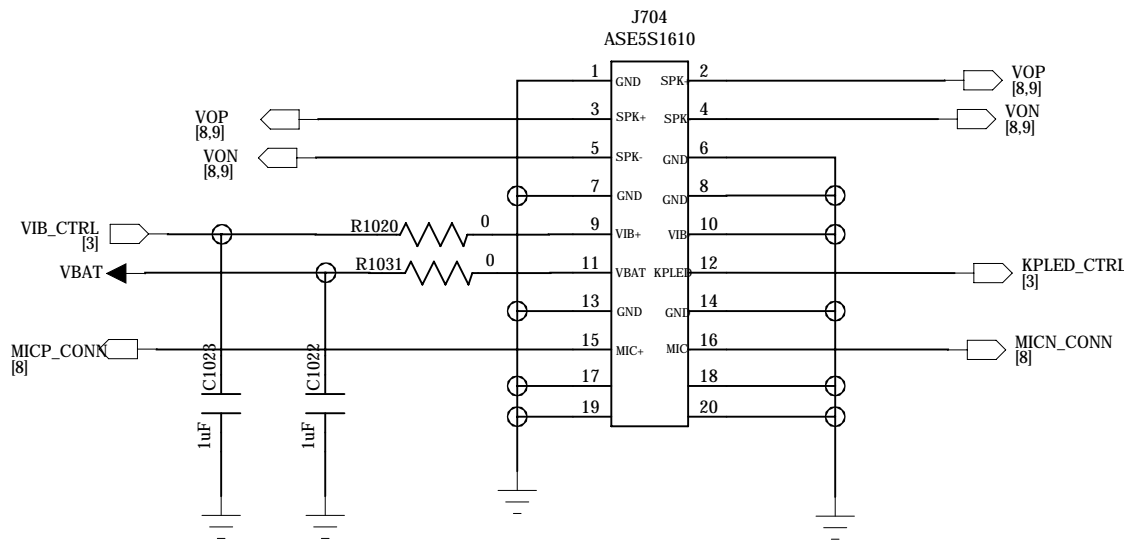
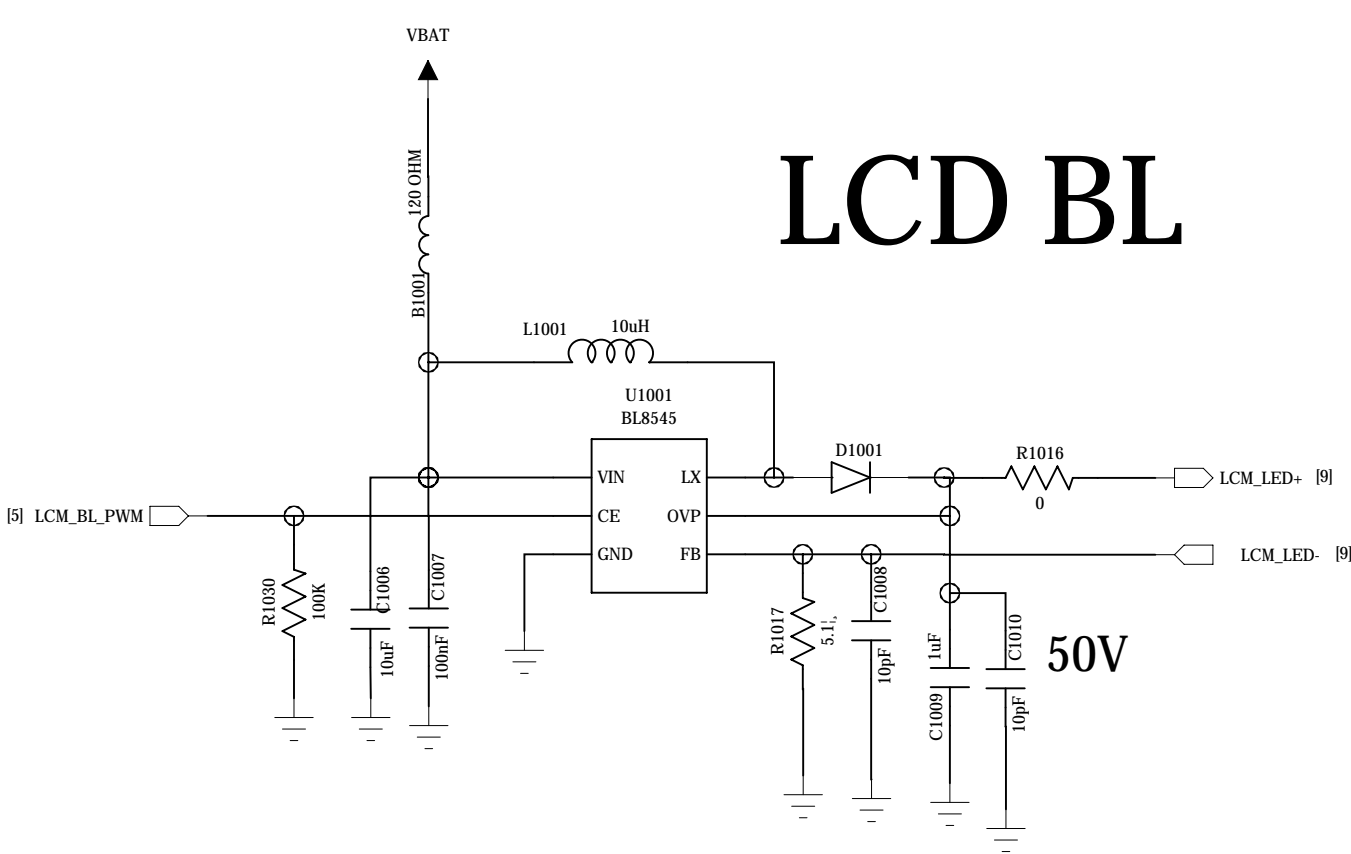
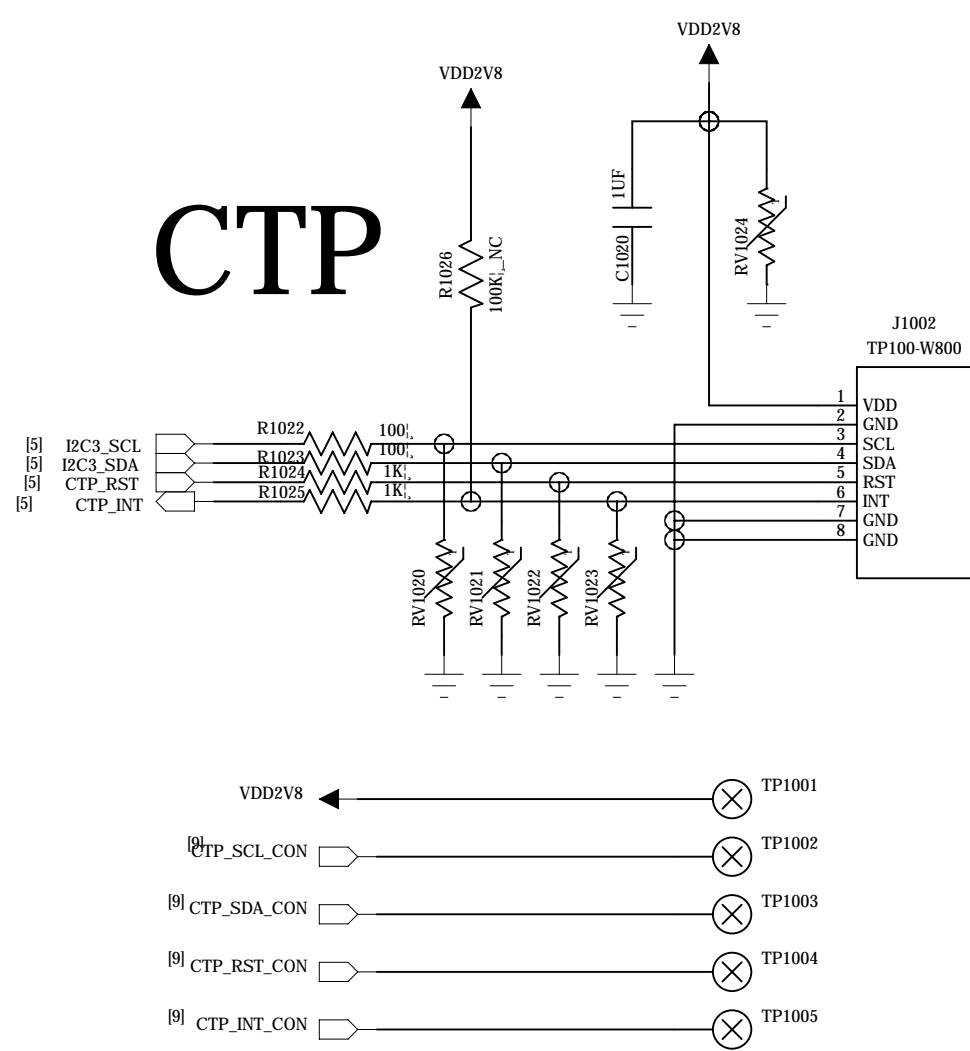
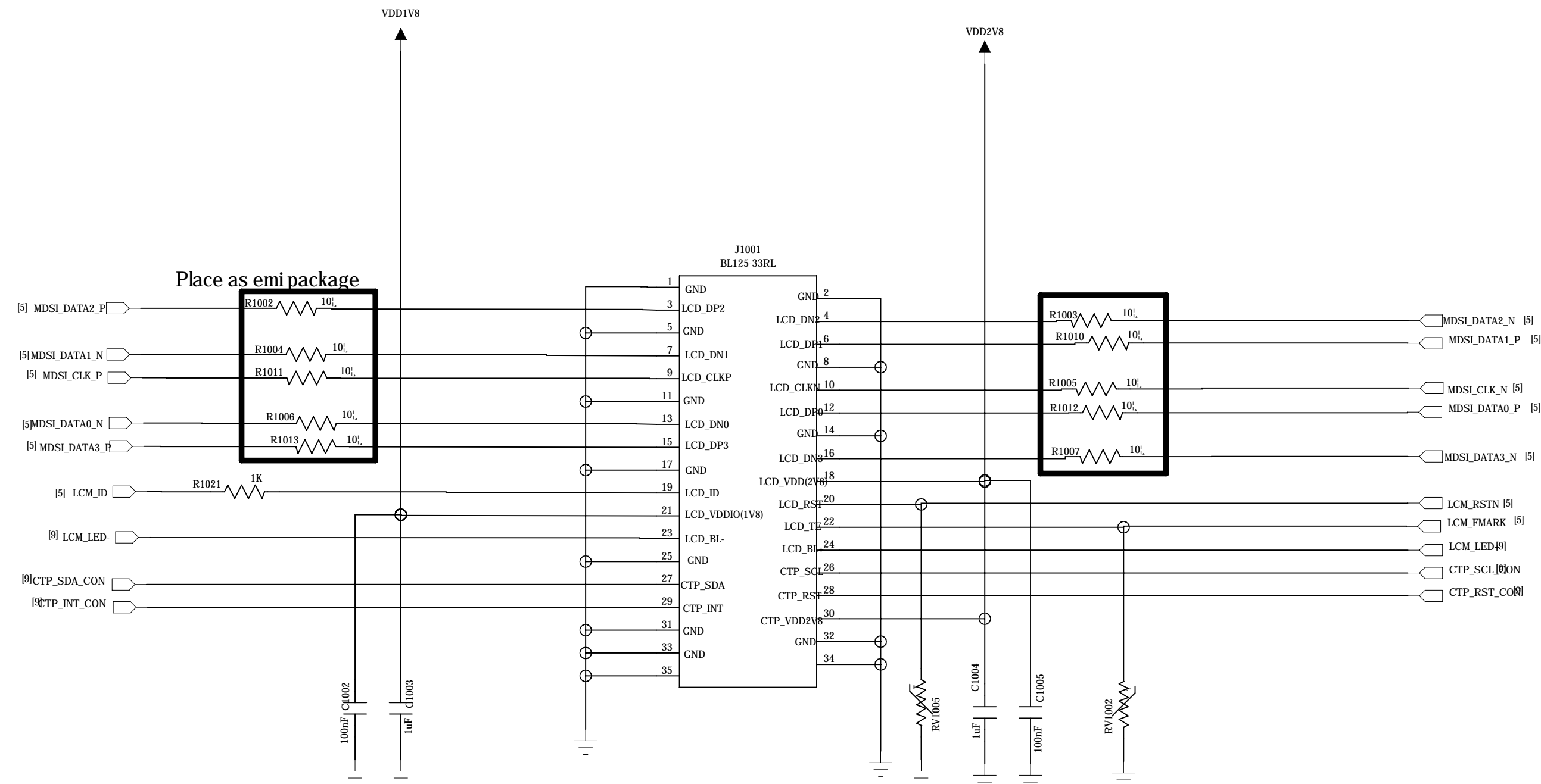
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LTR	ECO NO.	APPROVED:	DATE:

DIGITAL



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LT#	REV NO.	APPROVED	DATE

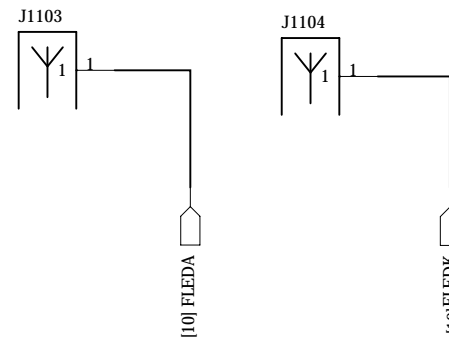
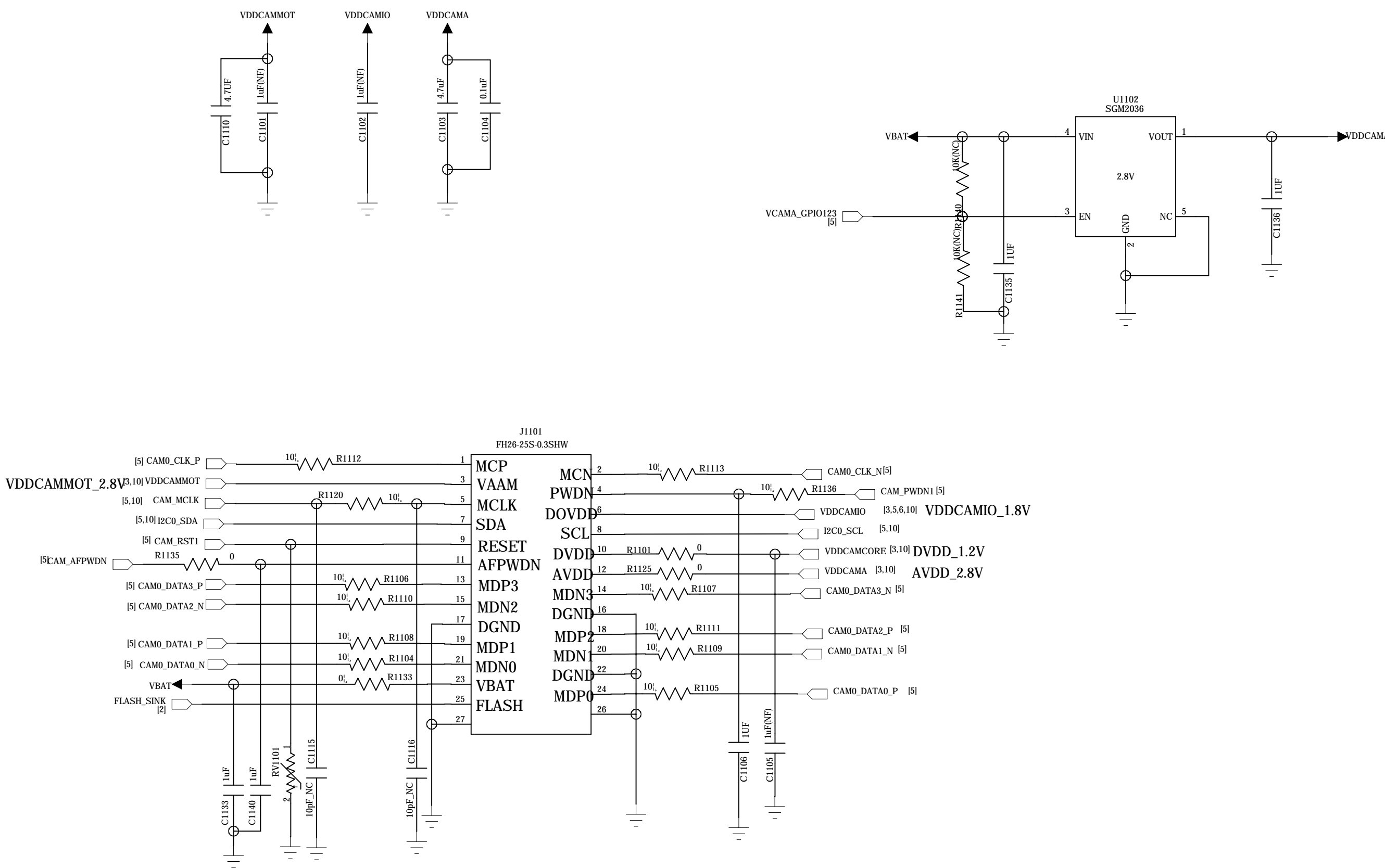
5.5" HVGA LCM & CTP



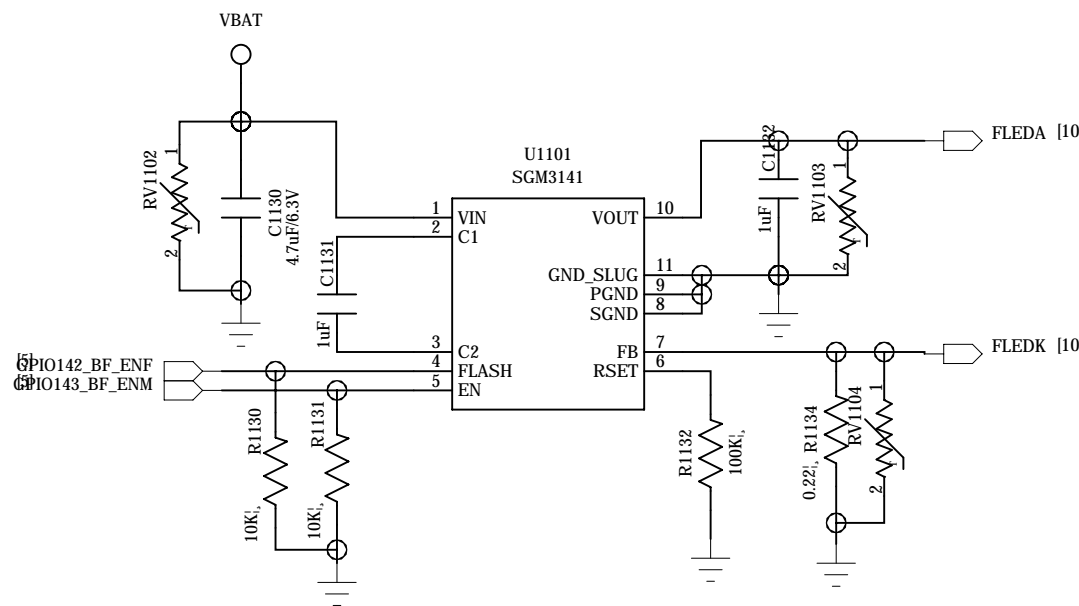
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SP9832A-2_CS_SCH_V0.1.0			
DOCUMENT NO.			
DESIGNER:	DATE:	DEPARTMENT:	
River.Du	2015-10-12	Hardware DEPT.	
EDA:	DATE:	COMPANY:	
Mulder.Han	2015-10-12	Spreadtrum communications, Inc.	
APPROVED:	DATE:	CONFIDENTIAL AND PROPRIETARY	
Gang.Xu	2015-10-12	SHEET: 14 OF 23	

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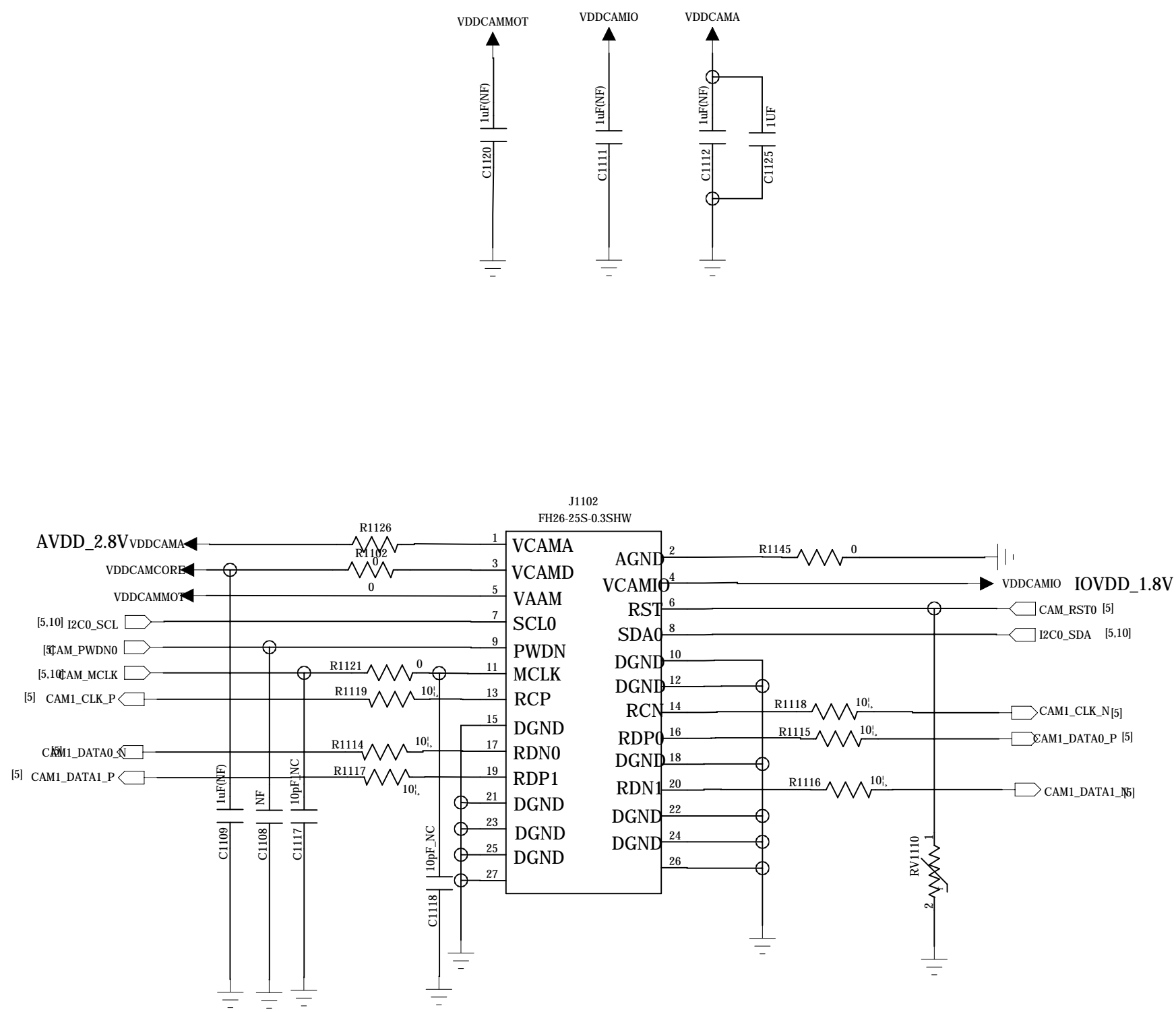
8M Sensor



REAR FLASH



5M Sensor



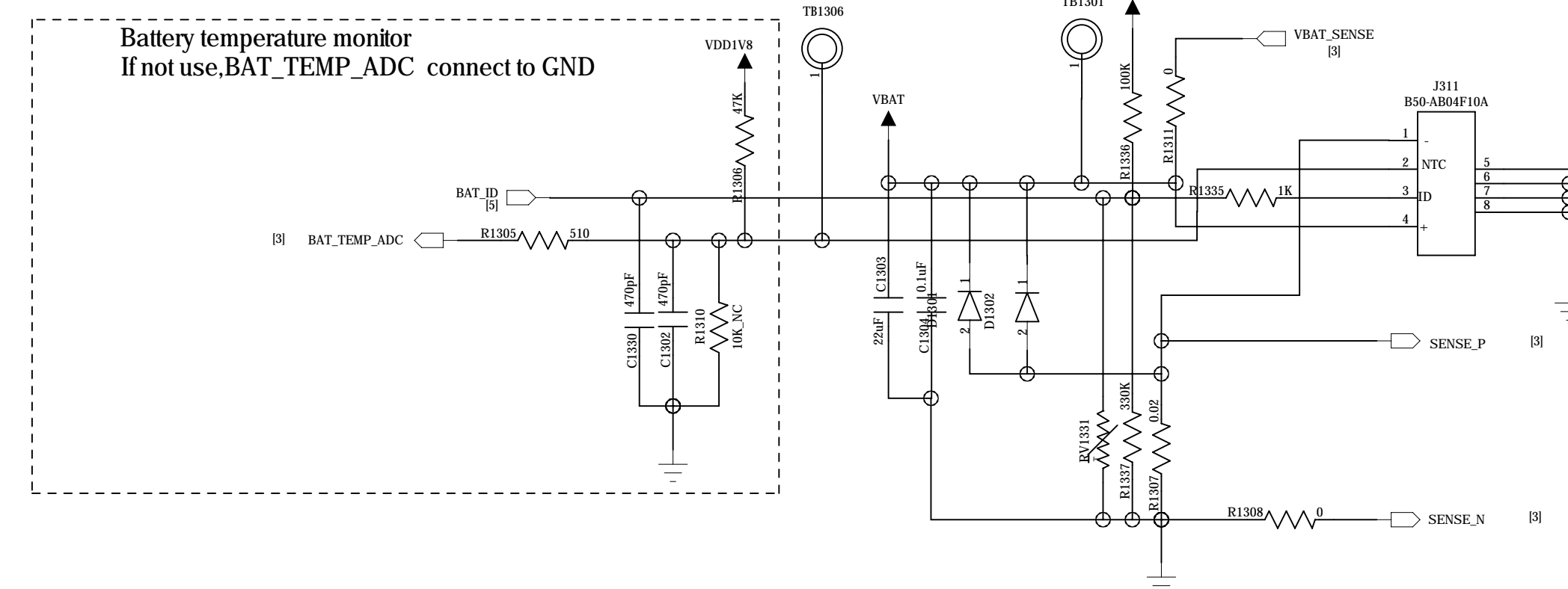
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DOCUMENT NO:

DESIGNER: Rher.Du	DATE: 2015-10-12	DEPARTMENT: Hardware DEPT.
EQA: Mukher.Han	DATE: 2015-10-12	COMPANY: Spreadtrum communications, Inc.
APPROVED: Gang.Xu	DATE: 2015-10-12	CONFIDENTIAL AND PROPRIETARY

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LTR	ECO NO:	APPROVED:	DATE:

BATTERY



5pin Micro USB

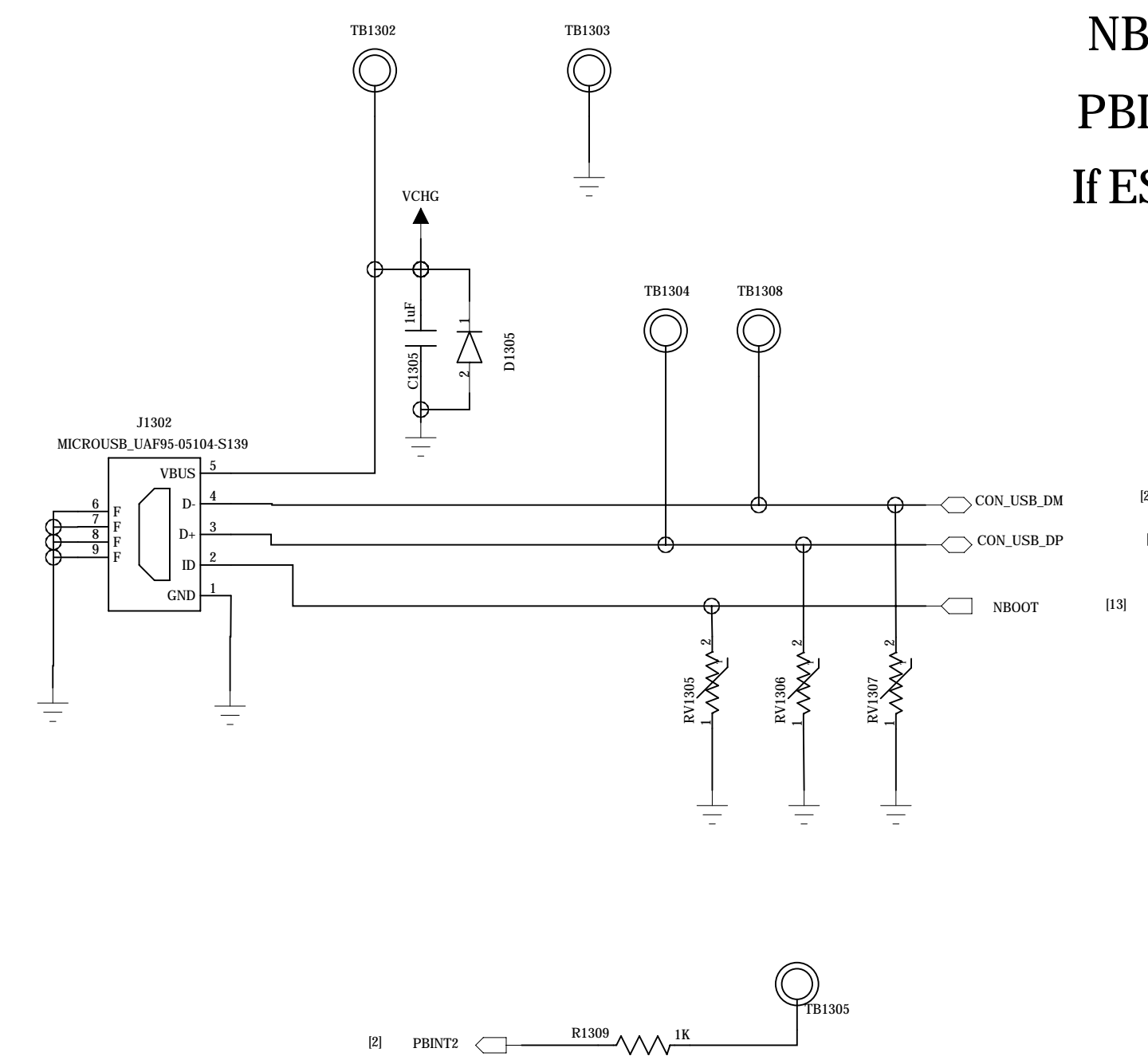
USB BOOT CONTROL

NBOOT = Float----->NAND BOOT

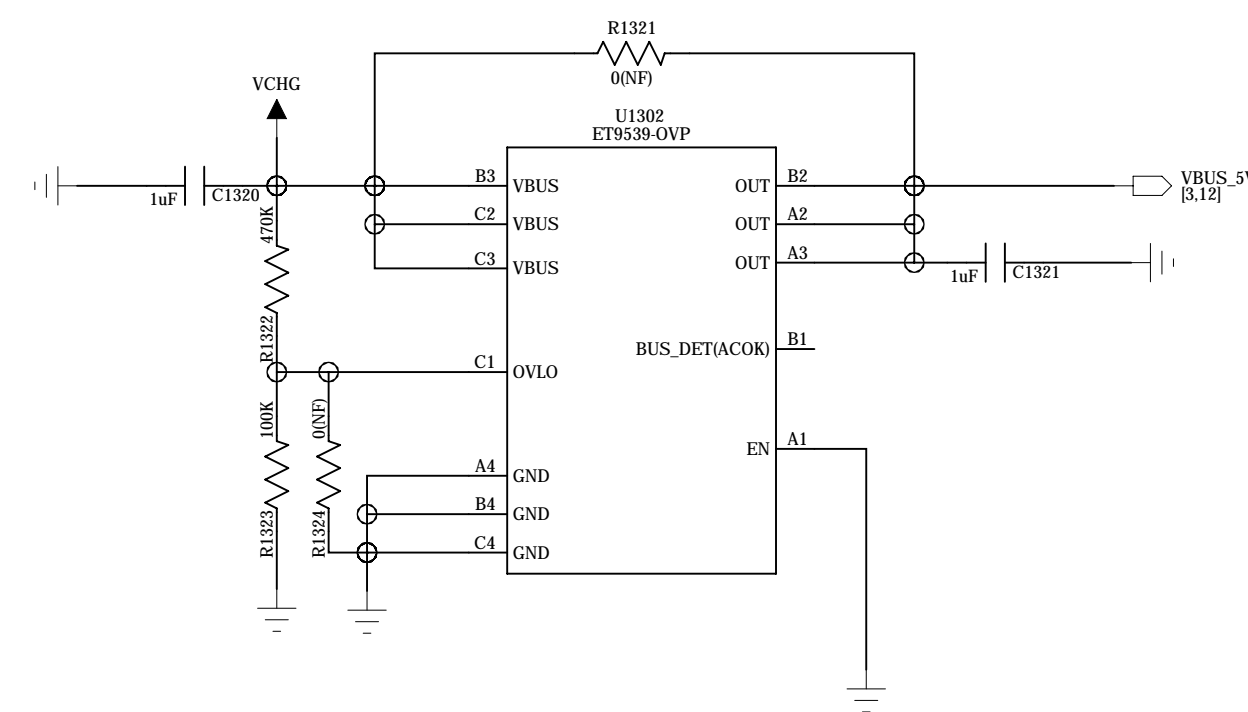
NBOOT = 0 ----->USB BOOT

PBINT2 is only for auto power on.

If ESD test FAILURE, Put on the MLVs and TVSSs

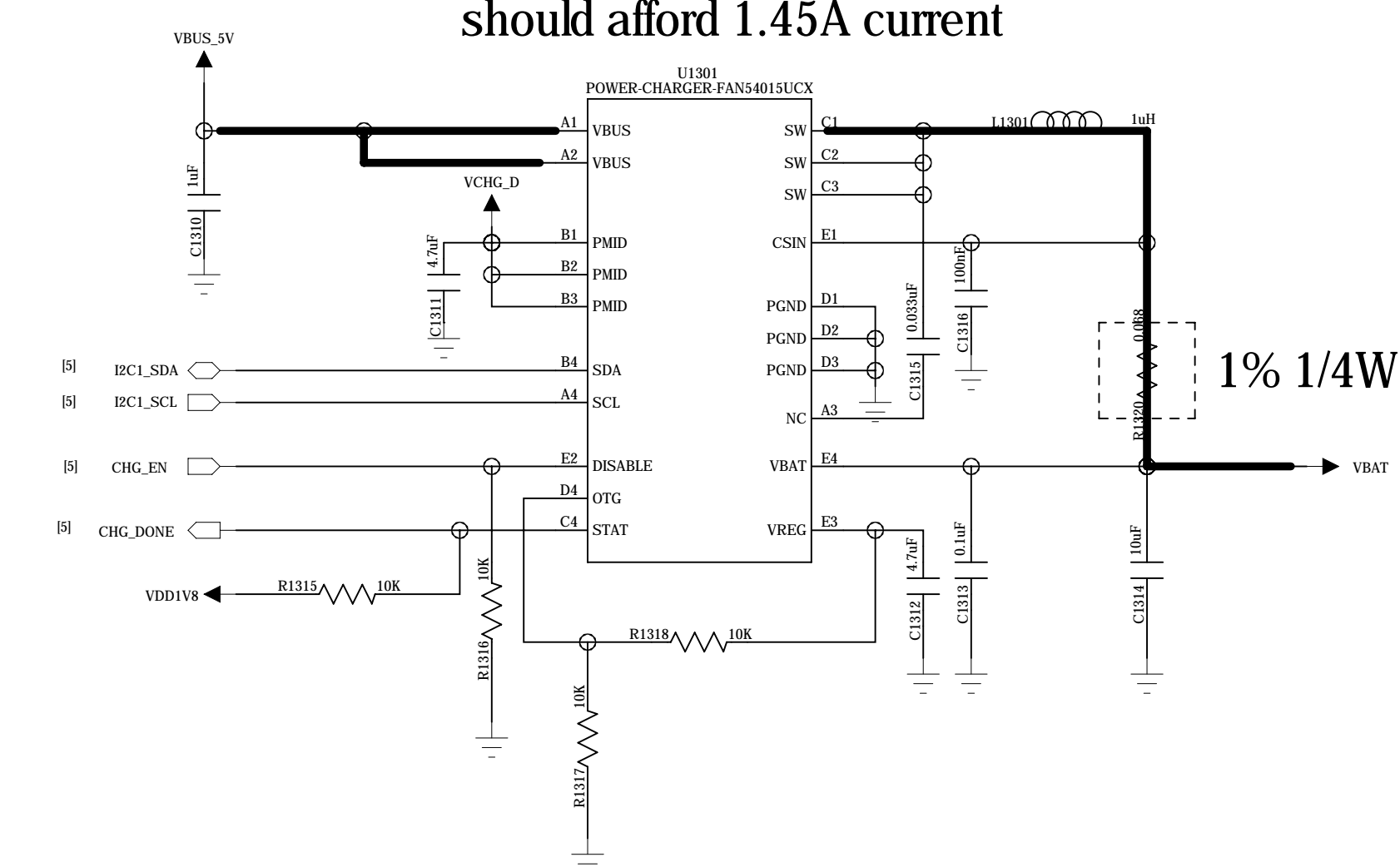


OVP



USB CHARGE/500mA OTG

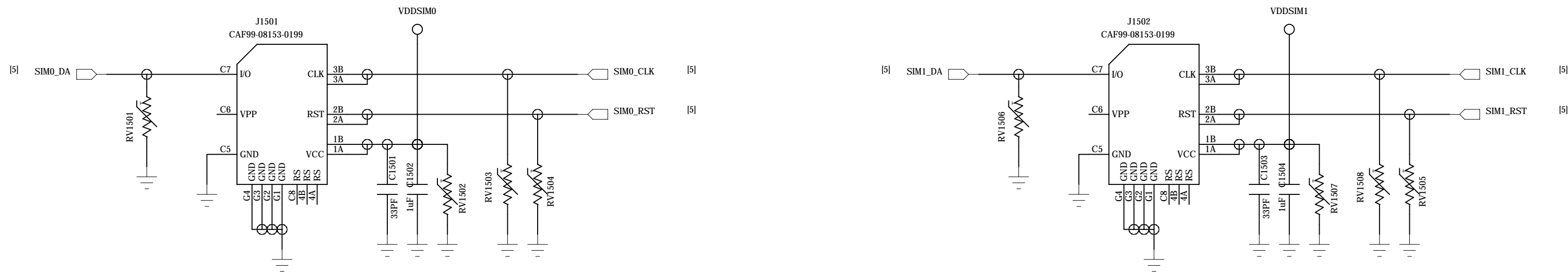
should afford 1.45A current



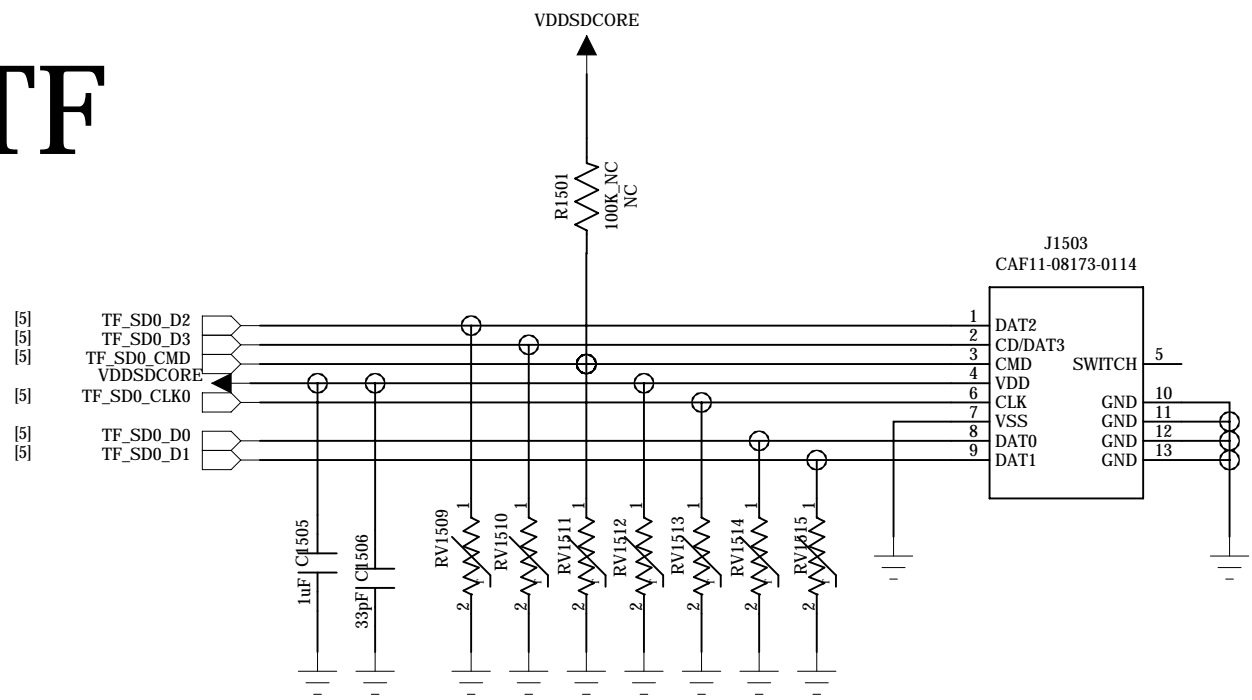
		TITLE & REV.	
		SP9832A-2_CS_SCH_V01.0	
		DOCUMENT NO.	
DESIGNER:	DATE:	DEPARTMENT:	
RiverDe	2015-10-12	Hardware DEPT.	
EQN:	DATE:	COMPANY:	
Mukder.Han	2015-10-12	Spectrum communications, Inc.	
APPROVED:	DATE:	CONFIDENTIAL AND PROPRIETARY	
Gang.Xu	2015-10-12		SHEET 17 OF 23

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LTR	ECO NO	APPROVED	DATE

SIM0 & SIM1

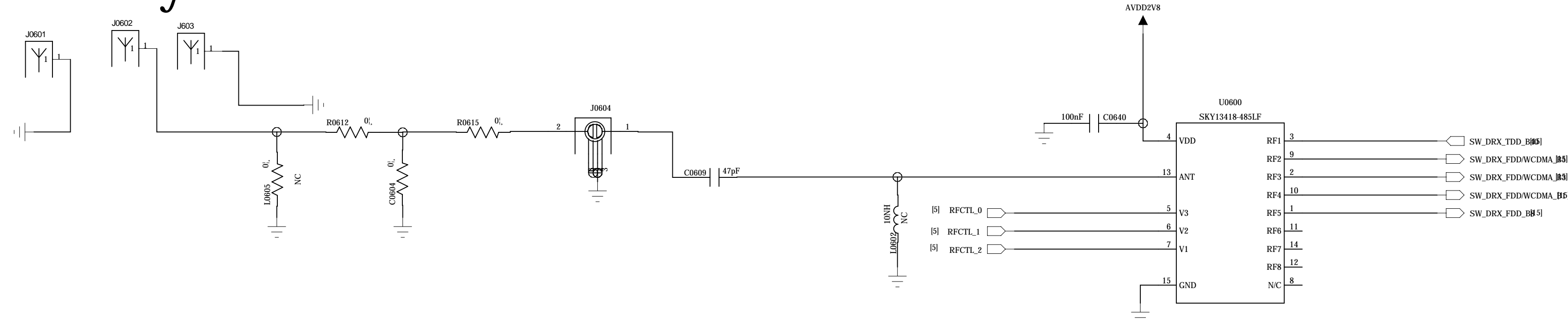


TF



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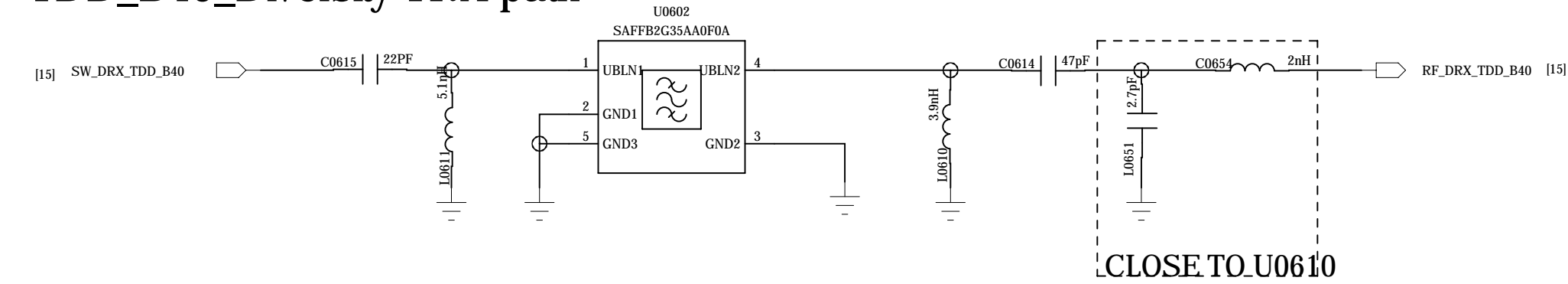
Diversity LTE Path



DIV ANT SAME WITH LA

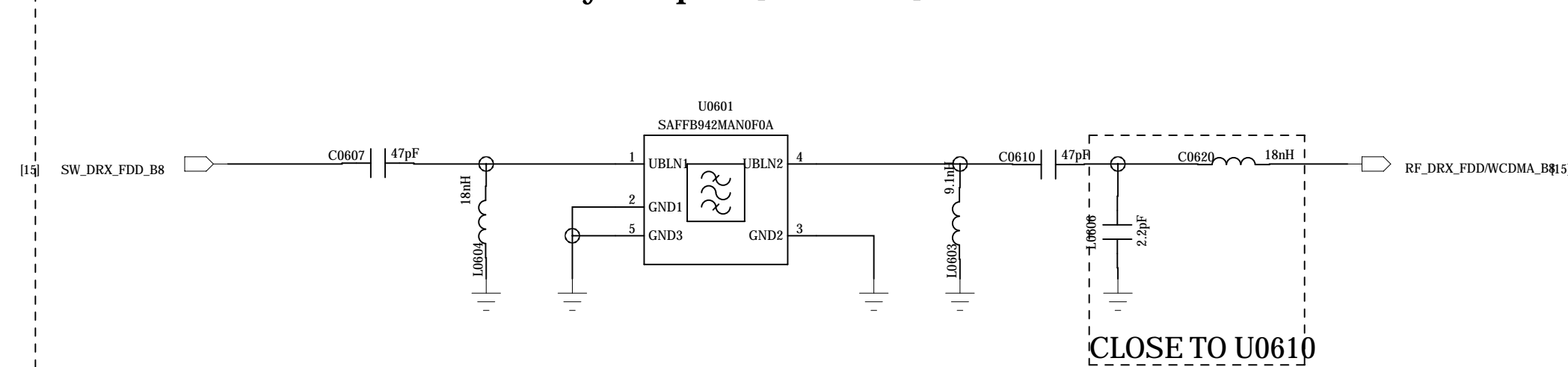
Diversity TDDLTE RX Path

TDD_B40_Diversity TRX path

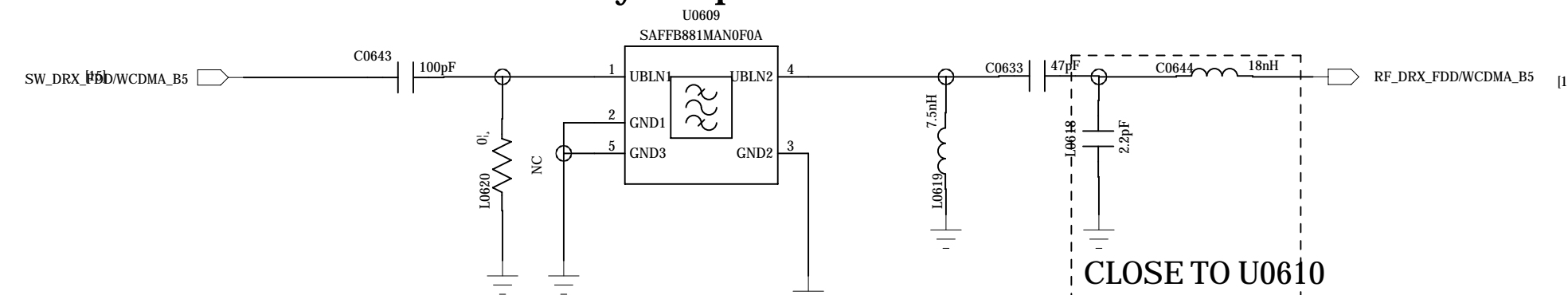


Diversity FDDLTE/WCDMA RX Path

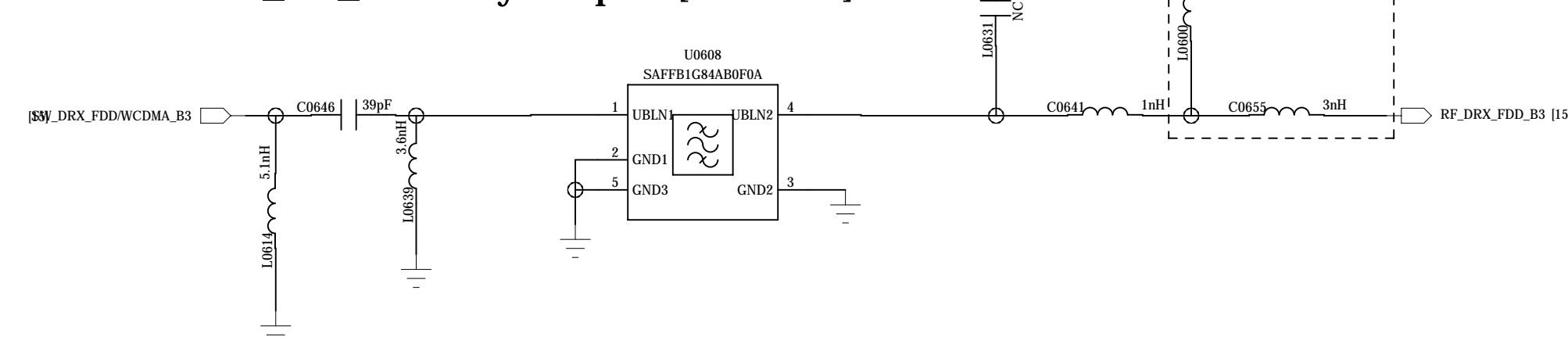
FDD/WCDMA_B8_Diversity Rx path[MATCH]



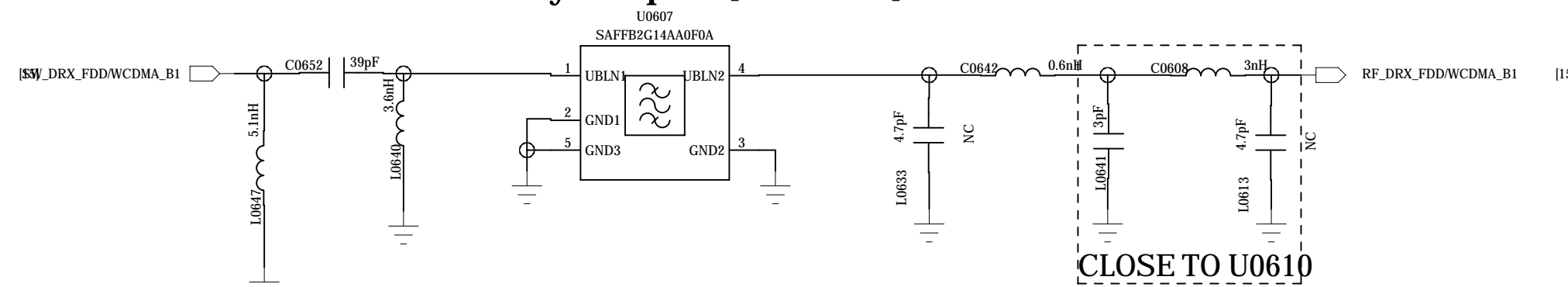
FDD/WCDMA_B5_Diversity Rx path



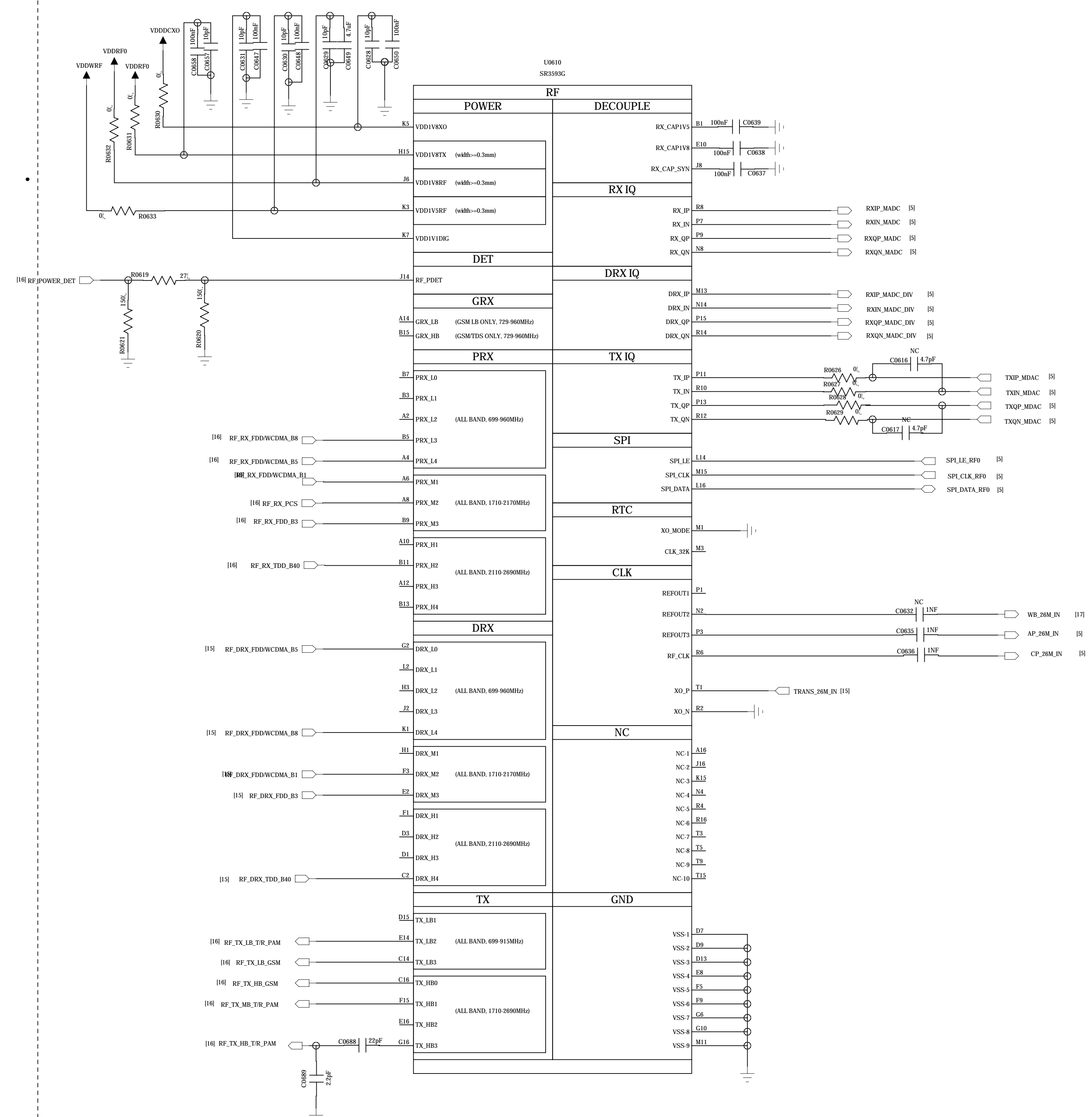
FDD/WCDMA_B3_Diversity Rx path[MATCH]



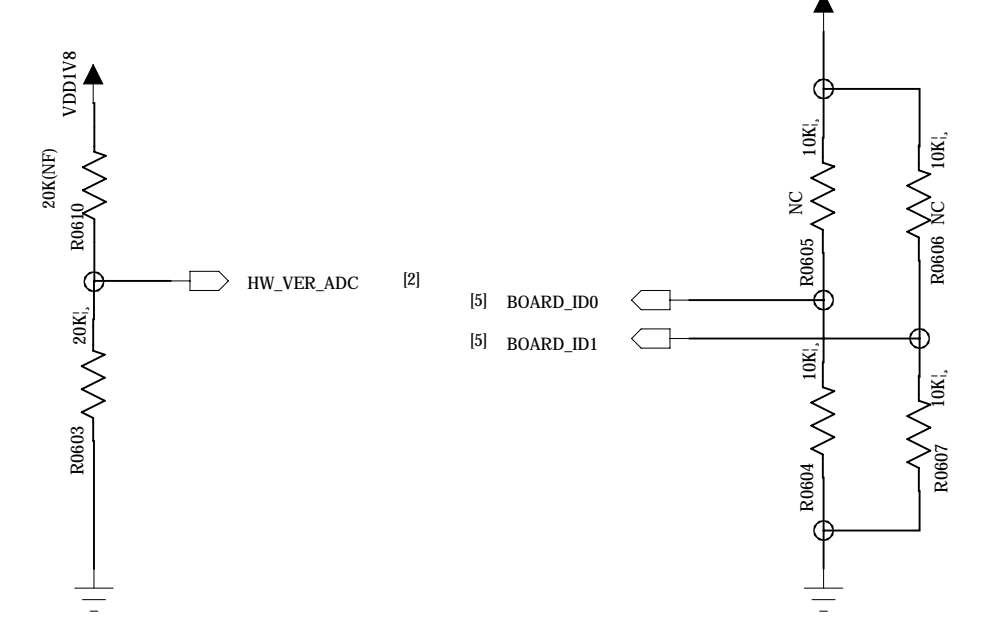
FDD/WCDMA_B1_Diversity Rx path[MATCH]



TRANSCIVER



Band info



ADC/GPIO	00	01	10	11
0	reserved	reserved	reserved	reserved
1	reserved	reserved	reserved	reserved
2	reserved	reserved	reserved	reserved
3	reserved	reserved	reserved	reserved

		TITLE & REV:	
		SP9832A-2_CS_SCH_V0.1.0	
		DOCUMENT NO:	
DESIGNED: River Du	DATE: 2015-10-12	DEPARTMENT: Hardware DEPT.	
DRAWN: Mukdes.Han	DATE: 2015-10-12	COMPANY: Spectrum communications, Inc.	
APPROVED: Gang Xu	DATE: 2015-10-12	CONFIDENTIAL AND PROPRIETARY	
		SHEET: 9 OF 23	

